

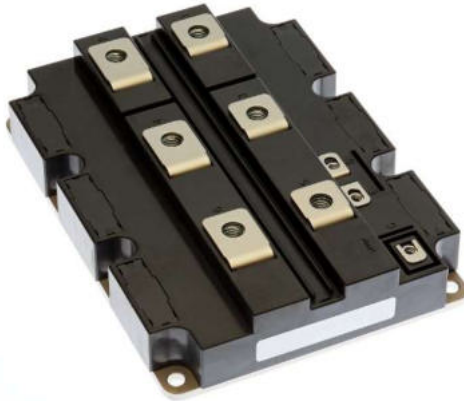
< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

# CM2400HCB-34X

HIGH POWER SWITCHING USE  
INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## CM2400HCB-34X



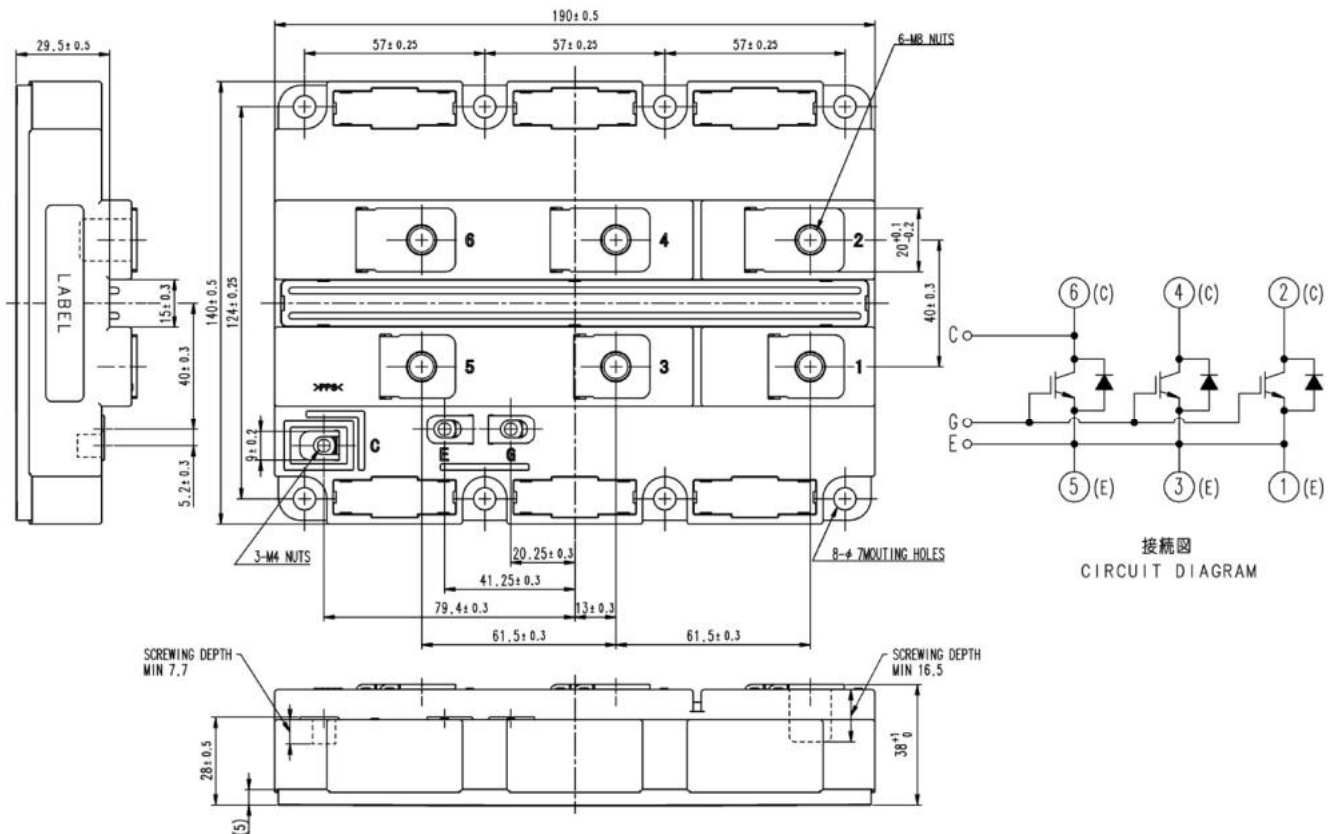
- $I_C$ .....2400A
- $V_{CES}$ .....1700V
- 1-element in a Pack
- Insulated Type
- CSTBT™(III)
- RFC Diode
- AISiC Baseplate
- UL recognized under UL1557

## APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers

## OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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**MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit
$V_{CES}$	Collector-emitter voltage	$V_{GE} = 0V, T_J = -40...+150^{\circ}C$	1700	V
		$V_{GE} = 0V, T_J = -50^{\circ}C$	1650	V
$V_{GES}$	Gate-emitter voltage	$V_{CE} = 0V, T_J = 25^{\circ}C$	$\pm 20$	V
$I_C$	Collector current	DC, $T_c = 95^{\circ}C$	2400	A
$I_{CRM}$		Pulse (Note 1)	4800	A
$I_E$	Emitter current (Note 2)	DC, $T_c = 75^{\circ}C$	2400	A
$I_{ERM}$		Pulse (Note 1)	4800	A
$P_{tot}$	Maximum power dissipation (Note 3)	$T_c = 25^{\circ}C$ , IGBT part	13800	W
$V_{iso}$	Isolation voltage	RMS, sinusoidal, $f = 60Hz$ , $t = 1min$	6000	V
$V_e$	Partial discharge extinction voltage	RMS, sinusoidal, $f = 60Hz$ , $Q_{PD} \leq 10pC$	2600	V
$T_J$	Junction temperature	—	$-50 \sim +150$	$^{\circ}C$
$T_{Jop}$	Operating junction temperature	—	$-50 \sim +150$	$^{\circ}C$
$T_{stg}$	Storage temperature	—	$-55 \sim +150$	$^{\circ}C$
$t_{psc}$	Short circuit pulse width	$V_{CC} \leq 1200V$ , $V_{CE} \leq V_{CES}$ , $V_{GE} = 15V$ , $T_J = 150^{\circ}C$	6.5	$\mu s$

**ELECTRICAL CHARACTERISTICS**

Symbol	Item	Conditions		Limits			Unit
				Min	Typ	Max	
I <sub>CES</sub>	Collector cutoff current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0V	T <sub>J</sub> = 25°C	—	—	4.0	mA
			T <sub>J</sub> = 125°C	—	3.5	—	
			T <sub>J</sub> = 150°C	—	—	40.0	
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	V <sub>CE</sub> = 10V, I <sub>C</sub> = 240mA, T <sub>J</sub> = 25°C		5.5	6.0	6.5	V
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0V, T <sub>J</sub> = 25°C		-0.5	—	0.5	μA
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 100kHz T <sub>J</sub> = 25°C		—	817	—	nF
C <sub>oes</sub>	Output capacitance			—	17.8	—	
C <sub>res</sub>	Reverse transfer capacitance			—	7.2	—	
Q <sub>G</sub>	Total gate charge	V <sub>CC</sub> = 900V, I <sub>C</sub> = 2400A, V <sub>GE</sub> = ±15V		—	51.0	—	μC
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 2400A (Note 4) V <sub>GE</sub> = 15V	T <sub>J</sub> = 25°C	—	1.60	—	V
			T <sub>J</sub> = 125°C	—	1.85	—	
			T <sub>J</sub> = 150°C	—	1.95	2.45	
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> = 900V I <sub>C</sub> = 2400A V <sub>GE</sub> = ±15V R <sub>G(on)</sub> = 0.62Ω L <sub>S</sub> = 75nH Inductive load	T <sub>J</sub> = 150°C	—	—	1.50	μs
t <sub>r</sub>	Rise time		T <sub>J</sub> = 150°C	—	—	0.50	μs
E <sub>on(10%)</sub>	Turn-on switching energy (per pulse) (Note 7)		T <sub>J</sub> = 25°C	—	0.40	—	J
			T <sub>J</sub> = 125°C	—	0.70	—	
			T <sub>J</sub> = 150°C	—	0.75	—	
E <sub>on</sub>	Turn-on switching energy (per pulse) (Note 5)		T <sub>J</sub> = 25°C	—	0.50	—	J
			T <sub>J</sub> = 125°C	—	0.75	—	
			T <sub>J</sub> = 150°C	—	0.80	—	
t <sub>d(off)</sub>	Turn-off delay time	T <sub>J</sub> = 25°C	—	6.00	—	μs	
		T <sub>J</sub> = 125°C	—	6.20	—		
		T <sub>J</sub> = 150°C	—	6.35	10.0		
t <sub>f</sub>	Fall time	V <sub>CC</sub> = 900V I <sub>C</sub> = 2400A V <sub>GE</sub> = ±15V R <sub>G(off)</sub> = 5.6Ω L <sub>S</sub> = 75nH Inductive load	T <sub>J</sub> = 25°C	—	0.30	—	μs
		T <sub>J</sub> = 125°C	—	0.32	—		
		T <sub>J</sub> = 150°C	—	0.34	1.00		
E <sub>off(10%)</sub>	Turn-off switching energy (per pulse) (Note 7)	T <sub>J</sub> = 25°C	—	0.95	—	J	
		T <sub>J</sub> = 125°C	—	1.10	—		
		T <sub>J</sub> = 150°C	—	1.20	—		
E <sub>off</sub>	Turn-off switching energy (per pulse) (Note 5)	T <sub>J</sub> = 25°C	—	1.00	—	J	
		T <sub>J</sub> = 125°C	—	1.15	—		
		T <sub>J</sub> = 150°C	—	1.25	—		

## &lt; High Voltage Insulated Gate Bipolar Transistor : HVIGBT &gt;

**CM2400HCB-34X**

HIGH POWER SWITCHING USE

INSULATED TYPE

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**ELECTRICAL CHARACTERISTICS**

Symbol	Item	Conditions		Limits			Unit
				Min	Typ	Max	
V <sub>EC</sub>	Emitter-collector voltage (Note 2)	I <sub>E</sub> = 2400A (Note 4) V <sub>GE</sub> = 0V	T <sub>J</sub> = 25°C	—	1.80	—	V
			T <sub>J</sub> = 125°C	—	1.95	—	
			T <sub>J</sub> = 150°C	—	1.95	2.45	
t <sub>rr</sub>	Reverse recovery time (Note 2)	V <sub>CC</sub> = 900V I <sub>E</sub> = 2400A V <sub>GE</sub> = ±15V R <sub>G(on)</sub> = 0.62Ω L <sub>S</sub> = 75nH Inductive load	T <sub>J</sub> = 25°C	—	0.40	—	μs
			T <sub>J</sub> = 125°C	—	0.55	—	
			T <sub>J</sub> = 150°C	—	0.60	—	
I <sub>rr</sub>	Reverse recovery current (Note 2)		T <sub>J</sub> = 25°C	—	1790	—	A
			T <sub>J</sub> = 125°C	—	1930	—	
			T <sub>J</sub> = 150°C	—	1980	—	
Q <sub>rr(10%)</sub>	Reverse recovery charge (Note 2,6)		T <sub>J</sub> = 25°C	—	430	—	μC
			T <sub>J</sub> = 125°C	—	720	—	
			T <sub>J</sub> = 150°C	—	820	—	
Q <sub>rr</sub>	Reverse recovery charge (Note 2,5)		T <sub>J</sub> = 25°C	—	480	—	μC
			T <sub>J</sub> = 125°C	—	785	—	
			T <sub>J</sub> = 150°C	—	890	—	
E <sub>rec(10%)</sub>	Reverse recovery energy (per pulse) (Note 2,7)		T <sub>J</sub> = 25°C	—	0.22	—	J
			T <sub>J</sub> = 125°C	—	0.40	—	
			T <sub>J</sub> = 150°C	—	0.46	—	
E <sub>rec</sub>	Reverse recovery energy (per pulse) (Note 2,5)		T <sub>J</sub> = 25°C	—	0.25	—	J
			T <sub>J</sub> = 125°C	—	0.45	—	
			T <sub>J</sub> = 150°C	—	0.55	—	

**THERMAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$R_{th(j-c)Q}$	Thermal resistance	Junction to Case, IGBT part	—	—	9.0	K/kW
$R_{th(j-c)D}$		Junction to Case, FWDi part	—	—	12.5	K/kW
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink $\lambda_{grease} = 1W/m^2K$ , $D_{(c-s)} = 80\mu m$	—	5.7	—	K/kW

**MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$M_t$	Mounting torque	M8 : Main terminals screw	7.0	—	19.0	N·m
$M_s$		M6 : Mounting screw	3.0	—	6.0	N·m
$M_t$		M4 : Auxiliary terminals screw (Note 8)	1.0	—	3.0	N·m
m	Mass		—	1.2	—	kg
CTI	Comparative tracking index		600	—	—	—
$d_a$	Clearance		19.5	—	—	mm
$d_s$	Creepage distance		32.0	—	—	mm
$L_{P_{CE}}$	Parasitic stray inductance		—	8.0	—	nH
$R_{CC'+EE'}$	Internal lead resistance	$T_C = 25^\circ C$	—	0.09	—	mΩ

Note1. Pulse width and repetition rate should be such that junction temperature ( $T_J$ ) does not exceed  $T_{Jopmax}$  rating.

Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWDi).

Note3. Junction temperature ( $T_J$ ) should not exceed  $T_{Jmax}$  rating ( $150^\circ C$ ).

Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

Note5. Definition of all items is according to IEC 60747, unless otherwise specified.

Note6. The integration range of reverse recovery charge is from  $I_E = 0A$  to  $10\%I_E$ .Note7. The integration range of switching energies is from  $10\%V_{CE}$  to  $10\%I_C(10\%I_E)$ .

Note8. The maximum specified value is under the condition of using PCB mounted on the power module. In case no PCB is used this maximum torque for M4 screw is 2.0 Nm.

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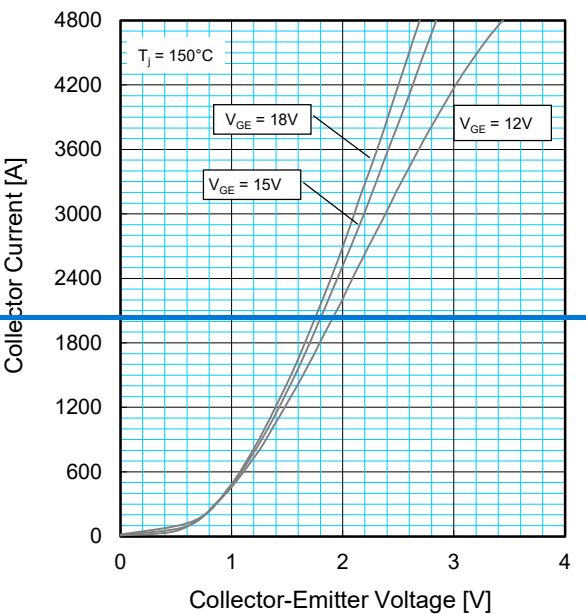
HIGH POWER SWITCHING USE

INSULATED TYPE

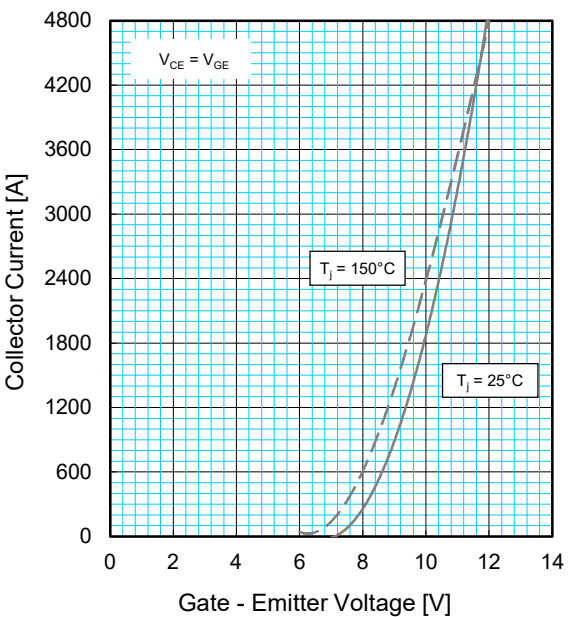
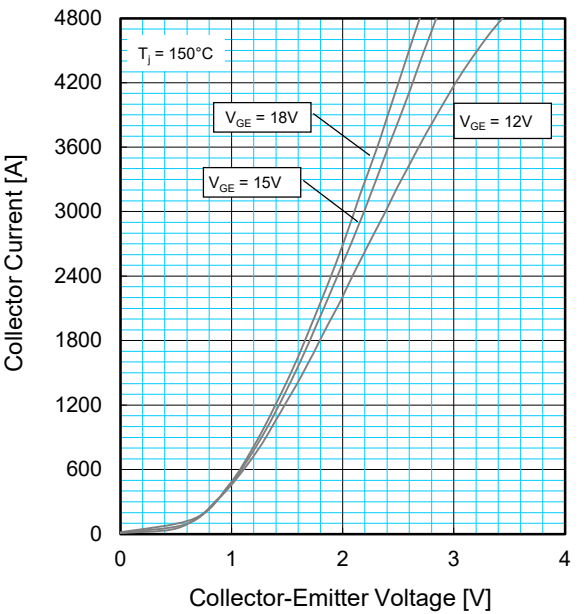
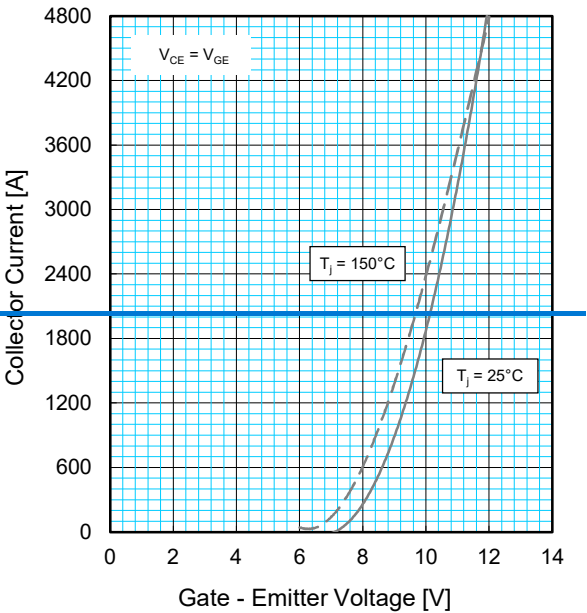
6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

PERFORMANCE CURVES

OUTPUT CHARACTERISTICS  
(TYPICAL)



TRANSFER CHARACTERISTICS  
(TYPICAL)



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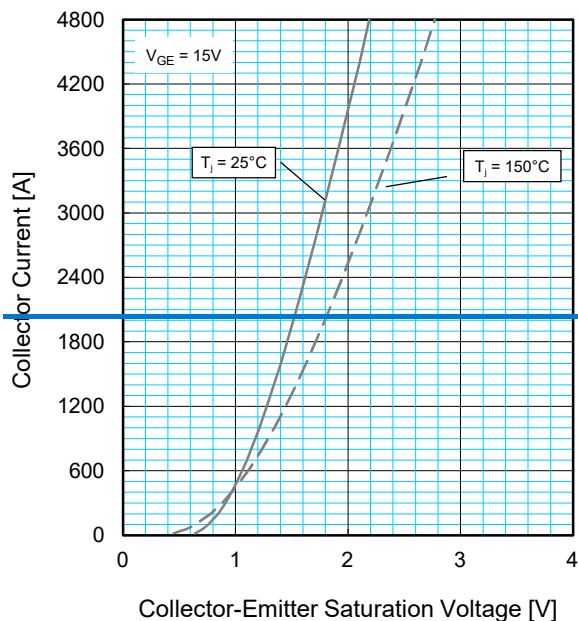
# CM2400HCB-34X

HIGH POWER SWITCHING USE

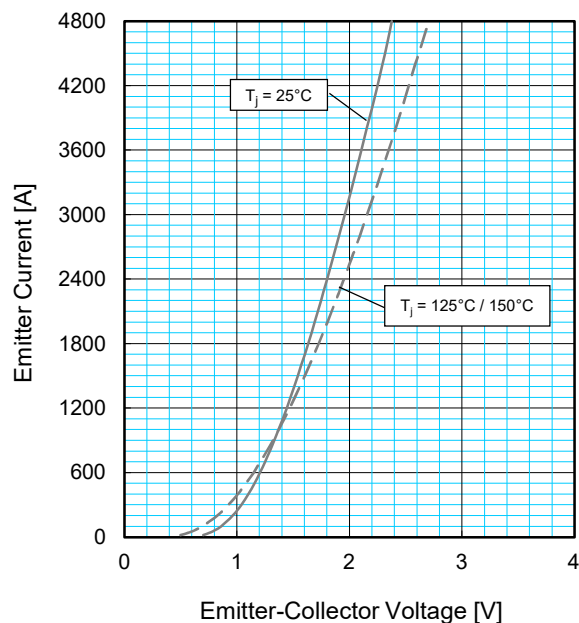
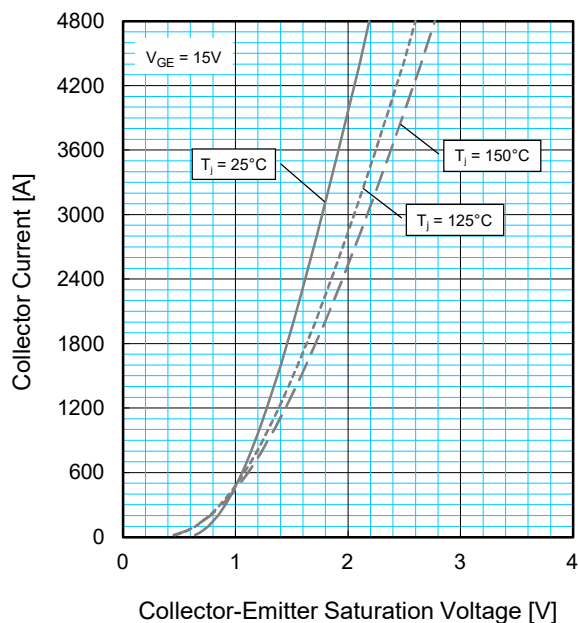
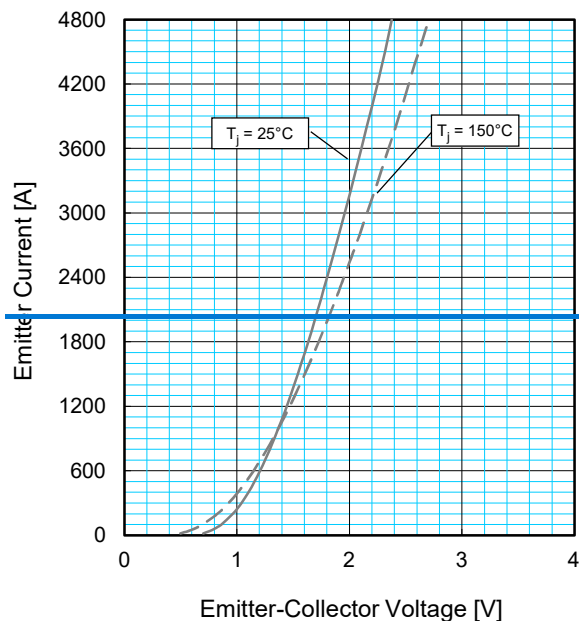
INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



## FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



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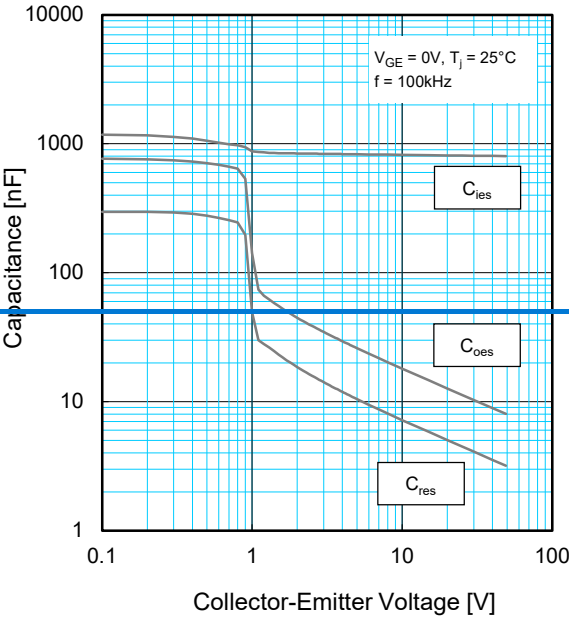
HIGH POWER SWITCHING USE

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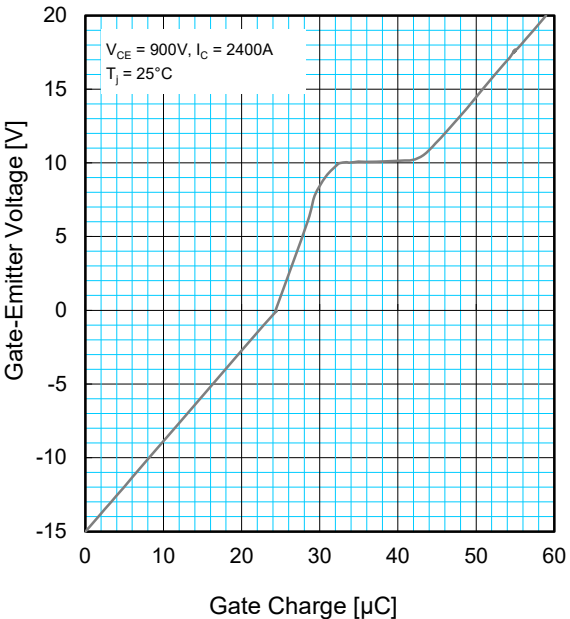
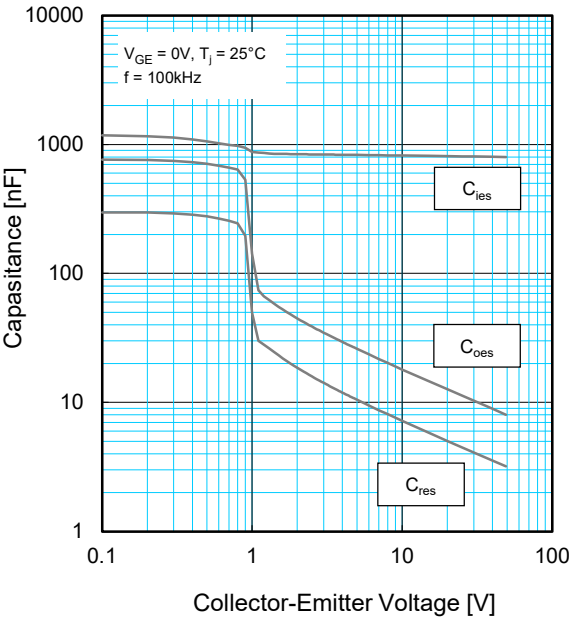
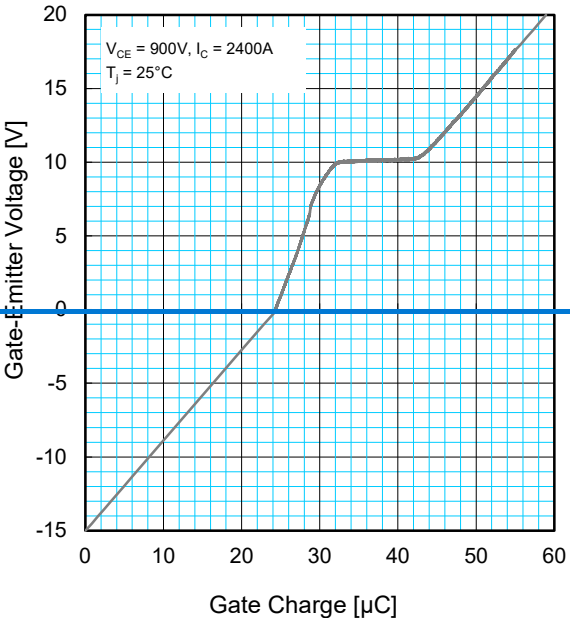
6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

PERFORMANCE CURVES

CAPACITANCE CHARACTERISTICS  
(TYPICAL)



GATE CHARGE CHARACTERISTICS  
(TYPICAL)



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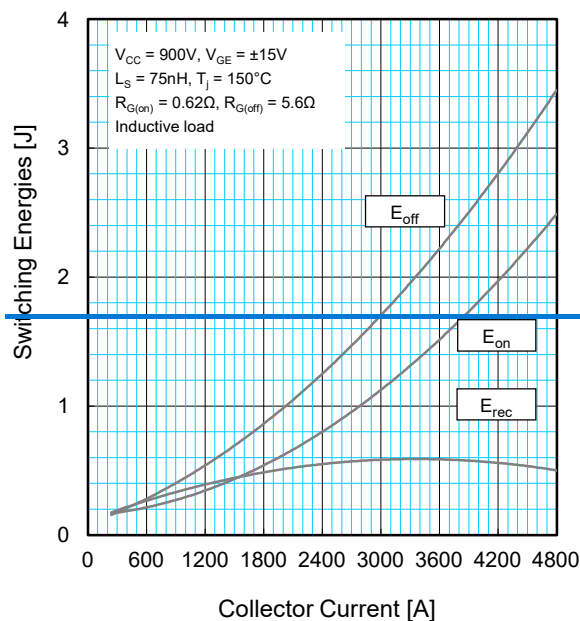
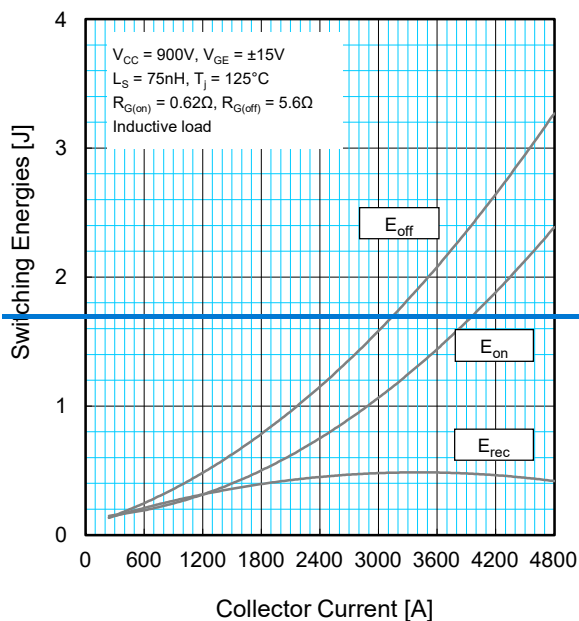
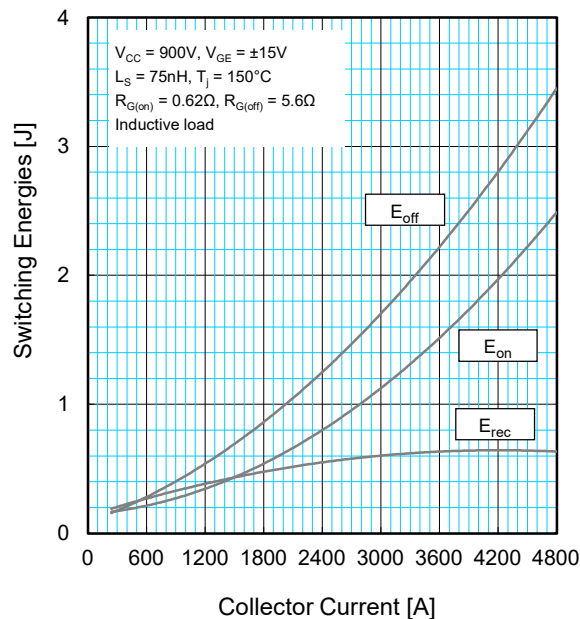
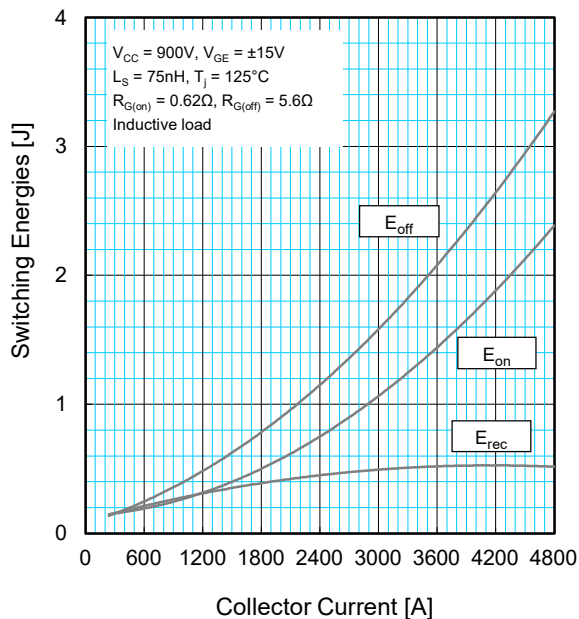
HIGH POWER SWITCHING USE

INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

## HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



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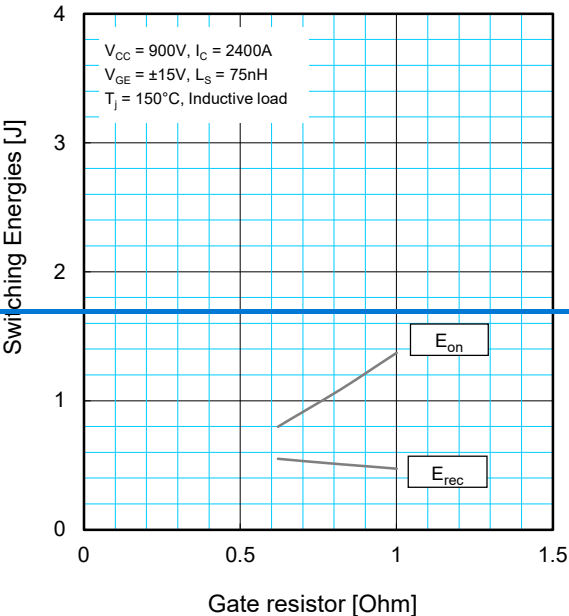
HIGH POWER SWITCHING USE

INSULATED TYPE

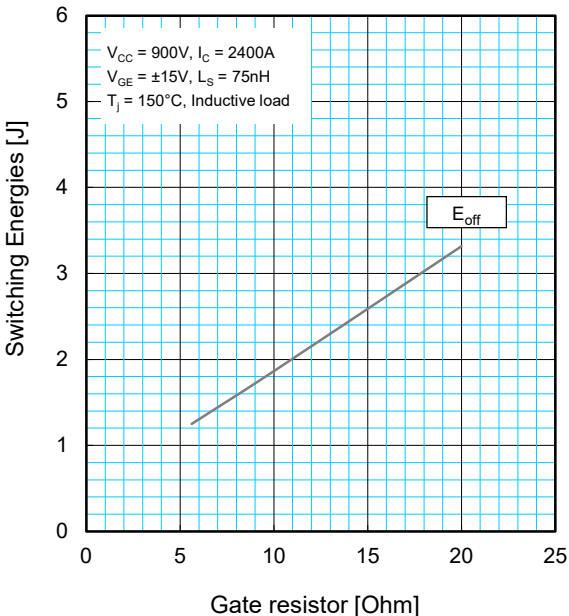
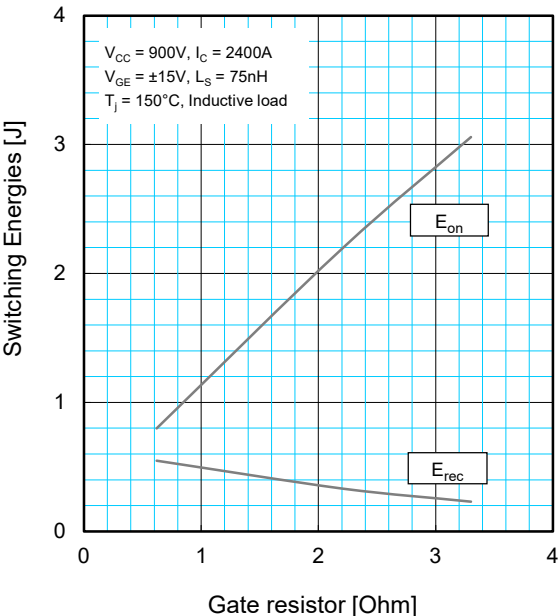
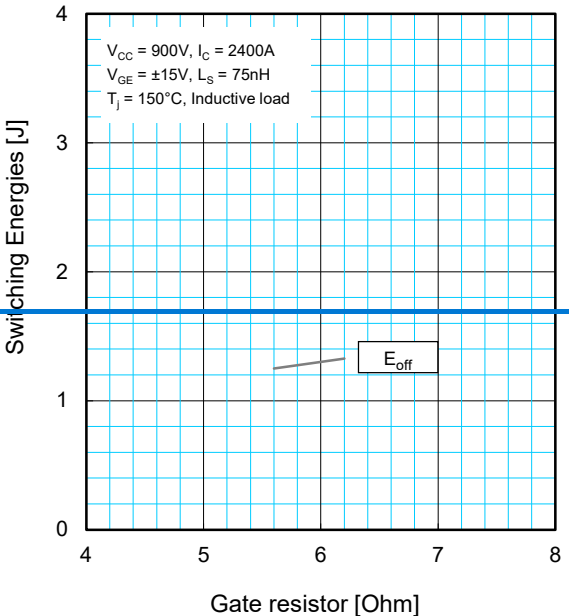
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PERFORMANCE CURVES

HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)





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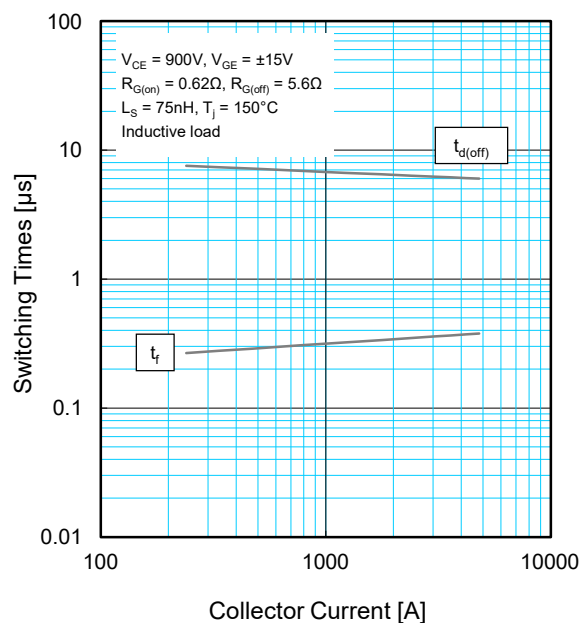
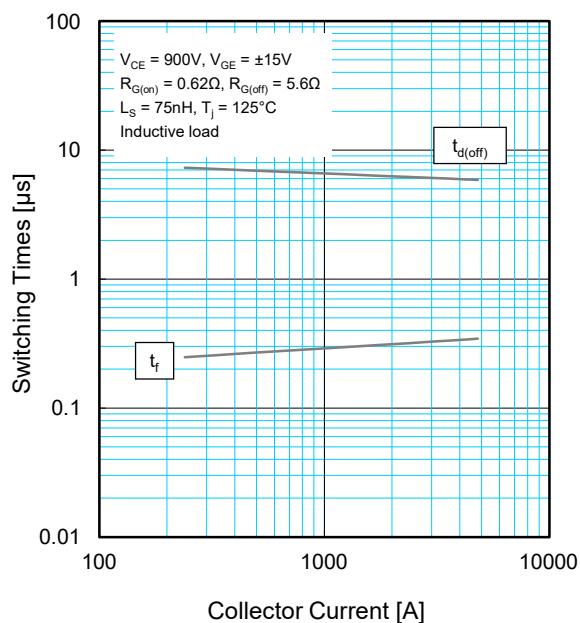
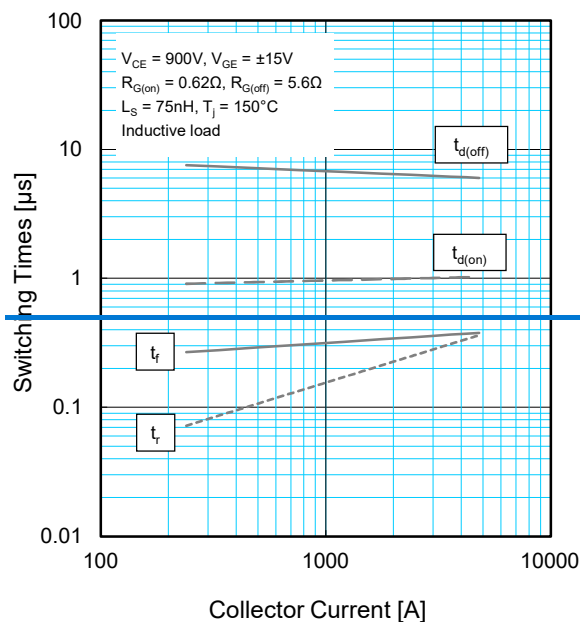
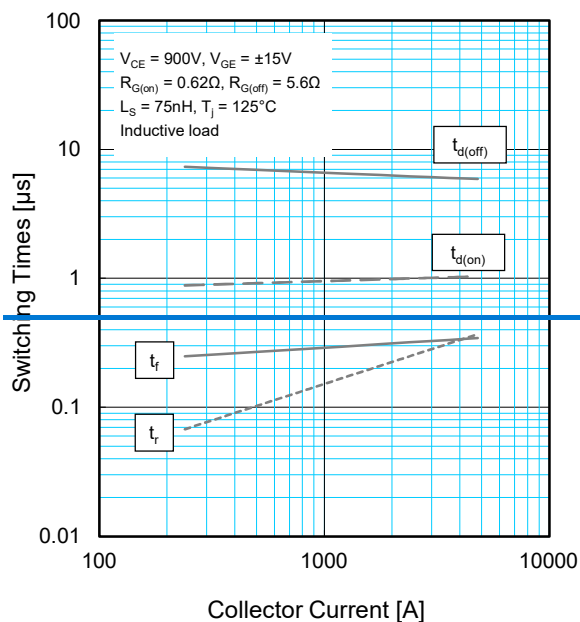
HIGH POWER SWITCHING USE

INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

HALF-BRIDGE SWITCHING TIME  
CHARACTERISTICS (TYPICAL)

HALF-BRIDGE SWITCHING TIME  
CHARACTERISTICS (TYPICAL)



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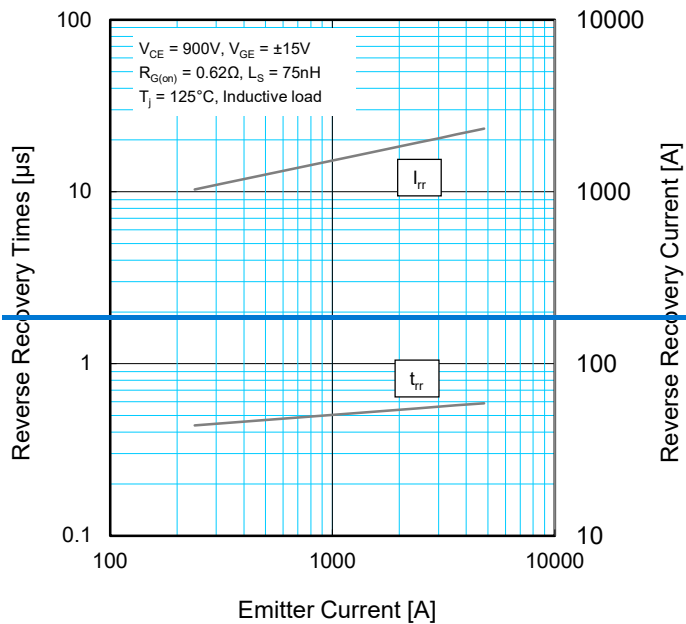
HIGH POWER SWITCHING USE

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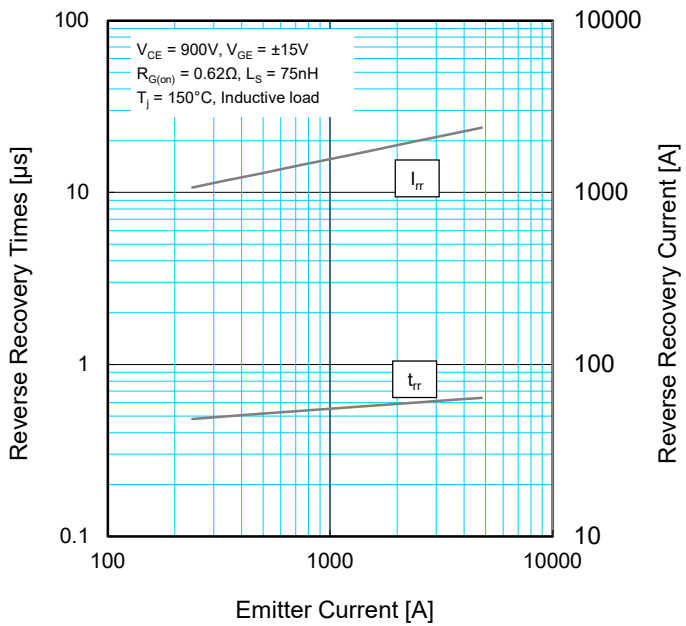
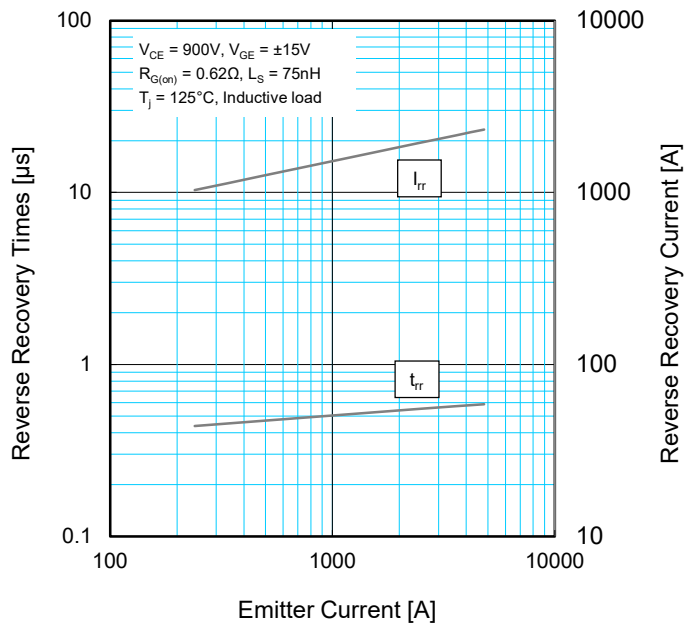
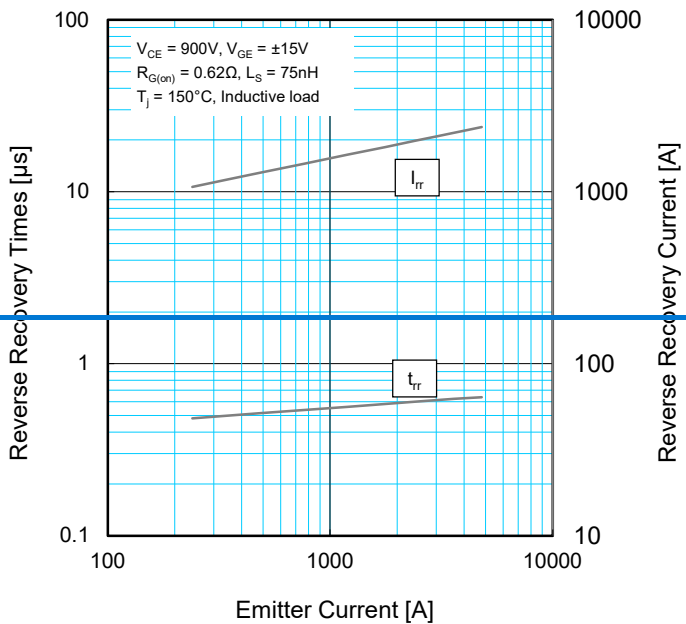
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PERFORMANCE CURVES

FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



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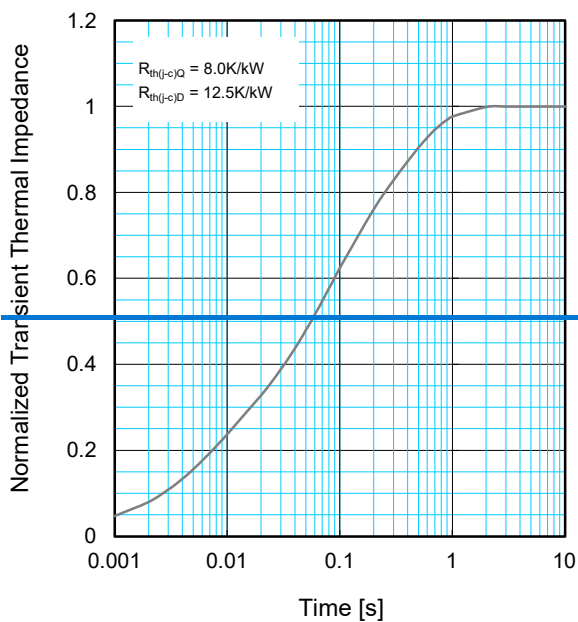
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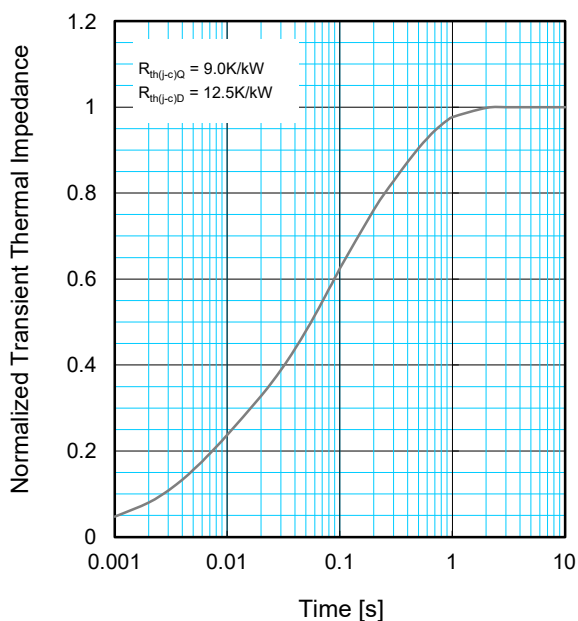
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## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i \left\{ 1 - \exp \left( -\frac{t}{\tau_i} \right) \right\}$$



	1	2	3	4
$R_i / R_{th(j-c)}$	0.0096	0.1893	0.4044	0.3967
$\tau_i$ [sec]	0.0001	0.0058	0.0602	0.3512

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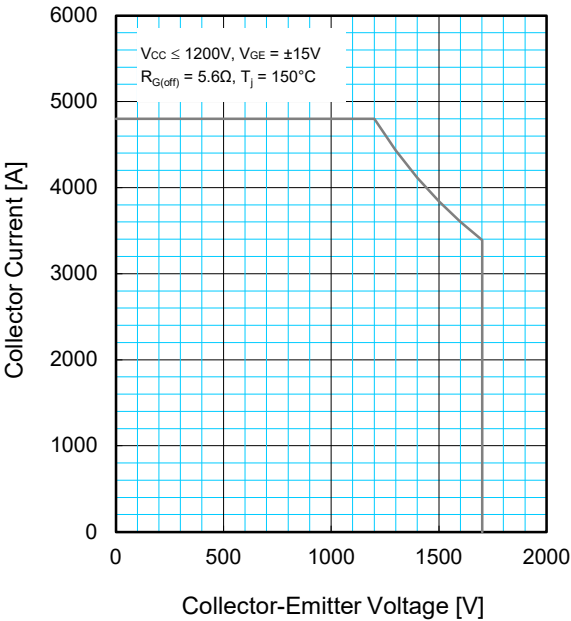
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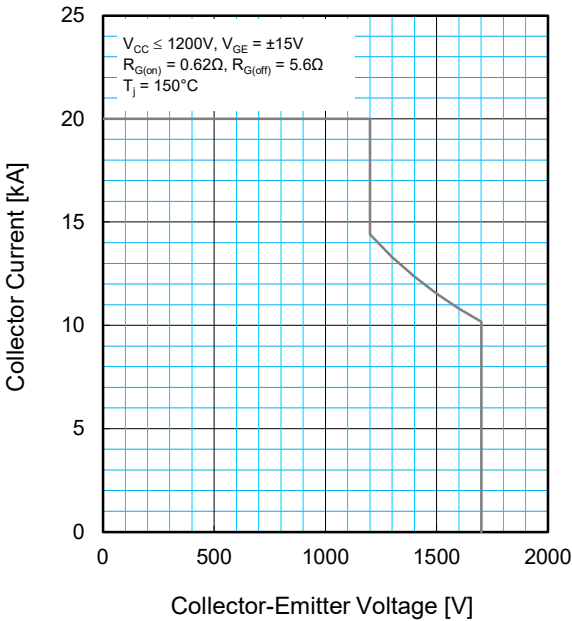
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PERFORMANCE CURVES

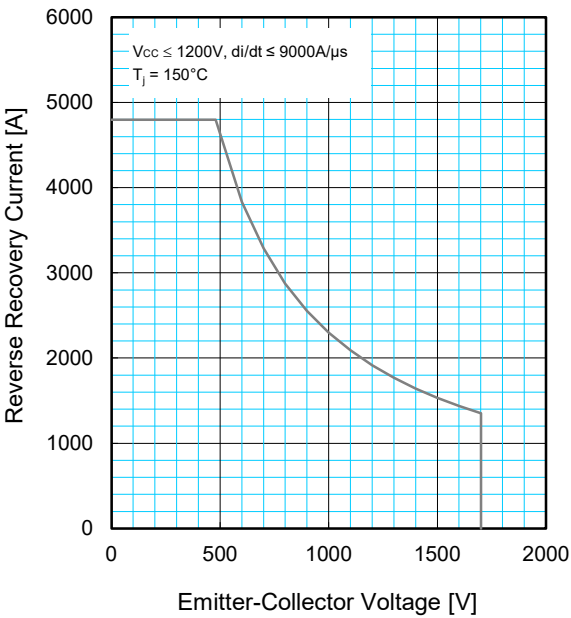
REVERSE BIAS SAFE OPERATING AREA (RBSOA)



SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



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### Important Notice

The information contained in this datasheet shall in no event be regarded as a guarantee of conditions or characteristics. This product has to be used within its specified maximum ratings, and is subject to customer's compliance with any applicable legal requirement, norms and standards.

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In usage of power semiconductor, there is always the possibility that trouble may occur with them by the reliability lifetime such as Power Cycle, Thermal Cycle or others, or when used under special circumstances (e.g. condensation, high humidity, dusty, salty, highlands, environment with lots of organic matter / corrosive gas / explosive gas, or situations which terminals of semiconductor products receive strong mechanical stress). Therefore, please pay sufficient attention to such circumstances. Further, depending on the technical requirements, our semiconductor products may contain environmental regulation substances, etc. If there is necessity of detailed confirmation, please contact our nearest sales branch or distributor.

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### **Keep safety first in your circuit designs!**

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