



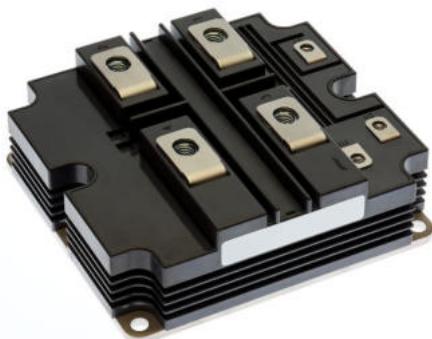
&lt; High Voltage Insulated Gate Bipolar Transistor: HVIGBT &gt;

# CM600HG-130X

HIGH POWER SWITCHING USE  
INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## CM600HG-130X



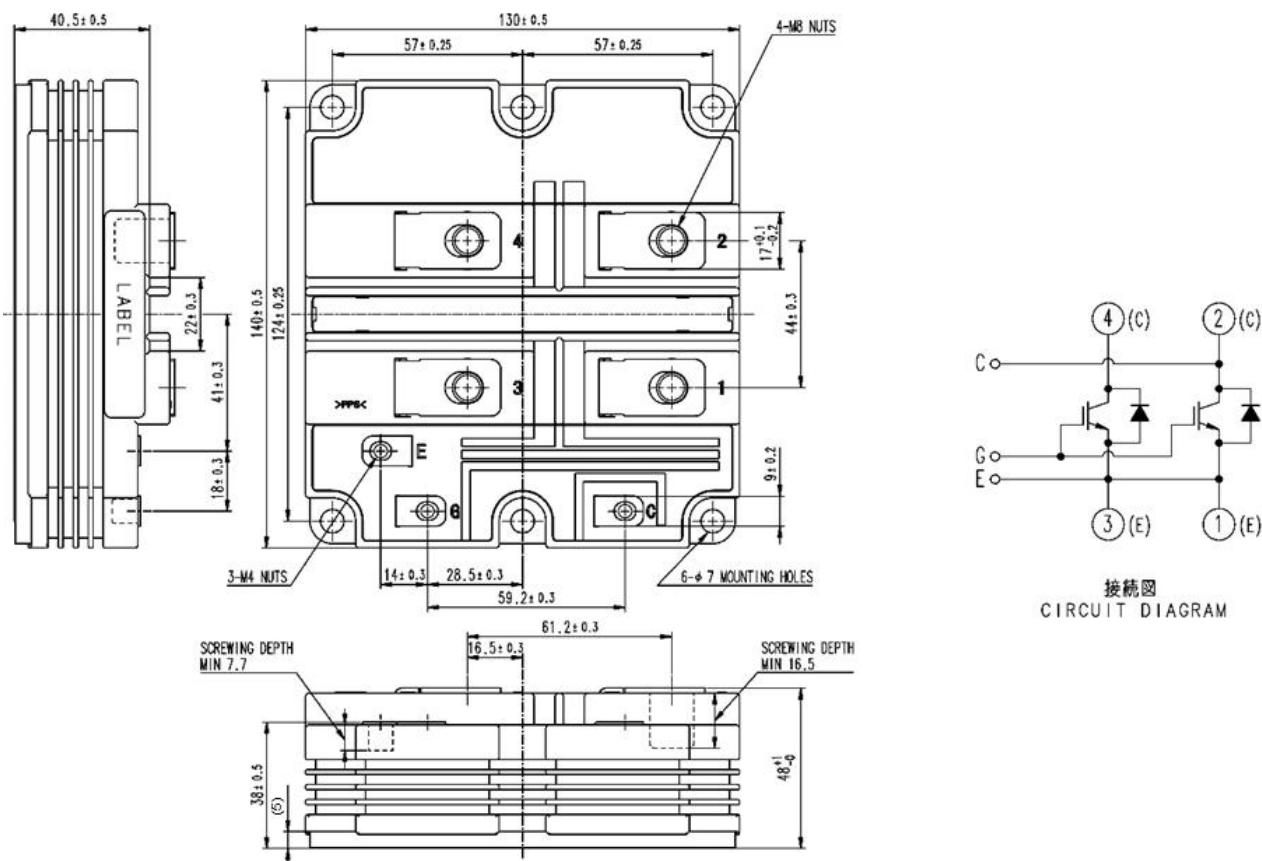
- $I_C$  ..... 600 A
- $V_{CES}$  ..... 6500 V
- 1-element in a Pack
- Insulated Type
- CSTBT™(III)
- RFC Diode
- AISiC Baseplate

## APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers

### OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



< High Voltage Insulated Gate Bipolar Transistor: HVIGBT MODULE >

**CM600HG-130X**

HIGH POWER SWITCHING USE  
INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor)

**MAXIMUM RATINGS (T<sub>j</sub>=25°C, unless otherwise specified)**

Symbol	Item	Conditions	Ratings	Unit
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V, T <sub>j</sub> = +150°C	6500	V
		V <sub>GE</sub> = 0V, T <sub>j</sub> = +25°C	6300	
		V <sub>GE</sub> = 0V, T <sub>j</sub> = -50°C	5700	
V <sub>GES</sub>	Gate-emitter voltage	V <sub>CE</sub> = 0V, T <sub>j</sub> = 25°C	±20	V
I <sub>C</sub>	Collector current	DC, T <sub>C</sub> = 110°C	600	A
I <sub>CRM</sub>		Pulse (Note 1)	1200	A
I <sub>E</sub>	Emitter current (Note 2)	DC, T <sub>C</sub> = 95°C	600	A
I <sub>ERM</sub>		Pulse (Note 1)	1200	A
P <sub>tot</sub>	Maximum power dissipation (Note 3)	T <sub>C</sub> = 25°C, IGBT part	8300	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min.	10200	V
Q <sub>pd</sub>	Partial discharge	V1 = 6900Vrms, V2 = 5100Vrms 60Hz	10	pC
T <sub>j</sub>	Junction temperature	—	-50 ~ +150	°C
T <sub>jop</sub>	Operating junction temperature	—	-50 ~ +150	°C
T <sub>stg</sub>	Storage temperature	—	-55 ~ +150	°C
t <sub>psc</sub>	Short circuit pulse width	V <sub>CC</sub> = 4500V, V <sub>CE</sub> ≤ V <sub>CES</sub> , V <sub>GE</sub> = ±15V T <sub>j</sub> = 150°C	10	μs

**ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25°C, unless otherwise specified)**

Symbol	Item	Conditions	Limits			Unit	
			Min.	Typ.	Max.		
I <sub>CES</sub>	Collector cutoff current	V <sub>CE</sub> = V <sub>CES</sub> V <sub>GE</sub> = 0V	T <sub>j</sub> = 25°C	—	—	4.0	
			T <sub>j</sub> = 125°C	—	3.5	—	
			T <sub>j</sub> = 150°C	—	—	100	
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	V <sub>CE</sub> = 10V, I <sub>C</sub> = 60mA, T <sub>j</sub> = 25°C	6.5	7.0	7.5	V	
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0V, T <sub>j</sub> = 25°C	-0.5	—	0.5	μA	
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 100kHz T <sub>j</sub> = 25°C	—	101	—	nF	
C <sub>oes</sub>	Output capacitance		—	4.1	—		
C <sub>res</sub>	Reverse transfer capacitance		—	0.5	—		
Q <sub>G</sub>	Total gate charge	V <sub>CC</sub> = 3600V, I <sub>C</sub> = 600A V <sub>GE</sub> = ±15V, T <sub>j</sub> = 25°C	—	6.6	—	μC	
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 600A (Note 4) V <sub>GE</sub> = 15V	T <sub>j</sub> = 25°C	—	2.50	—	
			T <sub>j</sub> = 125°C	—	3.20	—	
			T <sub>j</sub> = 150°C	—	3.30	3.80	
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> = 3600V I <sub>C</sub> = 600A V <sub>GE</sub> = ±15V R <sub>G(on)</sub> = 6.2Ω L <sub>s</sub> = 225nH	T <sub>j</sub> = 150°C	—	—	1.45	
t <sub>r</sub>	Rise time		T <sub>j</sub> = 150°C	—	—	0.50	
E <sub>on(10%)</sub>	Turn-on switching energy (Note 7) (per pulse)		T <sub>j</sub> = 25°C	—	4.15	—	
			T <sub>j</sub> = 125°C	—	4.50	—	
			T <sub>j</sub> = 150°C	—	5.10	—	
E <sub>on</sub>	Turn-on switching energy (Note 5) (per pulse)	Inductive load	T <sub>j</sub> = 25°C	—	4.40	—	
			T <sub>j</sub> = 125°C	—	4.85	—	
			T <sub>j</sub> = 150°C	—	5.50	—	
t <sub>d(off)</sub>	Turn-off delay time	V <sub>CC</sub> = 3600V I <sub>C</sub> = 600A V <sub>GE</sub> = ±15V R <sub>G(off)</sub> = 45Ω L <sub>s</sub> = 225nH	T <sub>j</sub> = 25°C	—	5.90	—	
			T <sub>j</sub> = 125°C	—	7.00	—	
			T <sub>j</sub> = 150°C	—	7.00	10.5	
t <sub>f</sub>	Fall time		T <sub>j</sub> = 25°C	—	0.40	—	
			T <sub>j</sub> = 125°C	—	0.80	—	
			T <sub>j</sub> = 150°C	—	0.80	—	
E <sub>off(10%)</sub>	Turn-off switching energy (Note 7) (per pulse)	Inductive load	T <sub>j</sub> = 25°C	—	2.40	—	
			T <sub>j</sub> = 125°C	—	3.85	—	
			T <sub>j</sub> = 150°C	—	4.00	—	
E <sub>off</sub>	Turn-off switching energy (Note 5) (per pulse)		T <sub>j</sub> = 25°C	—	2.55	—	
			T <sub>j</sub> = 125°C	—	4.10	—	
			T <sub>j</sub> = 150°C	—	4.25	—	

## &lt; High Voltage Insulated Gate Bipolar Transistor: HVIGBT MODULE &gt;

**CM600HG-130X**

## HIGH POWER SWITCHING USE

## INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor)

**ELECTRICAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit	
			Min.	Typ.	Max.		
V <sub>EC</sub>	Emitter-collector voltage (Note 2)	I <sub>E</sub> = 600A (Note 4) V <sub>GE</sub> = 0V	T <sub>j</sub> = 25°C	—	2.50	—	
			T <sub>j</sub> = 125°C	—	3.20	—	
			T <sub>j</sub> = 150°C	—	3.30	3.80	
t <sub>rr</sub>	Reverse recovery time (Note 2)	V <sub>CC</sub> = 3600V I <sub>E</sub> = 600A V <sub>GE</sub> = ±15V R <sub>G(on)</sub> = 6.2Ω L <sub>S</sub> = 225nH  Inductive load	T <sub>j</sub> = 25°C	—	1.70	—	
			T <sub>j</sub> = 125°C	—	2.05	—	
			T <sub>j</sub> = 150°C	—	2.15	—	
I <sub>rr</sub>	Reverse recovery current (Note 2)		T <sub>j</sub> = 25°C	—	950	—	
			T <sub>j</sub> = 125°C	—	900	—	
			T <sub>j</sub> = 150°C	—	900	—	
Q <sub>rr(10%)</sub>	Reverse recovery charge (Note 2,6)		T <sub>j</sub> = 25°C	—	1200	—	
			T <sub>j</sub> = 125°C	—	1550	—	
			T <sub>j</sub> = 150°C	—	1600	—	
Q <sub>rr</sub>	Reverse recovery charge (Note 2,5)		T <sub>j</sub> = 25°C	—	1200	—	
			T <sub>j</sub> = 125°C	—	1550	—	
			T <sub>j</sub> = 150°C	—	1650	—	
E <sub>rec(10%)</sub>	Reverse recovery energy (Note 2,7) (per pulse)		T <sub>j</sub> = 25°C	—	2.35	—	
			T <sub>j</sub> = 125°C	—	3.40	—	
			T <sub>j</sub> = 150°C	—	3.65	—	
E <sub>rec</sub>	Reverse recovery energy (Note 2,5) (per pulse)		T <sub>j</sub> = 25°C	—	2.50	—	
			T <sub>j</sub> = 125°C	—	3.60	—	
			T <sub>j</sub> = 150°C	—	4.00	—	

**THERMAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to Case, IGBT part	—	—	15.0	K/kW
		Junction to Case, FWDi part	—	—	24.0	K/kW
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink λ <sub>grease</sub> = 1W/m·K, D <sub>(c-s)</sub> = 80μm	—	7.5	—	K/kW

**MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M <sub>t</sub>	Mounting torque	Main terminals screw: M8	7.0	—	19.0	N·m
M <sub>s</sub>		Mounting screw: M6	3.0	—	6.0	N·m
M <sub>t</sub>		Auxiliary terminals screw: M4	1.0	—	3.0	N·m
m	Mass	—	—	1.0	—	kg
CTI	Comparative tracking index	—	600	—	—	—
d <sub>a</sub>	Clearance	—	26.0	—	—	mm
d <sub>s</sub>	Creepage distance	—	56.0	—	—	mm
L <sub>P(C-E)</sub>	Parasitic stray inductance	—	—	20.5	—	nH
R <sub>CC'+EE'</sub>	Internal lead resistance	T <sub>c</sub> = 25°C	—	0.18	—	mΩ

Note1. Pulse width and repetition rate should be such that junction temperature (T<sub>j</sub>) does not exceed T<sub>jopmax</sub> rating.

Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD).

Note3. Junction temperature (T<sub>j</sub>) should not exceed T<sub>jmax</sub> rating (150°C).

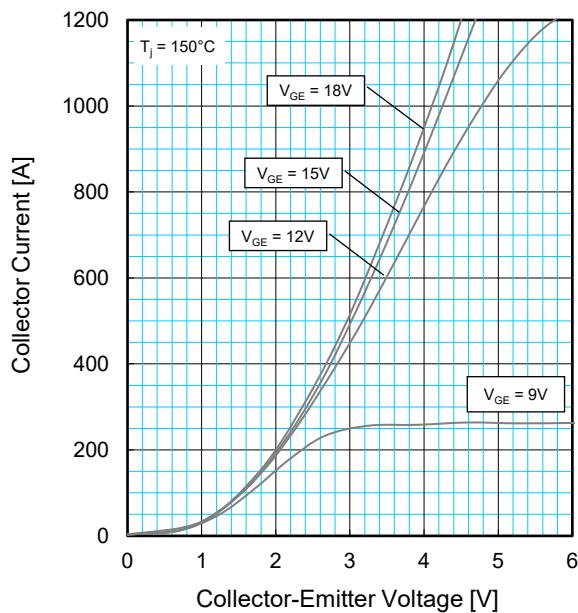
Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

Note5. Definition of all items is according to IEC 60747, unless otherwise specified.

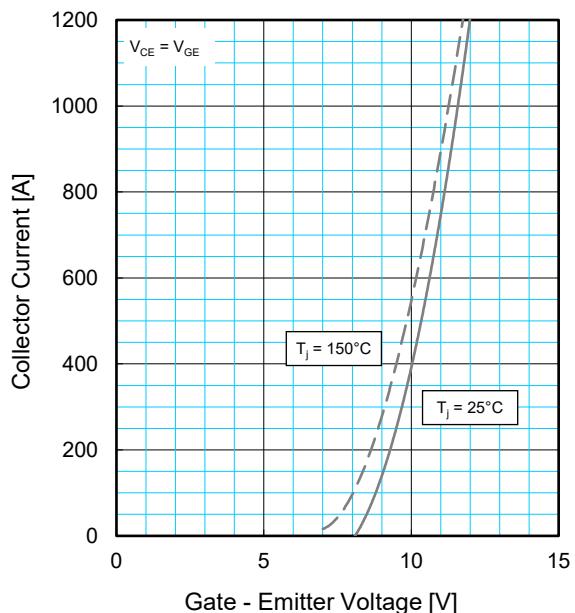
Note6. The integration range of reverse recovery charge is from I<sub>E</sub> = 0A to 10%I<sub>E</sub>.Note7. The integration range of switching energies is from 10%V<sub>CE</sub> to 10%I<sub>C</sub>(I<sub>E</sub>).

**PERFORMANCE CURVES**

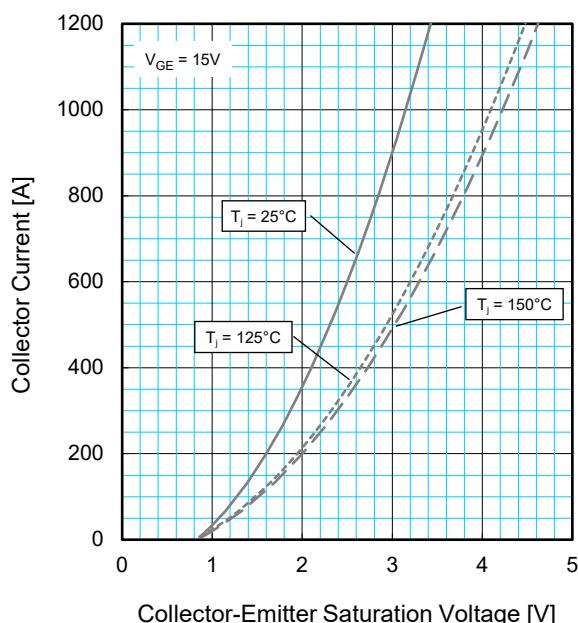
**OUTPUT CHARACTERISTICS  
(TYPICAL)**



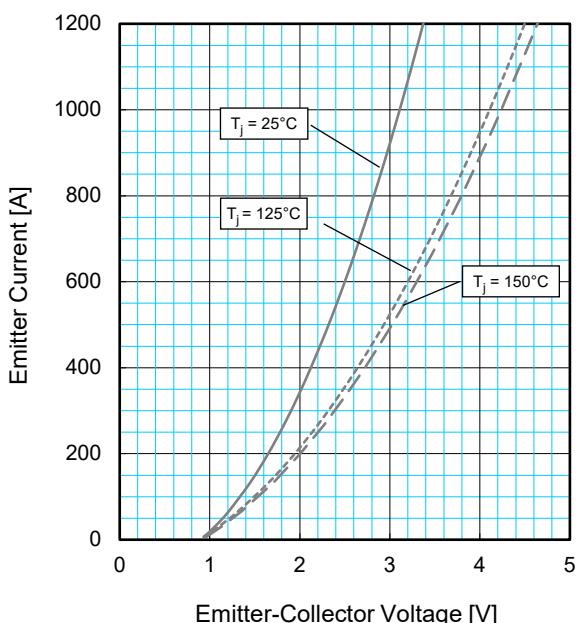
**TRANSFER CHARACTERISTICS  
(TYPICAL)**



**COLLECTOR-EMITTER SATURATION VOLTAGE  
CHARACTERISTICS (TYPICAL)**

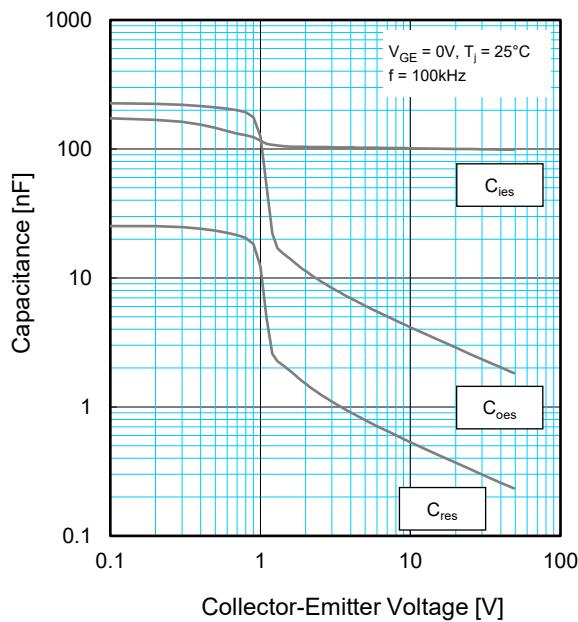


**FREE-WHEEL DIODE FORWARD  
CHARACTERISTICS (TYPICAL)**

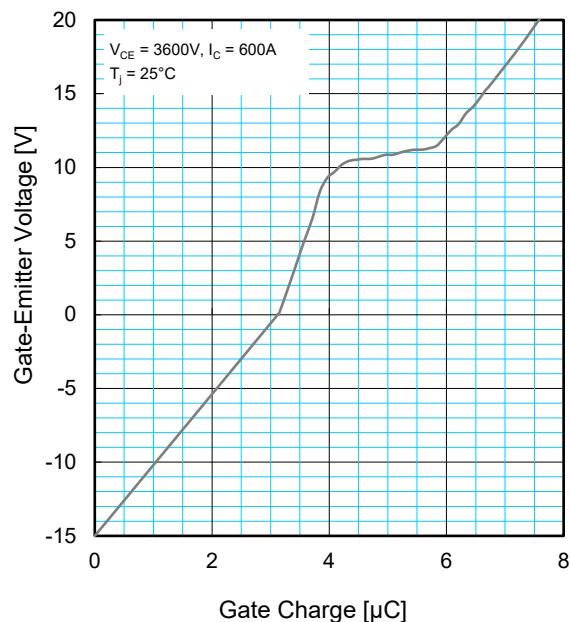


## PERFORMANCE CURVES

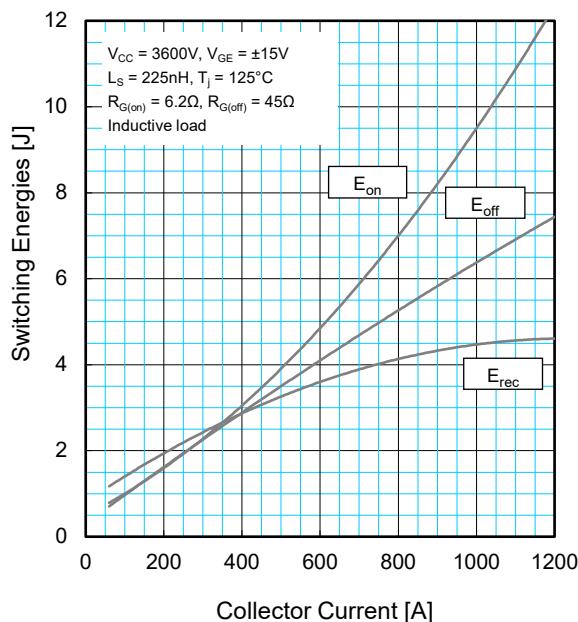
### CAPACITANCE CHARACTERISTICS (TYPICAL)



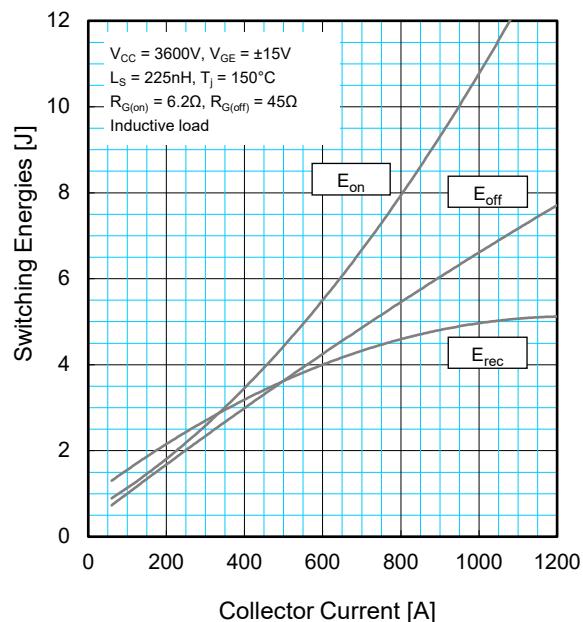
### GATE CHARGE CHARACTERISTICS (TYPICAL)



### HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

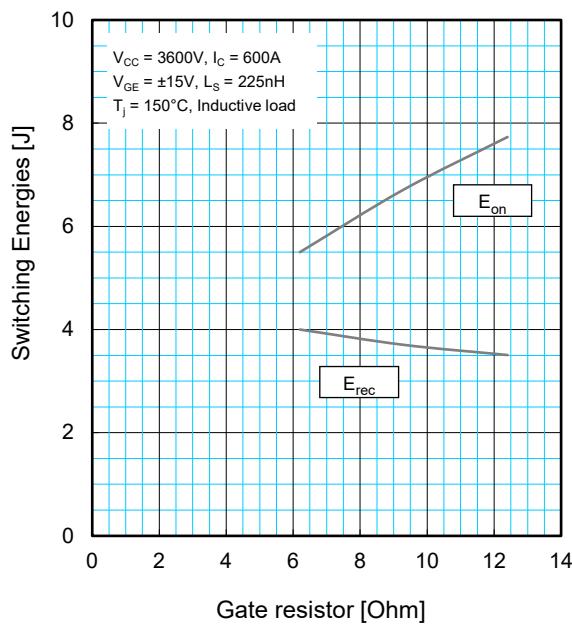


### HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

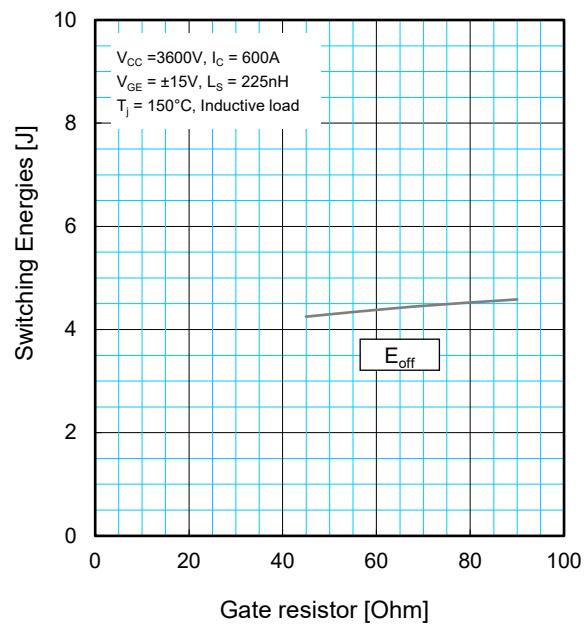


## PERFORMANCE CURVES

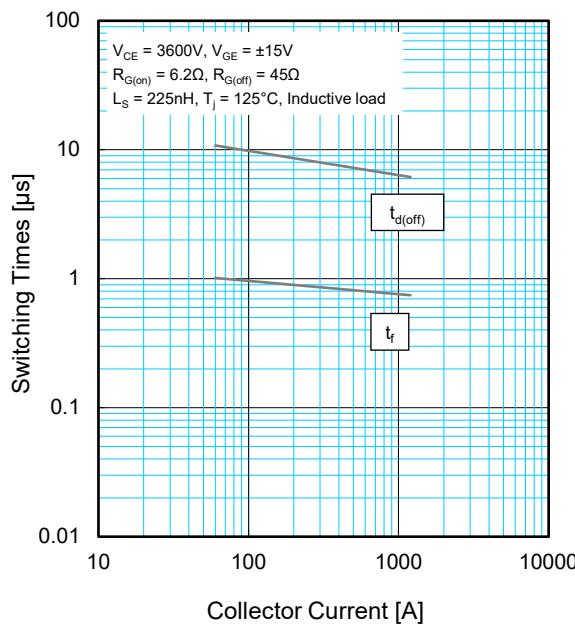
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



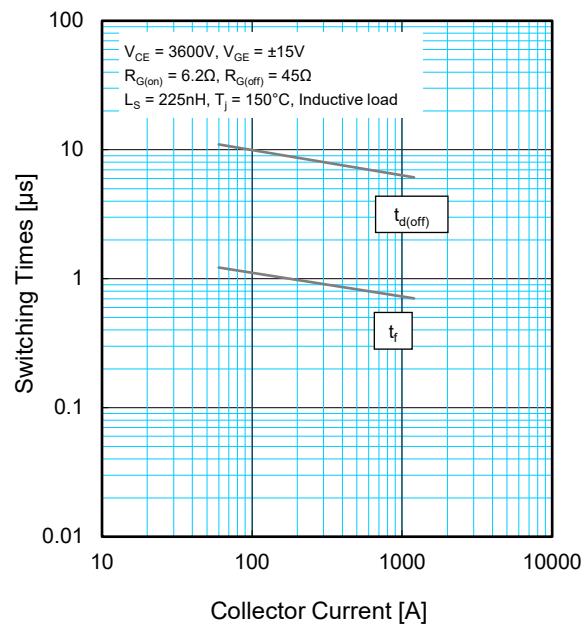
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**

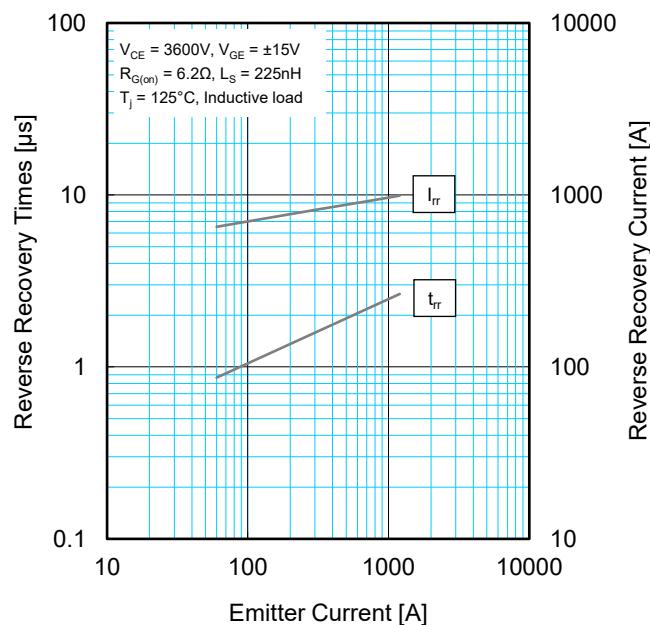
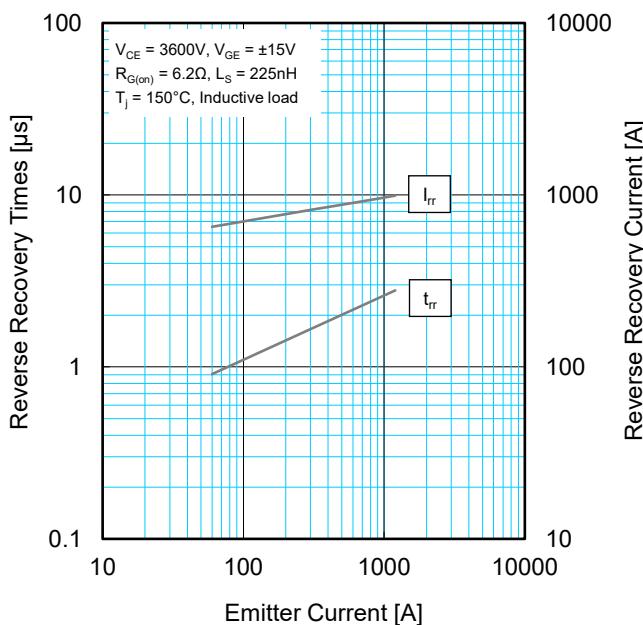
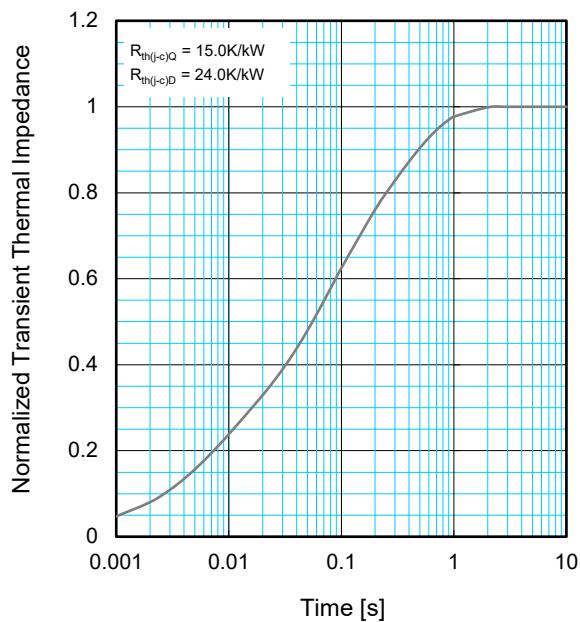


**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**



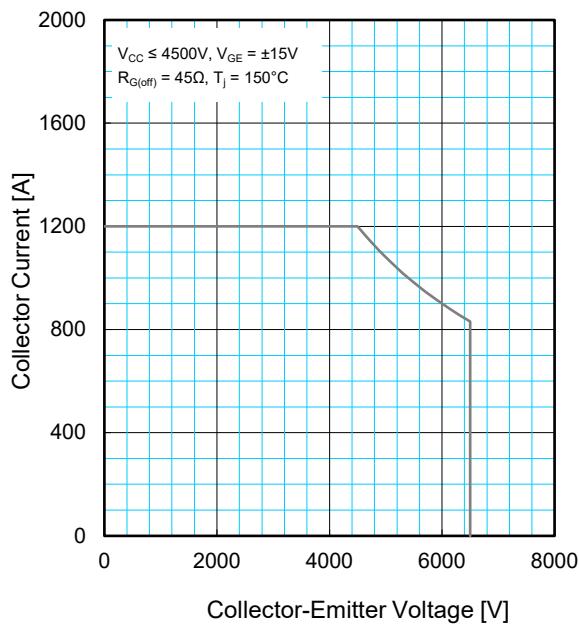
**PERFORMANCE CURVES****FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)****FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)****TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS**

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i \left\{ 1 - \exp\left(-\frac{t}{\tau_i}\right) \right\}$$

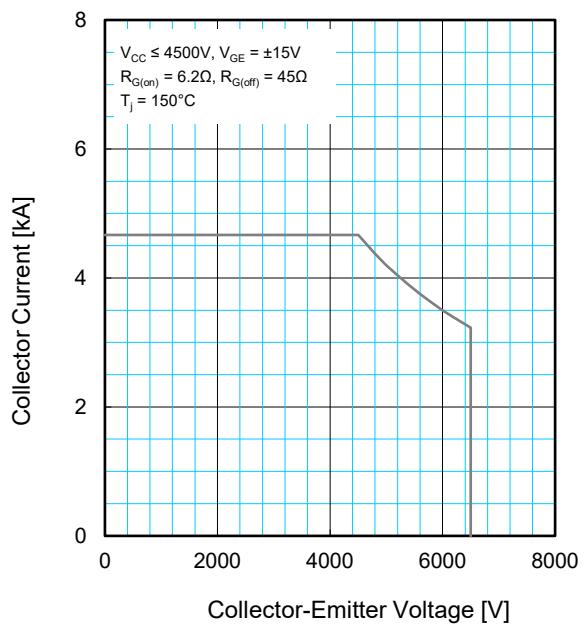
	1	2	3	4
$R_i / R_{th(j-c)}$	0.0096	0.1893	0.4044	0.3967
$\tau_i$ [s]	0.0001	0.0058	0.0602	0.3512

**PERFORMANCE CURVES**

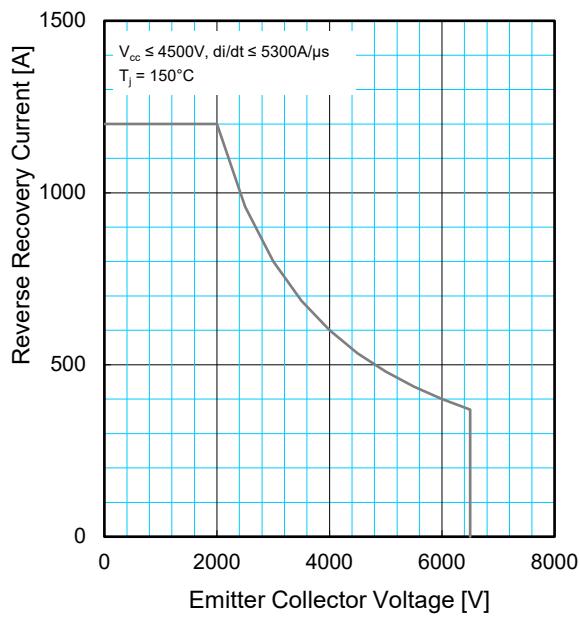
**REVERSE BIAS SAFE OPERATING AREA (RBSOA)**



**SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)**



**FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)**



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