

<IGBT Modules>

CM600HA-34S

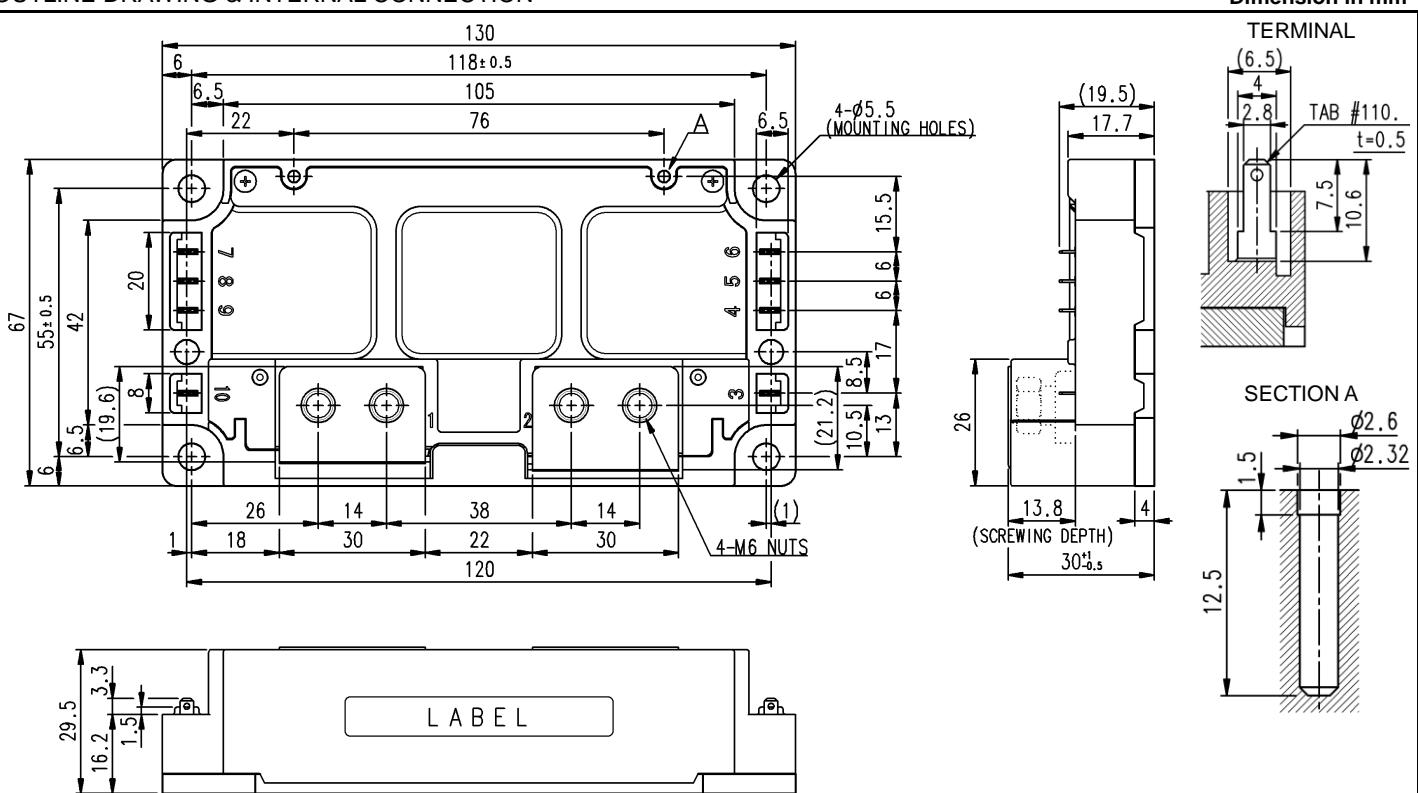
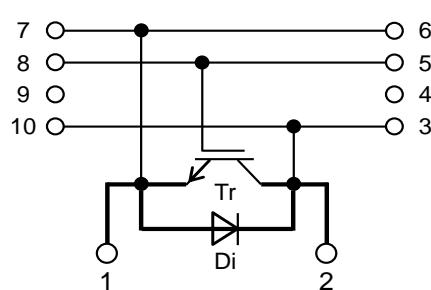
**HIGH POWER SWITCHING USE
INSULATED TYPE**


single pack

Collector current I_C **600 A**
 Collector-emitter voltage V_{CES} **1700 V**
 Maximum junction temperature T_{vjmax} **175 °C**
 •Flat base Type
 •Copper base plate
 •Tin plating pin terminals
 •RoHS Directive compliant
 •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, Photovoltaic power, Wind power, etc.

OUTLINE DRAWING & INTERNAL CONNECTION**Dimension in mm****INTERNAL CONNECTION****Terminal code**

- 1: E
- 2: C
- 3: C_S
- 4: NC
- 5: G
- 6: E_S
- 7: E_S
- 8: G
- 9: NC
- 10: C_S

Tolerance otherwise specified		
Division of Dimension	Tolerance	
0.5	to 3	± 0.2
over 3	to 6	± 0.3
over 6	to 30	± 0.5
over 30	to 120	± 0.8
over 120	to 400	± 1.2
over 400	to 1000	± 2.0
over 1000	to 2000	± 3.0
over 2000	to 4000	± 4.0

MAXIMUM RATINGS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I _C	Collector current	DC, T _C =111 °C (Note2, 4)	600	A
I _{CRM}		Pulse, Repetitive (Note3)	1200	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	4285	W
I _E (Note1)	Emitter current	DC (Note2)	600	A
I _{ERM} (Note1)		Pulse, Repetitive (Note3)	1200	
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note4)	125	
T _{vjop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited	-	-	0.5	µA
V _{GE(th)}	Gate-emitter threshold voltage	I _C =60 mA, V _{CE} =10 V	5.4	6.0	6.6	V
V _{CEsat} (Terminal)	Collector-emitter saturation voltage	I _C =600 A, V _{GE} =15 V, Refer to the figure of test circuit (Note5)	T _{vj} =25 °C	-	2.10	2.60
			T _{vj} =125 °C	-	2.35	-
			T _{vj} =150 °C	-	2.45	-
V _{CEsat} (Chip)		I _C =600 A, V _{GE} =15 V, (Note5)	T _{vj} =25 °C	-	2.00	2.50
			T _{vj} =125 °C	-	2.25	-
			T _{vj} =150 °C	-	2.35	-
C _{ies}	Input capacitance	V _{CE} =10 V, G-E short-circuited	-	-	140	nF
C _{oes}	Output capacitance		-	-	15	
C _{res}	Reverse transfer capacitance		-	-	2.5	
Q _G	Gate charge	V _{CC} =1000 V, I _C =600 A, V _{GE} =15 V	-	2.52	-	µC
t _{d(on)}	Turn-on delay time	V _{CC} =1000 V, I _C =600 A, V _{GE} =±15 V, R _G =0 Ω, Inductive load	-	-	900	ns
t _r	Rise time		-	-	300	
t _{d(off)}	Turn-off delay time		-	-	900	
t _f	Fall time		-	-	400	
V _{EC} (Note1) (Terminal)	Emitter-collector voltage	I _E =600 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T _{vj} =25 °C	-	2.10	2.60
			T _{vj} =125 °C	-	2.20	-
			T _{vj} =150 °C	-	2.15	-
V _{EC} (Note1) (Chip)		I _E =600 A, G-E short-circuited, (Note5)	T _{vj} =25 °C	-	2.00	2.50
			T _{vj} =125 °C	-	2.10	-
			T _{vj} =150 °C	-	2.05	-
t _{rr} (Note1)	Reverse recovery time	V _{CC} =1000 V, I _E =600 A, V _{GE} =±15 V, R _G =0 Ω, Inductive load	-	-	500	ns
Q _{rr} (Note1)	Reverse recovery charge		-	120	-	µC
E _{on}	Turn-on switching energy per pulse	V _{CC} =1000 V, I _C =I _E =600 A, V _{GE} =±15 V, R _G =0 Ω, T _{vj} =150 °C, Inductive load	-	287	-	mJ
E _{off}	Turn-off switching energy per pulse		-	154	-	
E _{rr} (Note1)	Reverse recovery energy per pulse		-	152	-	
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, T _C =25 °C (Note4)	-	0.2	-	mΩ
r _g	Internal gate resistance	-	-	3.67	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, IGBT (Note4)	-	-	35	K/kW
$R_{th(j-c)D}$		Junction to case, FWD (Note4)	-	-	53.4	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, Thermal grease applied (Note4, 6)	-	18	-	K/kW

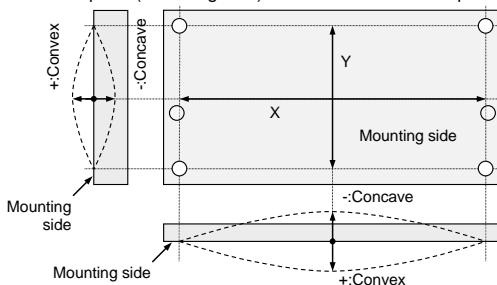
MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit	
			Min.	Typ.	Max.		
M_t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
M_s	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
d_s	Creepage distance	Terminal to terminal	22.0	-	-	mm	
		Terminal to base plate	21.9	-	-		
d_a	Clearance	Terminal to terminal	16.5	-	-	mm	
		Terminal to base plate	12.5	-	-		
e_c	Flatness of base plate	On the centerline X, Y (Note7)	-50	-	+100	μm	
m	mass	-	-	490	-	g	

*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature (T_{vj}) should not exceed $T_{vj\max}$ rating.
3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed $T_{vj\max}$ rating.
4. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
6. Typical value is measured by using thermally conductive grease of $\lambda=0.9 \text{ W}/(\text{m}\cdot\text{K})/D_{(C-S)}=100 \mu\text{m}$.
7. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



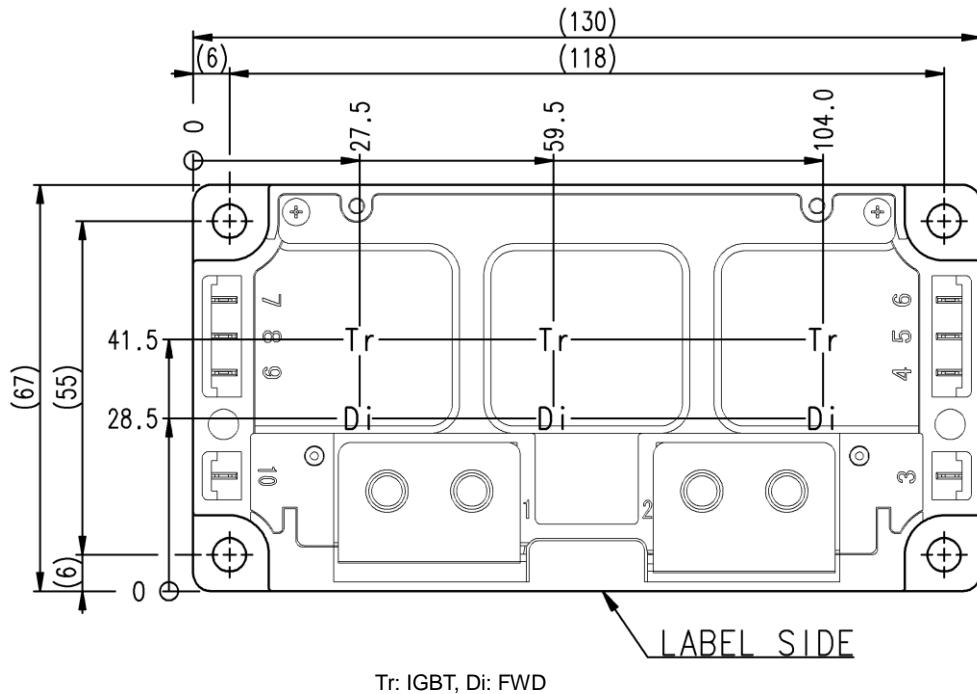
8. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness (t1.0).

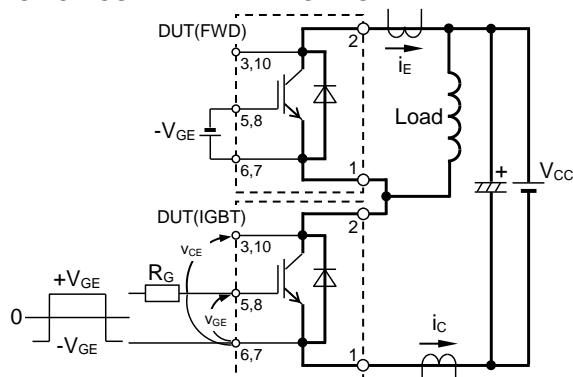
Type	Size	Tightening torque	Recommended tightening method
(1) PT®	K25x8	$0.55 \pm 0.055 \text{ N}\cdot\text{m}$	by handwork (equivalent to 30 r/min by mechanical screw driver) ~ 600 r/min (by mechanical screw driver)
(2) PT®	K25x10	$0.85 \pm 0.085 \text{ N}\cdot\text{m}$	
(3) DELTA PT®	25x8	$0.55 \pm 0.055 \text{ N}\cdot\text{m}$	
(4) DELTA PT®	25x10	$0.85 \pm 0.085 \text{ N}\cdot\text{m}$	
(5) B1 tapping screw	φ2.6x10 or φ2.6x12	$0.85 \pm 0.085 \text{ N}\cdot\text{m}$	

RECOMMENDED OPERATING CONDITIONS

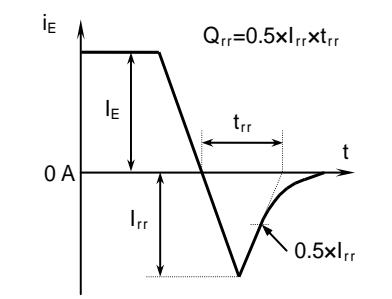
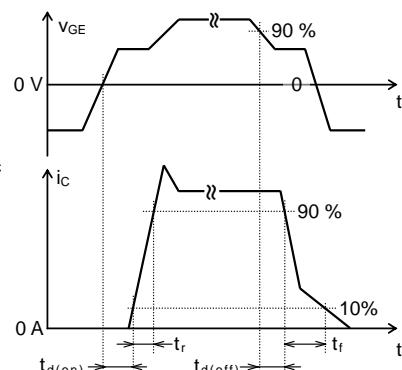
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across C-E terminals	-	1000	1200	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G-Es terminals	13.5	15.0	16.5	V
R_G	External gate resistance	-	0	-	15	Ω

CHIP LOCATION (Top view)Dimension in mm, tolerance: ± 1 mm

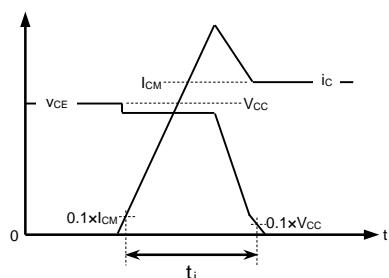
TEST CIRCUIT AND WAVEFORMS



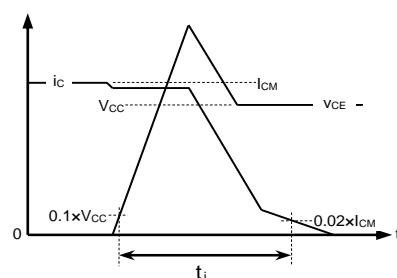
Switching characteristics test circuit and waveforms



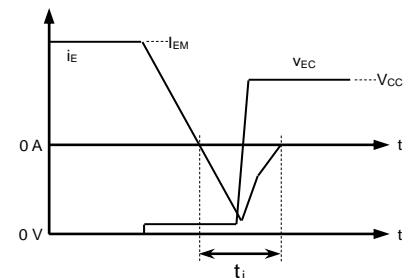
trr, Qrr characteristics test waveform



IGBT Turn-on switching energy



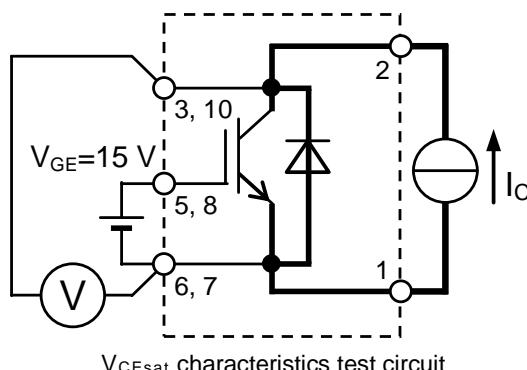
IGBT Turn-off switching energy



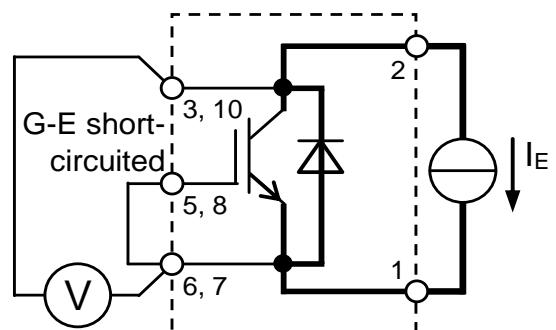
FWD Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

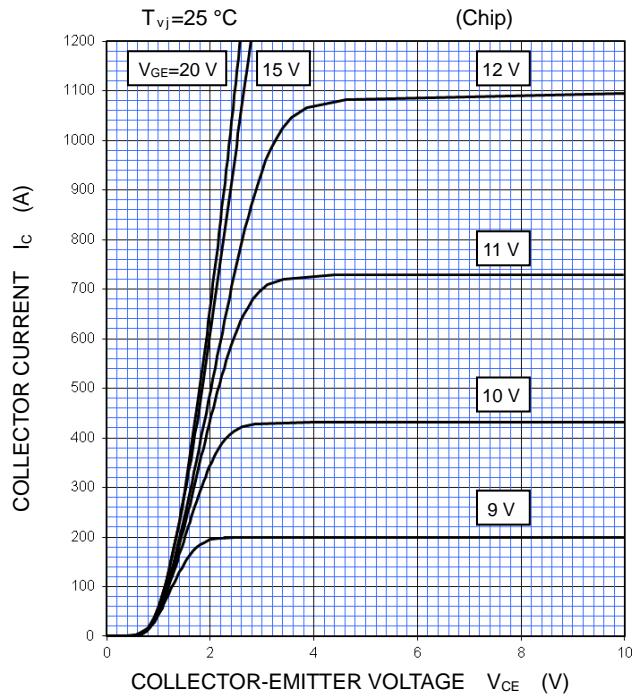
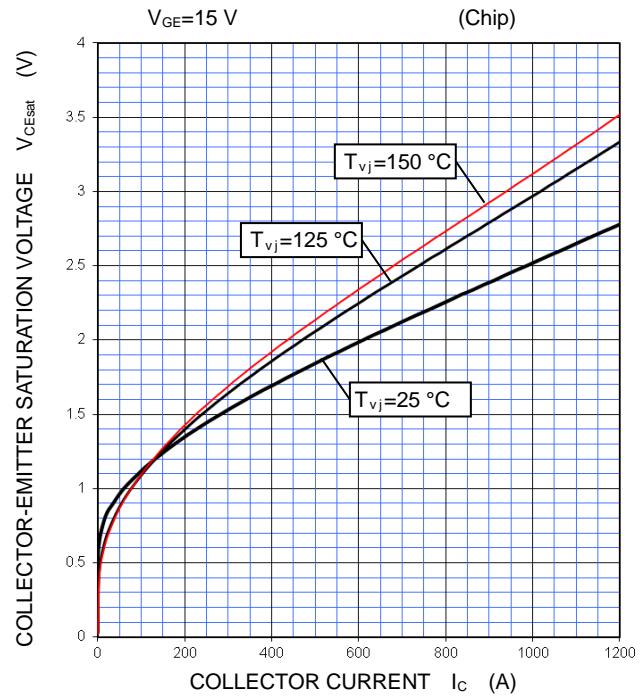
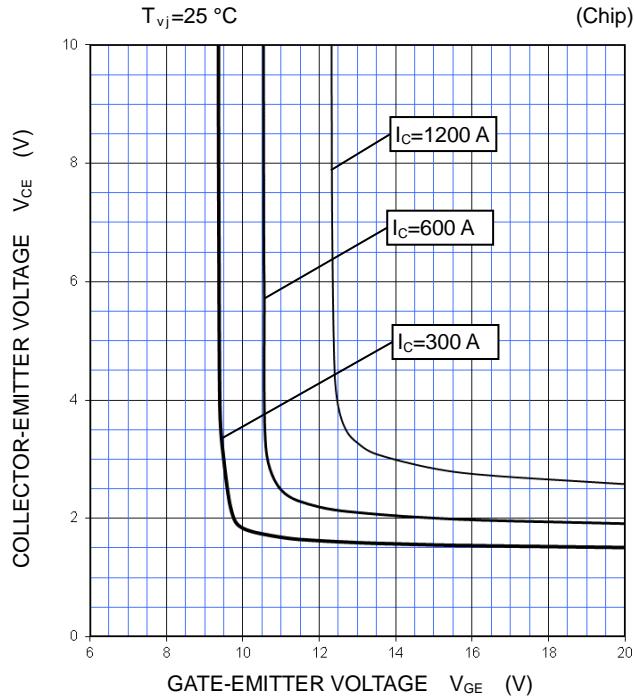
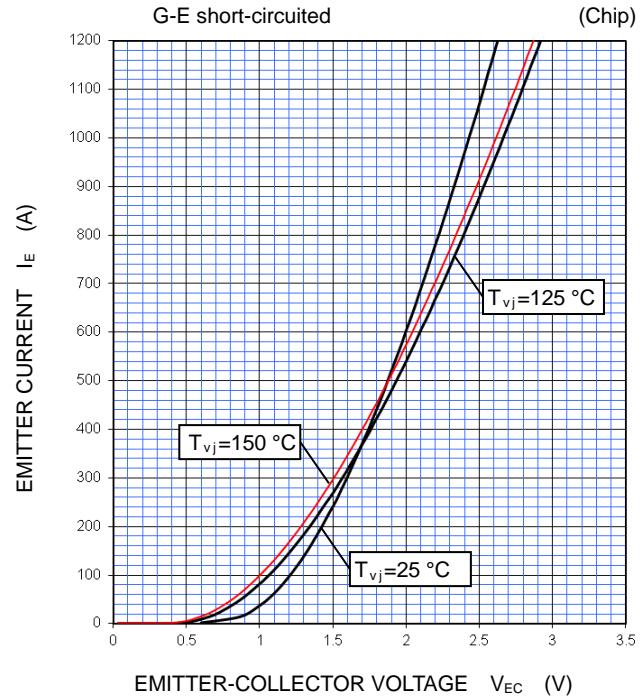
TEST CIRCUIT



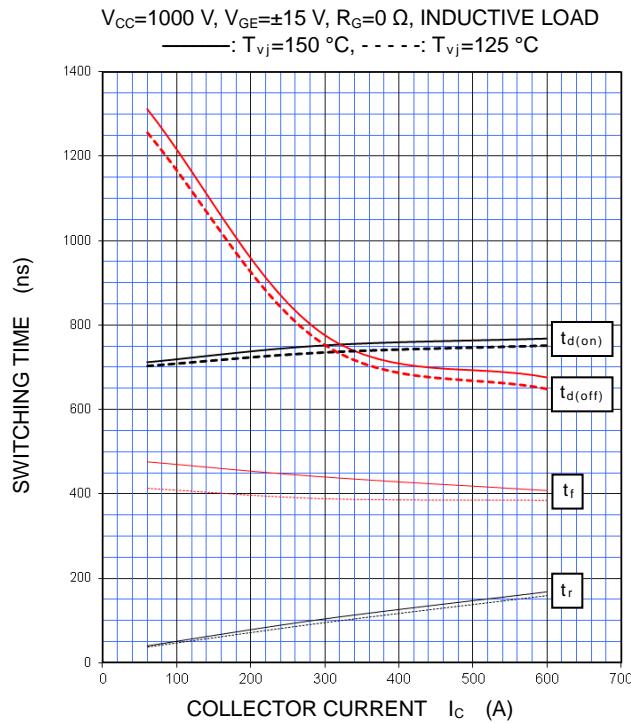
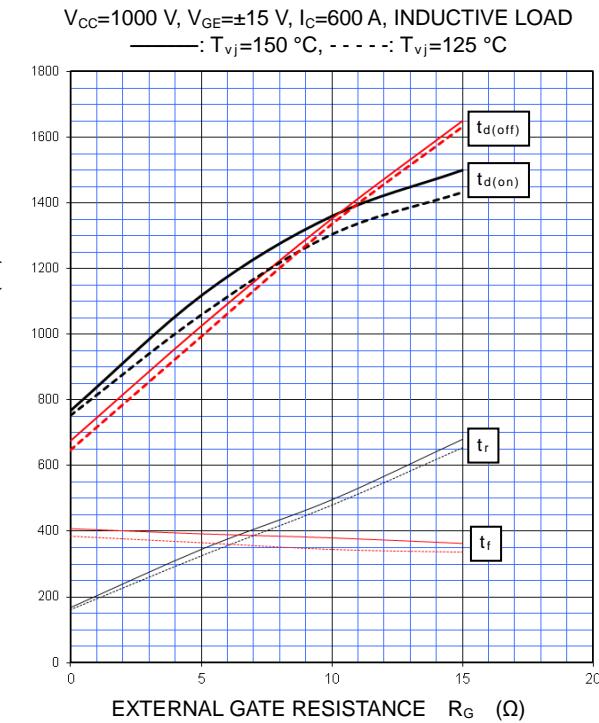
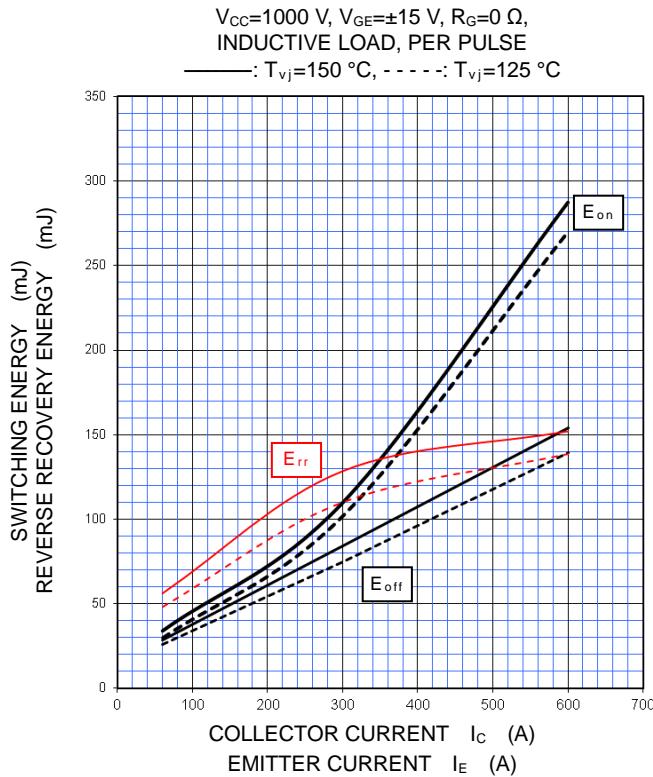
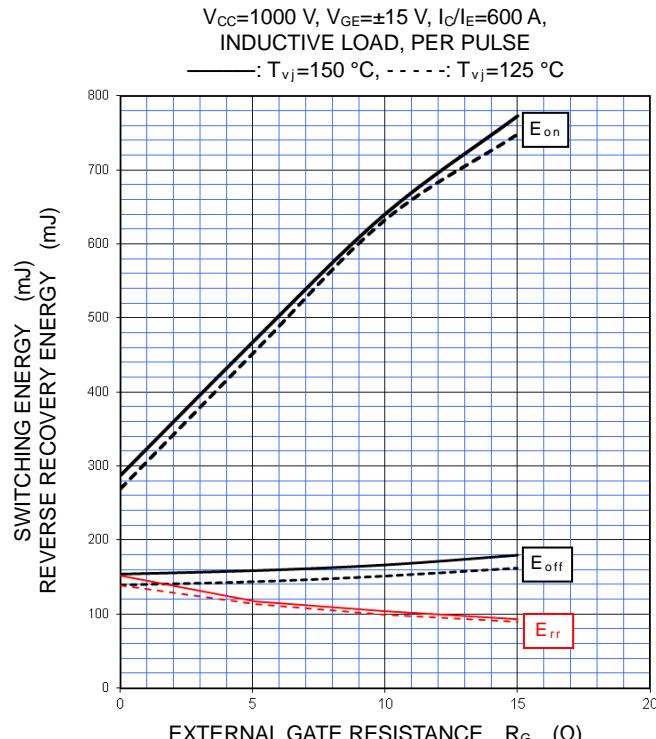
VCEsat characteristics test circuit

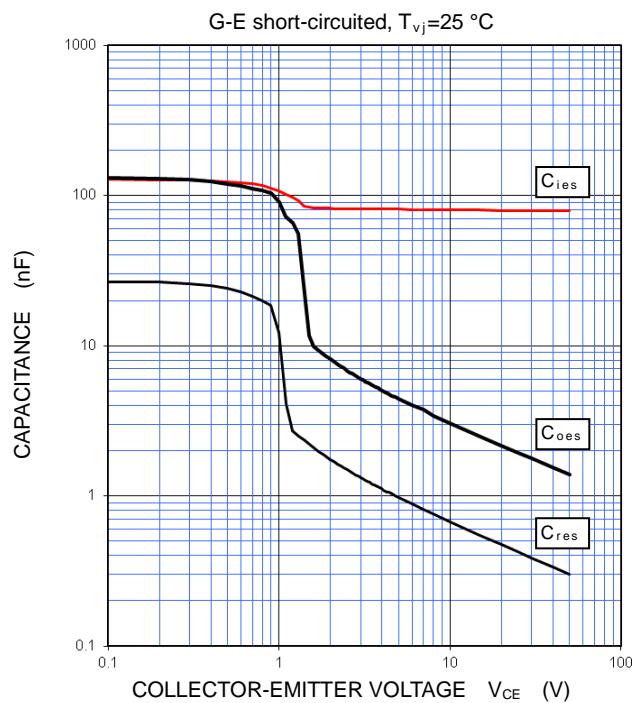
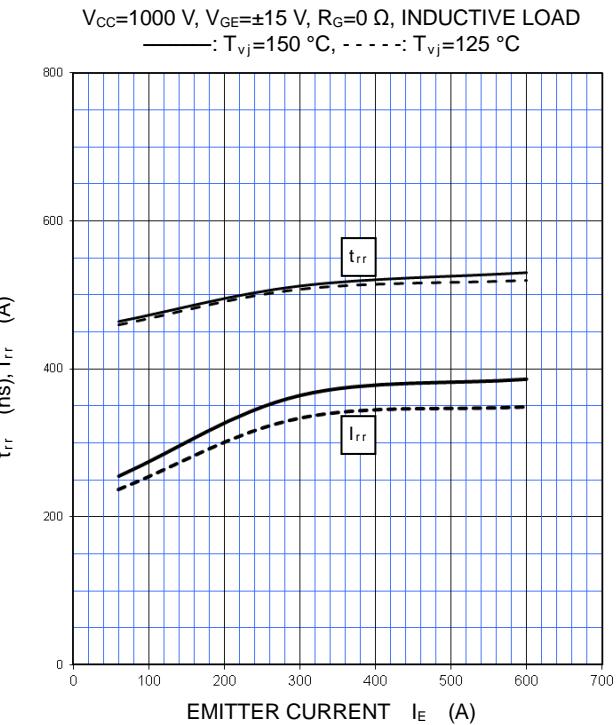
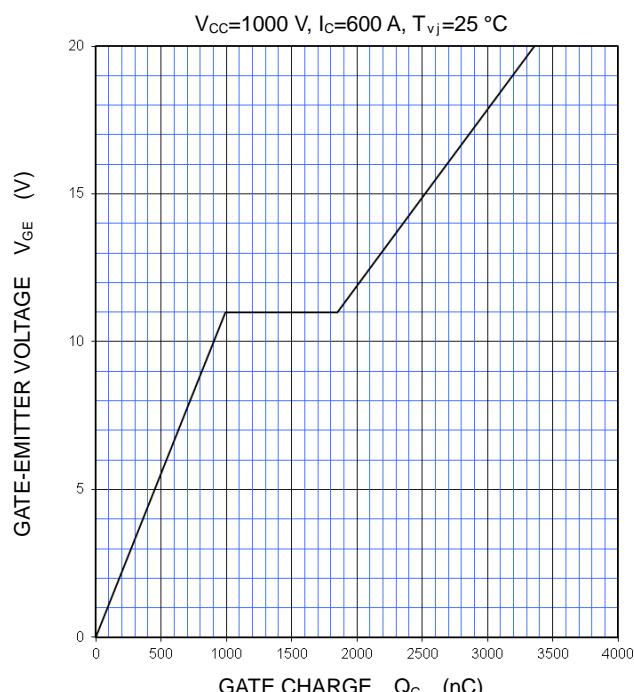
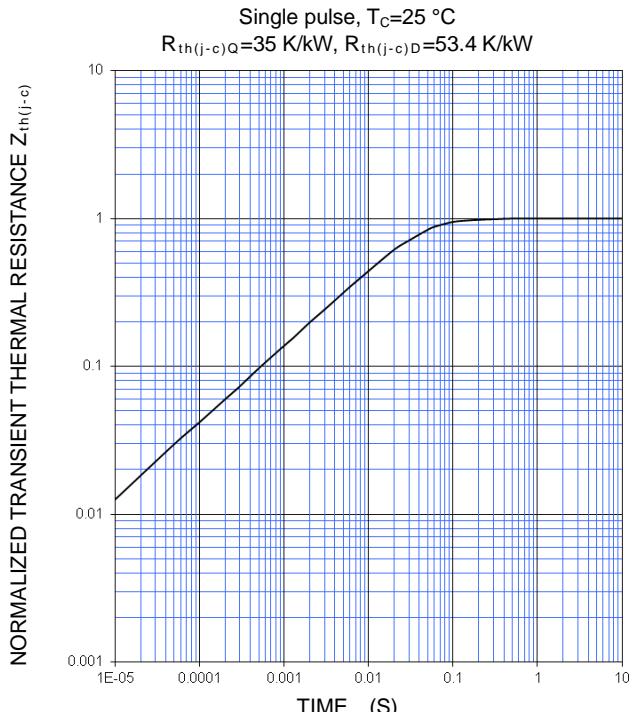


VEC characteristics test circuit

PERFORMANCE CURVES**OUTPUT CHARACTERISTICS****(TYPICAL)****COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)****COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)****FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)**

PERFORMANCE CURVES

HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)

PERFORMANCE CURVES**CAPACITANCE CHARACTERISTICS
(TYPICAL)****FREE WHEELING DIODE
REVERSE RECOVERY CHARACTERISTICS
(TYPICAL)****GATE CHARGE CHARACTERISTICS
(TYPICAL)****TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS
(MAXIMUM)**

Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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