



<IGBT Modules>

# CM1000DX-24T/CM1000DXP-24T

HIGH POWER SWITCHING USE  
INSULATED TYPE

DX		Collector current $I_C$ ..... <b>1 0 0 0 A</b> Collector-emitter voltage $V_{CES}$ ..... <b>1 2 0 0 V</b> Maximum junction temperature $T_{vjmax}$ ..... <b>1 7 5 °C</b> <ul style="list-style-type: none"><li>•Flat base type</li><li>•Copper base plate (Nickel-plating)</li><li>•RoHS Directive compliant</li><li>•Tin-plating pin terminals</li></ul>
DXP		Collector current $I_C$ ..... <b>1 0 0 0 A</b> Collector-emitter voltage $V_{CES}$ ..... <b>1 2 0 0 V</b> Maximum junction temperature $T_{vjmax}$ ..... <b>1 7 5 °C</b> <ul style="list-style-type: none"><li>•Flat base type</li><li>•Copper base plate (Nickel-plating)</li><li>•RoHS Directive compliant</li><li>•Tin-plating pressfit terminals</li></ul>
dual switch (half-bridge)		•UL Recognized under UL1557, File No. E323585

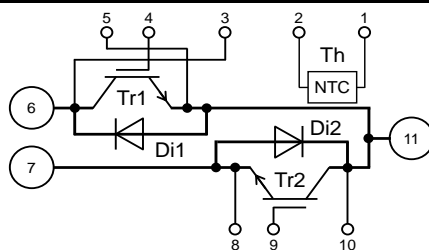
## APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

## OPTION (Below options are available.)

- PC-TIM (Phase Change Thermal Interface Material) pre-apply (Note9)
- $V_{CESat}$  selection for parallel connection

## INTERNAL CONNECTION



## TERMINAL CODE

1. TH1	6. C1
2. TH2	7. E2
3. Cs1	8. Es2
4. G1	9. G2
5. Es1	10. Cs2
	11. C2E1

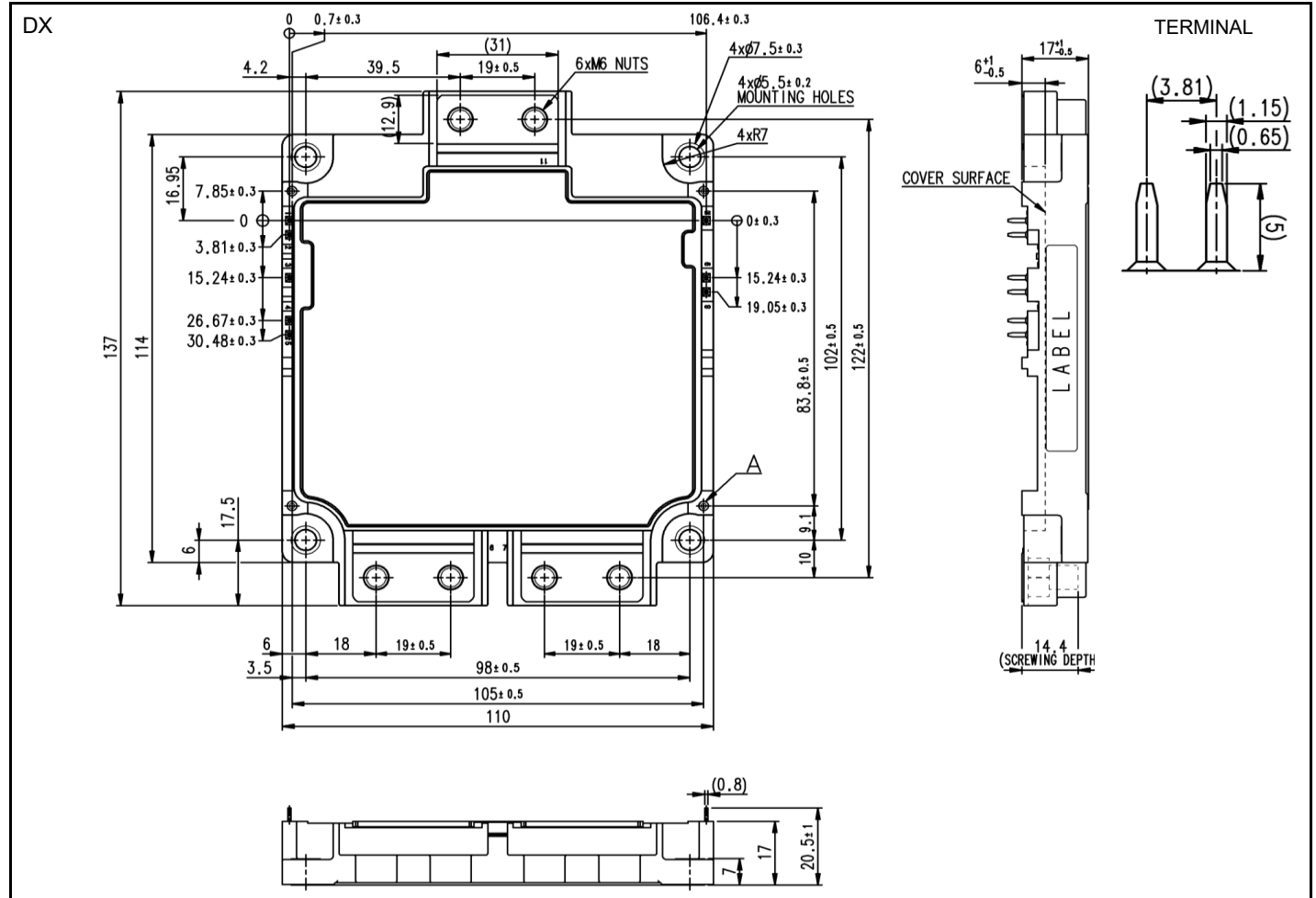
# CM1000DX-24T/CM1000DXP-24T

HIGH POWER SWITCHING USE

INSULATED TYPE

## OUTLINE DRAWING

Dimension in mm



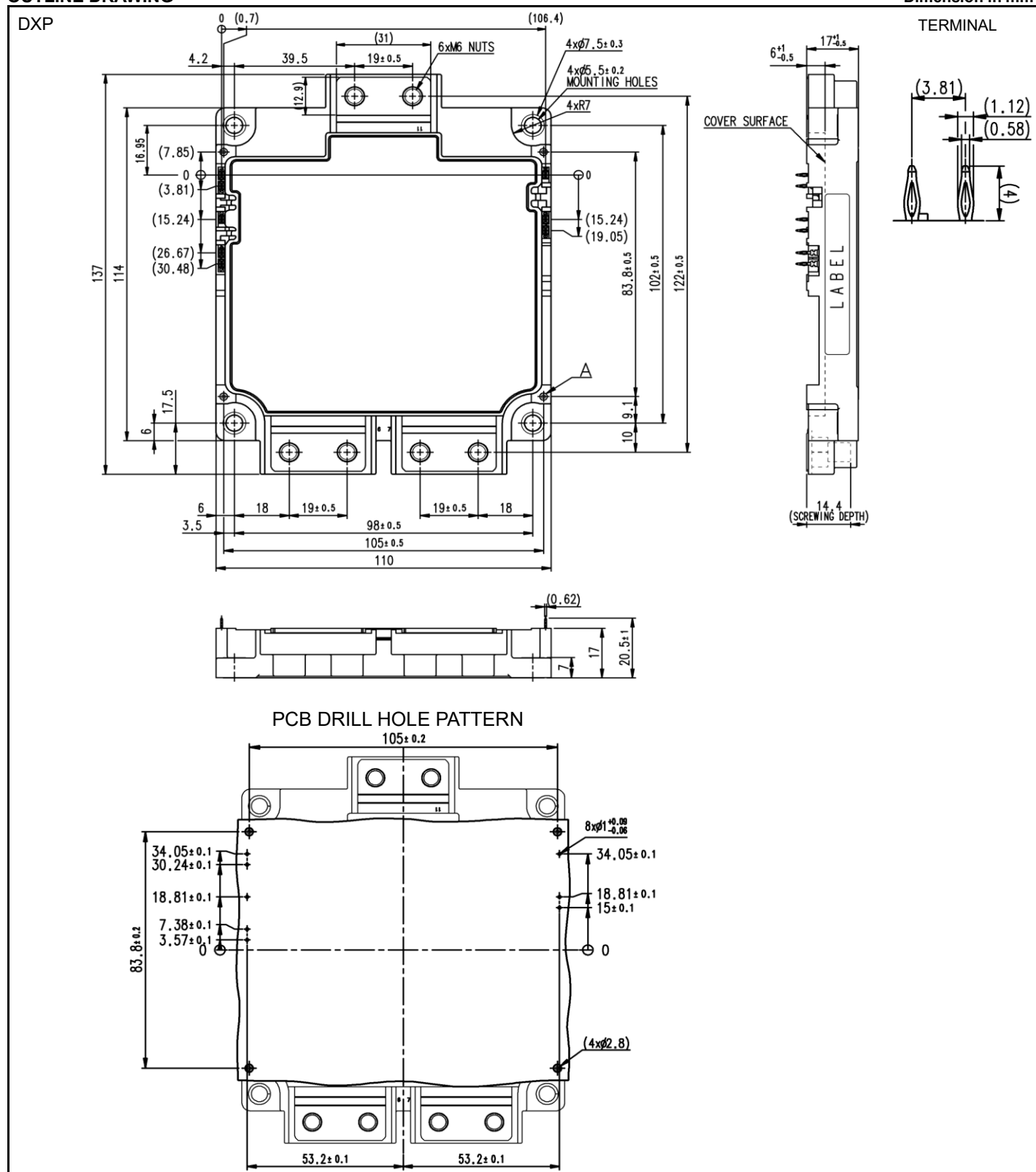
## CM1000DX-24T/CM1000DXP-24T

HIGH POWER SWITCHING USE

INSULATED TYPE

## OUTLINE DRAWING

Dimension in mm



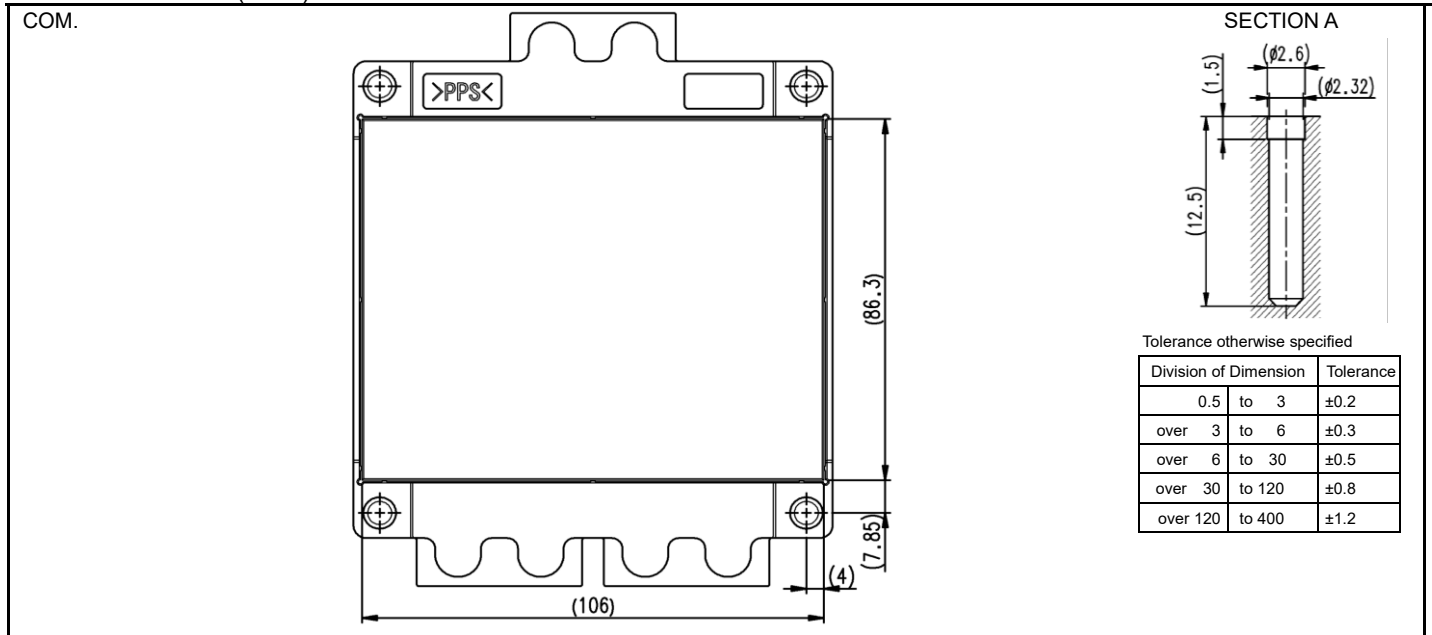
**CM1000DX-24T/CM1000DXP-24T**

HIGH POWER SWITCHING USE

INSULATED TYPE

## OUTLINE DRAWING(Cont.)

Dimension in mm

**MAXIMUM RATINGS ( $T_{vj}=25\text{ }^{\circ}\text{C}$ , unless otherwise specified)****INVERTER PART IGBT/FWD**

Symbol	Item	Conditions	Rating	Unit
$V_{CES}$	Collector-emitter voltage	G-E short-circuited	1200	V
$V_{GES}$	Gate-emitter voltage	C-E short-circuited	± 20	V
$I_C$	Collector current	DC, $T_C=116\text{ }^{\circ}\text{C}$ (Note2, 4)	1000	A
$I_{CRM}$		Pulse, Repetitive (Note3)	2000	
$P_{tot}$	Total power dissipation	$T_C=25\text{ }^{\circ}\text{C}$ (Note2, 4)	5355	W
$I_E$ (Note1)	Emitter current	DC (Note2)	1000	A
$I_{ERM}$ (Note1)		Pulse, Repetitive (Note3)	2000	

**MODULE**

Symbol	Item	Conditions	Rating	Unit
$V_{isol}$	Isolation voltage	Terminals to base plate, RMS, $f=60\text{ Hz}$ , AC 1 min	2500	V
$T_{vjmax}$	Maximum junction temperature	Instantaneous event (overload) (Note9)	175	$^{\circ}\text{C}$
$T_{Cmax}$	Maximum case temperature	(Note4, 9)	125	
$T_{vjop}$	Operating junction temperature	Continuous operation (under switching) (Note9)	-40 ~ +150	$^{\circ}\text{C}$
$T_{stg}$	Storage temperature	-	-40 ~ +125	

## CM1000DX-24T/CM1000DXP-24T

HIGH POWER SWITCHING USE  
INSULATED TYPEELECTRICAL CHARACTERISTICS ( $T_{vj}=25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

## INVERTER PART IGBT/FWD

INVERTER PART 1C217A WD

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	I <sub>C</sub> =100 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
V <sub>CEsat</sub> (Terminal)	Collector-emitter saturation voltage	I <sub>C</sub> =1000 A, V <sub>GE</sub> =15 V, Refer to the figure of test circuit (Note5)	T <sub>vj</sub> =25 °C	-	1.55	1.95	V
			T <sub>vj</sub> =125 °C	-	1.70	-	
			T <sub>vj</sub> =150 °C	-	1.75	-	
V <sub>CEsat</sub> (Chip)		I <sub>C</sub> =1000 A, V <sub>GE</sub> =15 V, (Note5)	T <sub>vj</sub> =25 °C	-	1.50	1.75	V
			T <sub>vj</sub> =125 °C	-	1.70	-	
			T <sub>vj</sub> =150 °C	-	1.75	-	
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	242.5	nF
C <sub>oes</sub>	Output capacitance			-	-	6.8	
C <sub>res</sub>	Reverse transfer capacitance			-	-	3.0	
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =1000 A, V <sub>GE</sub> =15 V		-	7.5	-	μC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =600 V, I <sub>C</sub> =1000 A, V <sub>GE</sub> =±15 V, R <sub>G</sub> =2.0 Ω, Inductive load		-	-	800	ns
t <sub>r</sub>	Rise time			-	-	400	
t <sub>d(off)</sub>	Turn-off delay time			-	-	1300	
t <sub>f</sub>	Fall time			-	-	400	
V <sub>EC</sub> (Note1) (Terminal)	Emitter-collector voltage	I <sub>E</sub> =1000 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T <sub>vj</sub> =25 °C	-	1.65	2.15	V
			T <sub>vj</sub> =125 °C	-	1.75	-	
			T <sub>vj</sub> =150 °C	-	1.80	-	
V <sub>EC</sub> (Note1) (Chip)		I <sub>E</sub> =1000 A, G-E short-circuited, (Note5)	T <sub>vj</sub> =25 °C	-	1.60	1.95	V
			T <sub>vj</sub> =125 °C	-	1.60	-	
			T <sub>vj</sub> =150 °C	-	1.60	-	
t <sub>rr</sub> (Note1)	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>E</sub> =1000 A, V <sub>GE</sub> =±15 V, R <sub>G</sub> =2.0 Ω, Inductive load		-	-	500	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge			-	78	-	μC
E <sub>on</sub>	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =1000 A,		-	150.5	-	mJ
E <sub>off</sub>	Turn-off switching energy per pulse	V <sub>GE</sub> =±15 V, R <sub>G</sub> =2.0 Ω, T <sub>vj</sub> =150 °C,		-	128.4	-	
E <sub>rr</sub> (Note1)	Reverse recovery energy per pulse	Inductive load		-	69	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =25 °C (Note4)		-	0.5	-	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	0.4	-	Ω

## NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{25}$	Zero-power resistance	$T_C=25\text{ }^{\circ}\text{C}$ (Note4)	4.85	5.00	5.15	k $\Omega$
$\Delta R/R$	Deviation of resistance	$R_{100}=493\text{ }\Omega$ , $T_C=100\text{ }^{\circ}\text{C}$ (Note4)	-7.3	-	+7.8	%
$B_{(25/50)}$	B-constant	Approximate by equation (Note6)	-	3375	-	K
$P_{25}$	Power dissipation	$T_C=25\text{ }^{\circ}\text{C}$ (Note4)	-	-	10	mW

## THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	28	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)	-	-	49	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7, 9)	-	7.1	-	K/kW

## CM1000DX-24T/CM1000DXP-24T

HIGH POWER SWITCHING USE  
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## MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
M <sub>t</sub>	Mounting torque	Main terminals M 6 screw		3.5	4.0	4.5	N·m
M <sub>s</sub>	Mounting torque	Mounting to heat sink M 5 screw		2.5	3.0	3.5	N·m
d <sub>s</sub>	Creepage distance	Solder pin type (DX)	Terminal to terminal	17.3	-	-	mm
			Terminal to base plate	17.5	-	-	
		Pressfit pin type (DXP)	Terminal to terminal	16.5	-	-	mm
			Terminal to base plate	18.0	-	-	
d <sub>a</sub>	Clearance	Solder pin type (DX)	Terminal to terminal	10.3	-	-	mm
			Terminal to base plate	11.7	-	-	
		Pressfit pin type (DXP)	Terminal to terminal	10.2	-	-	mm
			Terminal to base plate	11.8	-	-	
e <sub>c</sub>	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+200	μm
m	mass	-		-	490	-	g

\*. This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

- Junction temperature ( $T_{vj}$ ) should not increase beyond  $T_{vj\max}$  rating.
- Pulse width and repetition rate should be such that the device junction temperature ( $T_{vj}$ ) dose not exceed  $T_{vj\max}$  rating.
- Case temperature ( $T_c$ ) and heat sink temperature ( $T_s$ ) are defined on the each surface (mounting side) of base plate and heat sink just under the chips.  
Refer to the figure of chip location.
- Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

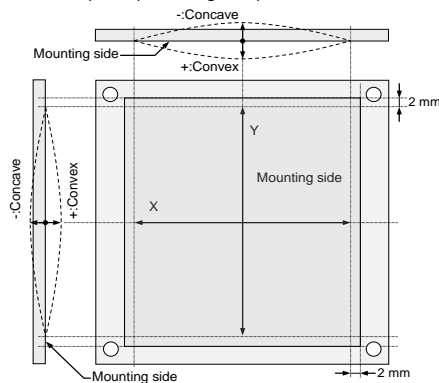
$$6. B_{(25/50)} = \ln\left(\frac{R_{25}}{R_{50}}\right) / \left(\frac{1}{T_{25}} - \frac{1}{T_{50}}\right)$$

$R_{25}$ : resistance at absolute temperature  $T_{25}$  [K];  $T_{25}=25\text{ [°C]}+273.15=298.15$  [K]

$R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}=50\text{ [°C]}+273.15=323.15$  [K]

7. Typical value is by thermally conductive grease of  $\lambda=0.9\text{ W/(m·K)}/D_{(C-S)}=50\text{ }\mu\text{m}$ .

8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



- Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition ( $T_{vj\max}$ ,  $T_{vj\text{op}}$ ,  $T_{c\max}$ ) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

**CM1000DX-24T/CM1000DXP-24T**

HIGH POWER SWITCHING USE

INSULATED TYPE

Note11. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness : t1.6

Type	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1) PT®	EJOT	K25×8	0.55 ± 0.055	by handwork (equivalent to 30 rpm by mechanical screw driver) ~ 600 rpm (by mechanical screw driver)
(2) PT®		K25×10	0.75 ± 0.075 N·m	
(3) DELTA PT®		25×8	0.55 ± 0.055 N·m	
(4) DELTA PT®		25×10	0.75 ± 0.075 N·m	
(5) B1 tapping screw	-	φ2.6×10 φ2.6×12	0.75 ± 0.075 N·m	

**RECOMMENDED OPERATING CONDITIONS**

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{CC}$	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
$V_{GEon}$	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
$R_G$	External gate resistance	Per switch	2.0	-	20	Ω

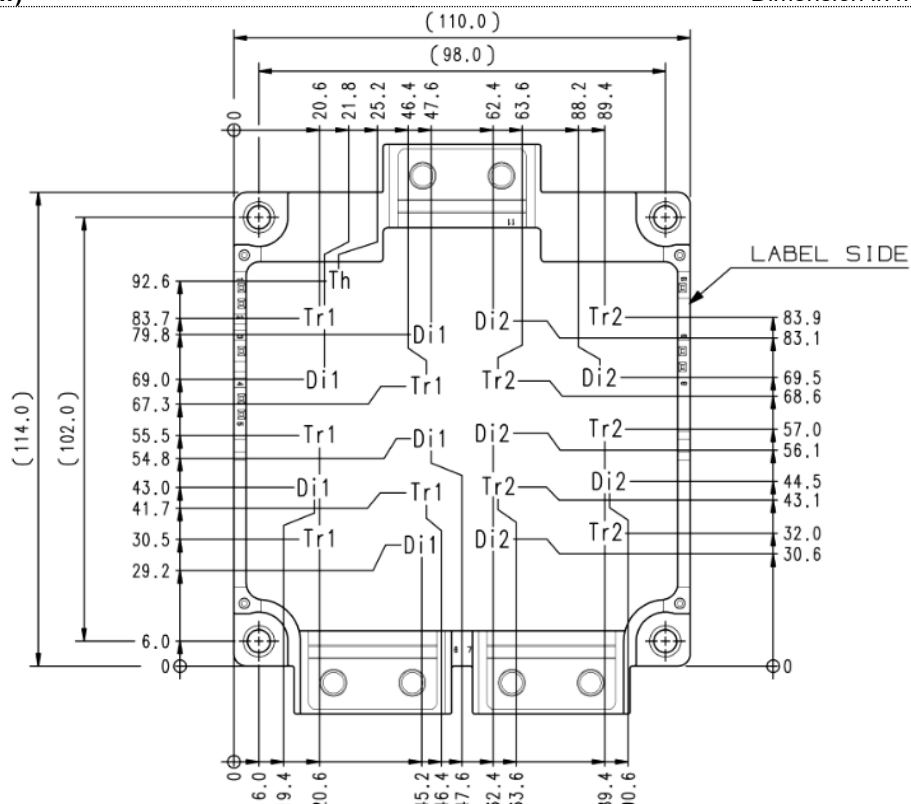
**CM1000DX-24T/CM1000DXP-24T**

HIGH POWER SWITCHING USE

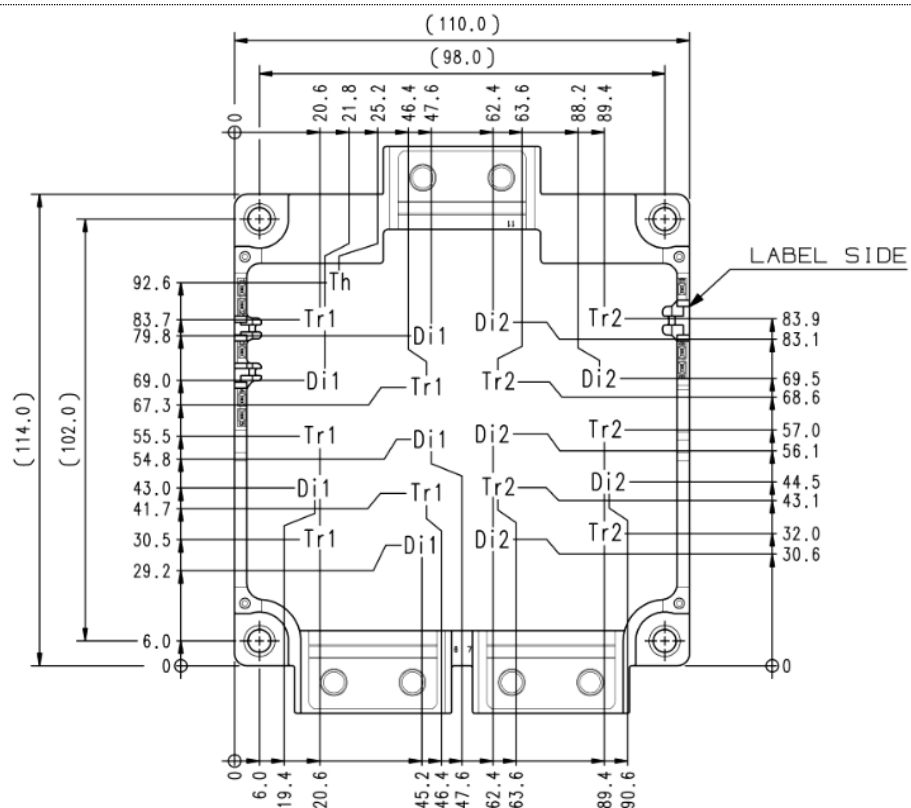
INSULATED TYPE

**CHIP LOCATION (Top view)**Dimension in mm, tolerance:  $\pm 1$  mm

DX

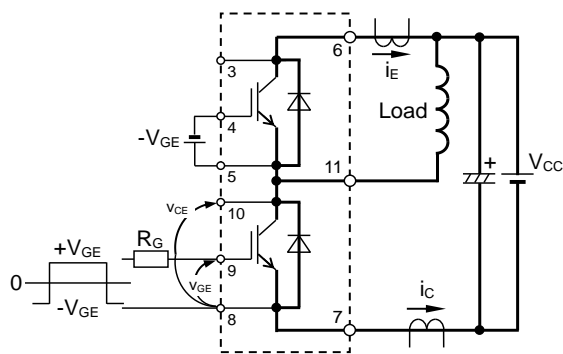


DXP

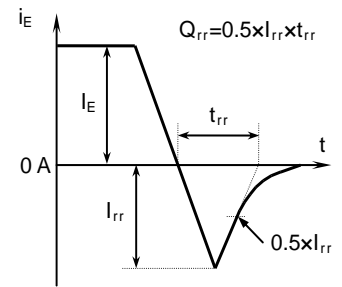
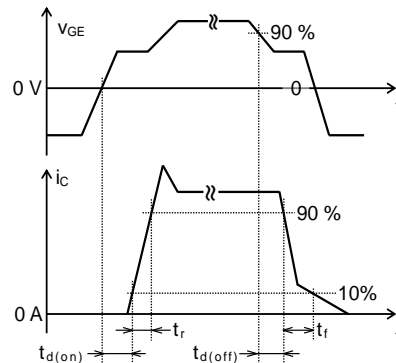
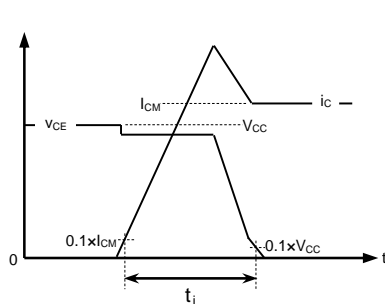


Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

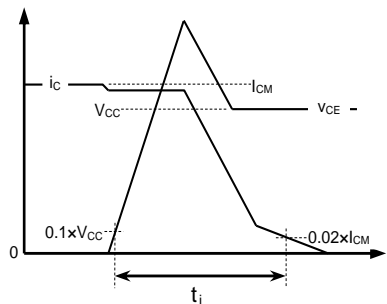
## TEST CIRCUIT AND WAVEFORMS



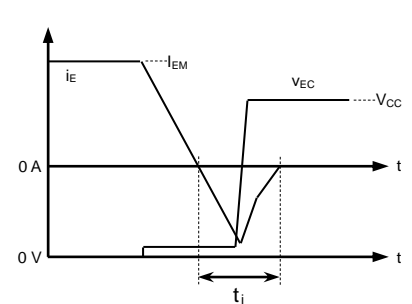
Switching characteristics test circuit and waveforms

 $t_{rr}$ ,  $Q_{rr}$  characteristics test waveform

IGBT Turn-on switching energy



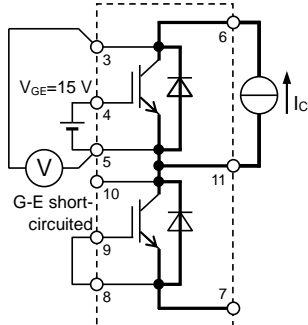
IGBT Turn-off switching energy



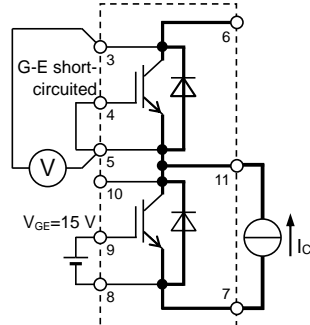
FWD Reverse recovery energy

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

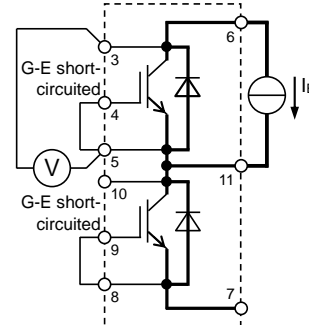
## TEST CIRCUIT



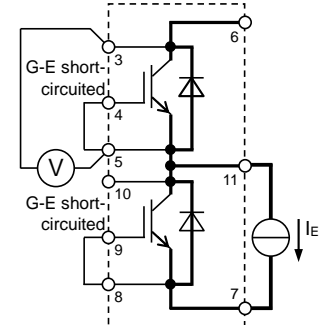
Tr1

 $V_{CEsat}$  characteristics test circuit

Tr2



Di1

 $V_{EC}$  characteristics test circuit

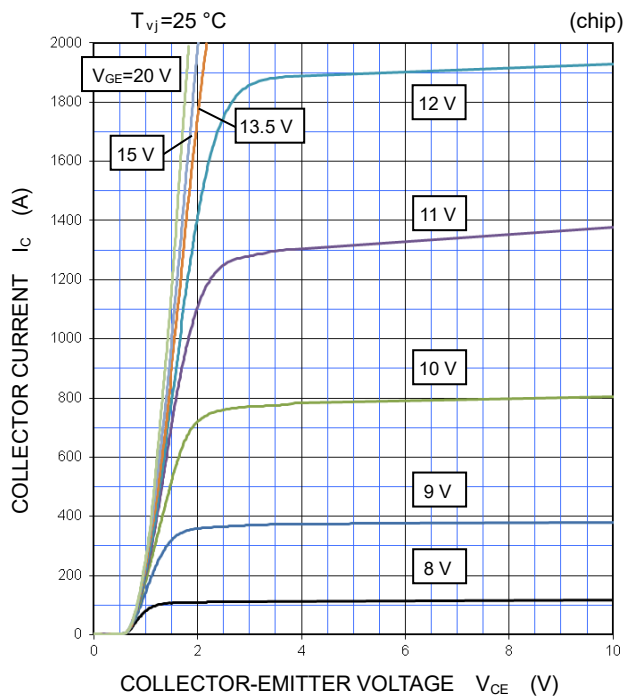
Di2

**CM1000DX-24T/CM1000DXP-24T**

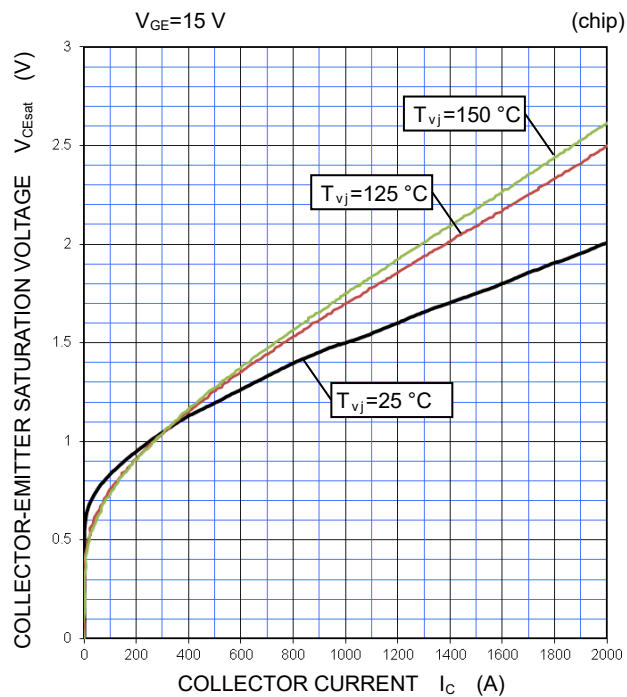
HIGH POWER SWITCHING USE  
INSULATED TYPE

**PERFORMANCE CURVES****INVERTER PART**

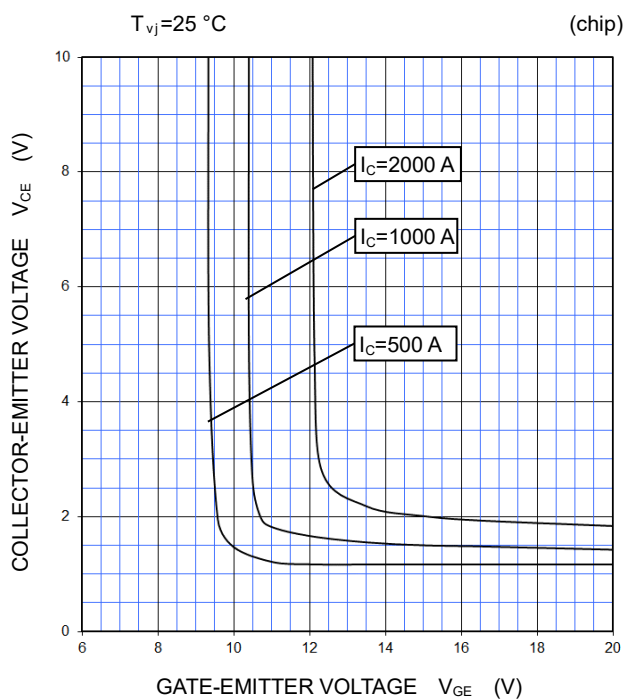
**OUTPUT CHARACTERISTICS  
(TYPICAL)**



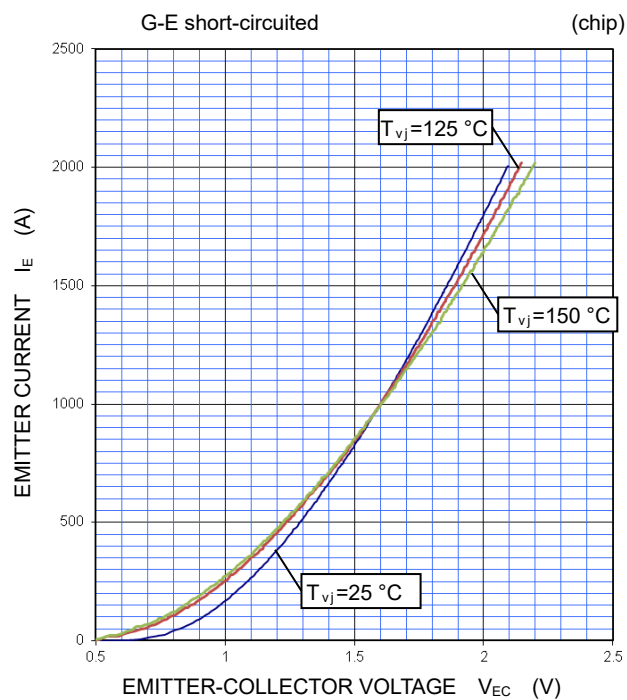
**COLLECTOR-EMITTER SATURATION VOLTAGE  
CHARACTERISTICS  
(TYPICAL)**



**COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS  
(TYPICAL)**



**FREE WHEELING DIODE  
FORWARD CHARACTERISTICS  
(TYPICAL)**

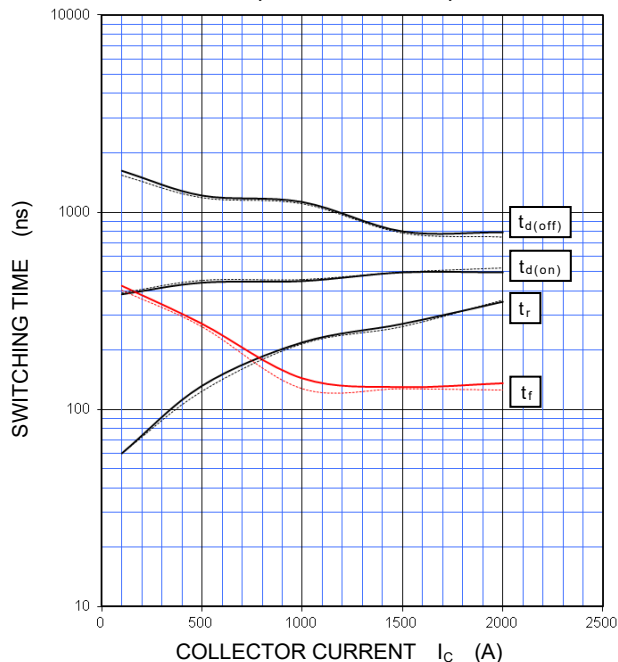


## PERFORMANCE CURVES

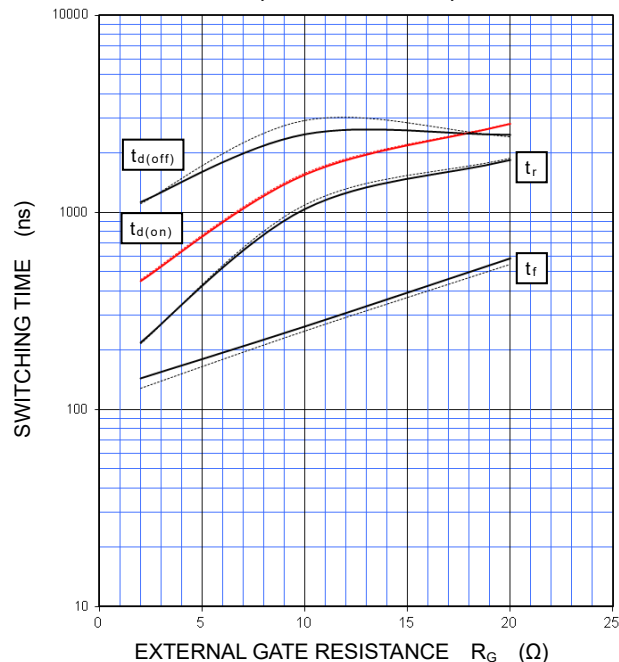
## INVERTER PART

HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)

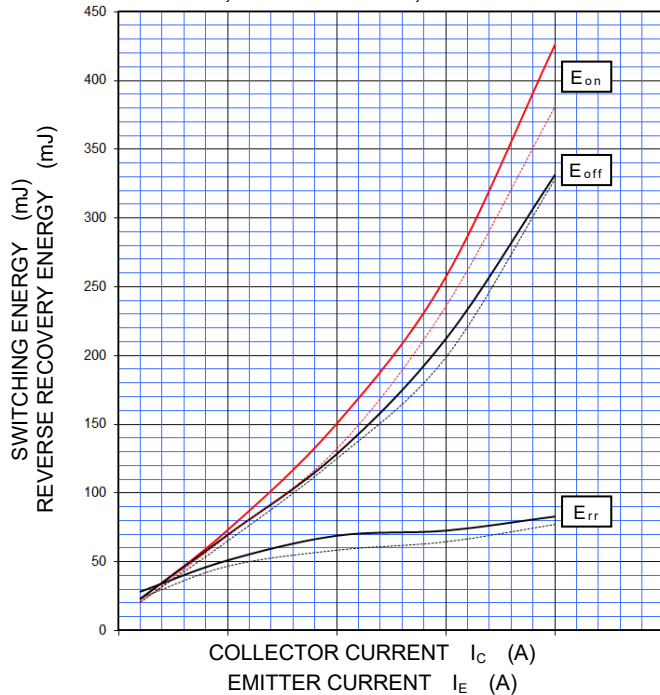
$V_{CC}=600\text{ V}$ ,  $R_G=2.0\ \Omega$ ,  $V_{GE}=\pm 15\text{ V}$ , INDUCTIVE LOAD  
 —:  $T_{vj}=150\text{ }^\circ\text{C}$ , - - - - -:  $T_{vj}=125\text{ }^\circ\text{C}$

HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)

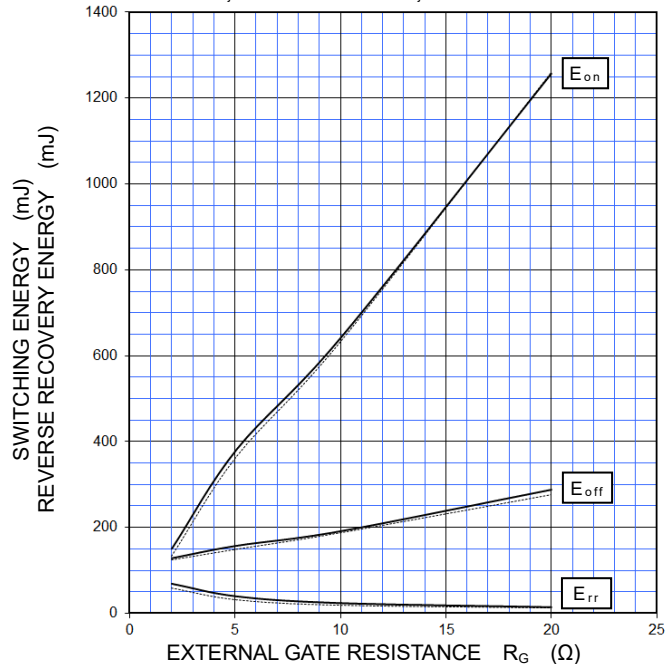
$V_{CC}=600\text{ V}$ ,  $I_C=1000\text{ A}$ ,  $V_{GE}=\pm 15\text{ V}$ , INDUCTIVE LOAD  
 —:  $T_{vj}=150\text{ }^\circ\text{C}$ , - - - - -:  $T_{vj}=125\text{ }^\circ\text{C}$

HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)

$V_{CC}=600\text{ V}$ ,  $R_G=2.0\ \Omega$ ,  $V_{GE}=\pm 15\text{ V}$ , INDUCTIVE LOAD,  
 —:  $T_{vj}=150\text{ }^\circ\text{C}$ , - - - - -:  $T_{vj}=125\text{ }^\circ\text{C}$ , PER PULSE

HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)

$V_{CC}=600\text{ V}$ ,  $I_C/I_E=1000\text{ A}$ ,  $V_{GE}=\pm 15\text{ V}$ , INDUCTIVE LOAD,  
 —:  $T_{vj}=150\text{ }^\circ\text{C}$ , - - - - -:  $T_{vj}=125\text{ }^\circ\text{C}$ , PER PULSE

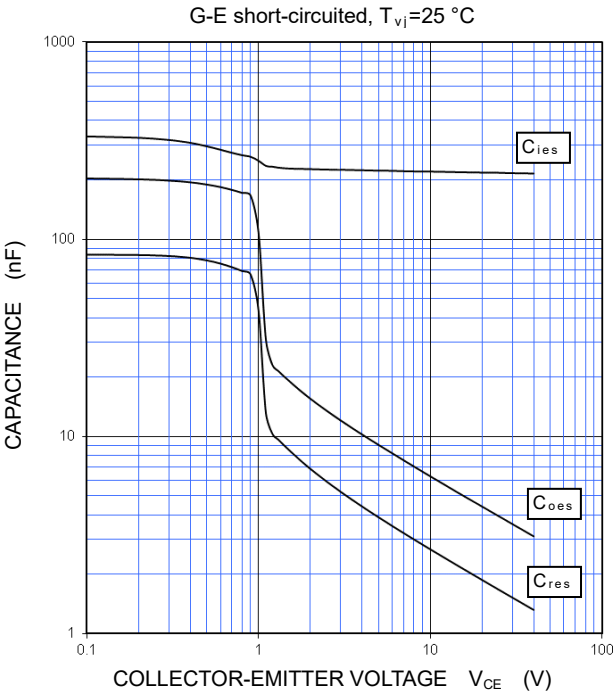


<IGBT Modules>  
**CM1000DX-24T/CM1000DXP-24T**  
HIGH POWER SWITCHING USE  
INSULATED TYPE

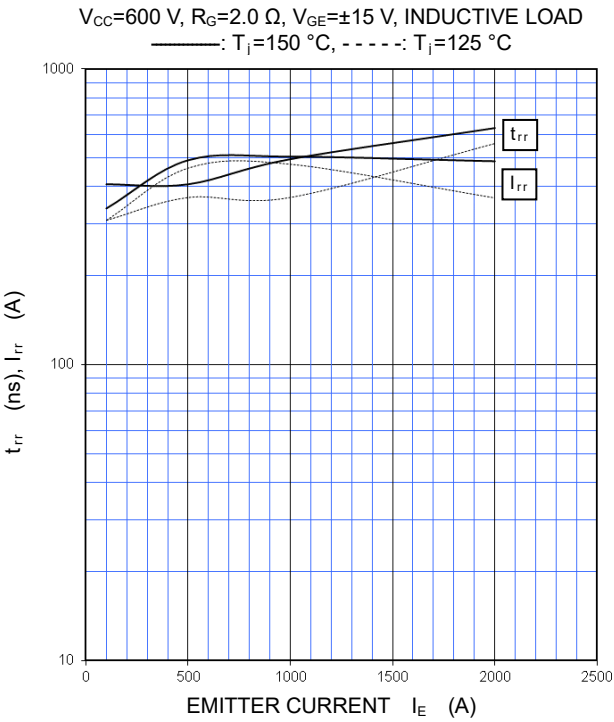
PERFORMANCE CURVES

INVERTER PART

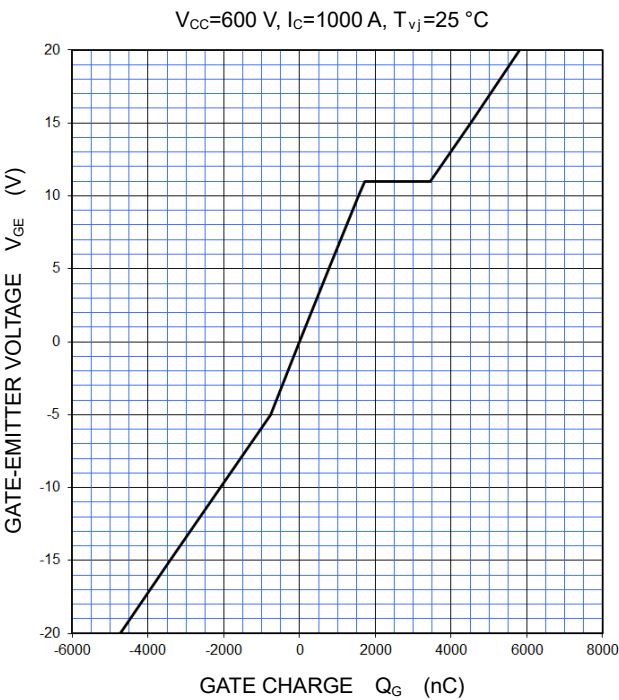
CAPACITANCE CHARACTERISTICS  
(TYPICAL)



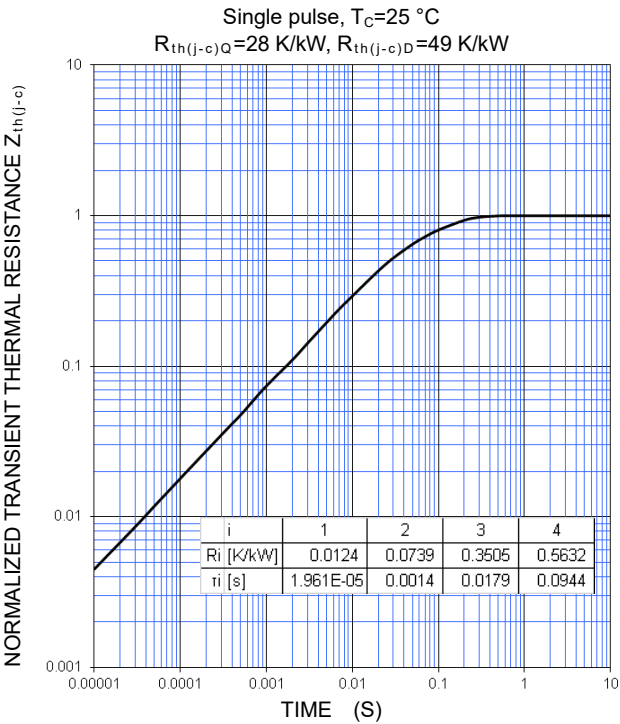
FREE WHEELING DIODE  
REVERSE RECOVERY CHARACTERISTICS  
(TYPICAL)



GATE CHARGE CHARACTERISTICS  
(TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS  
(MAXIMUM)



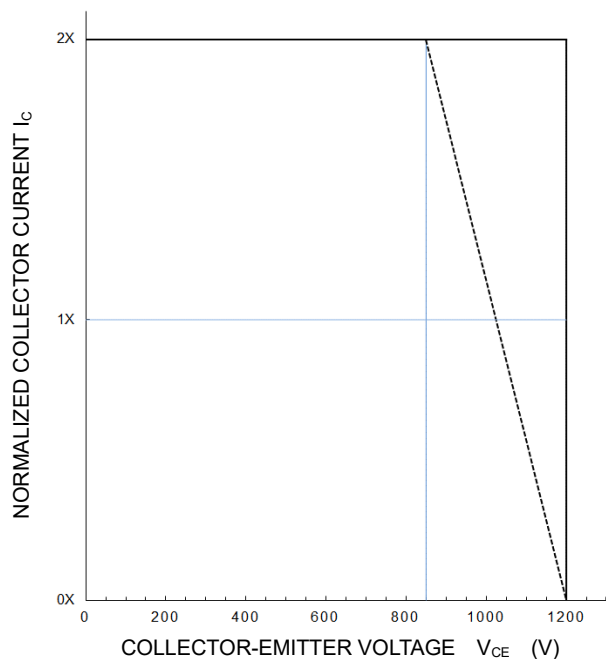
**CM1000DX-24T/CM1000DXP-24T**

HIGH POWER SWITCHING USE

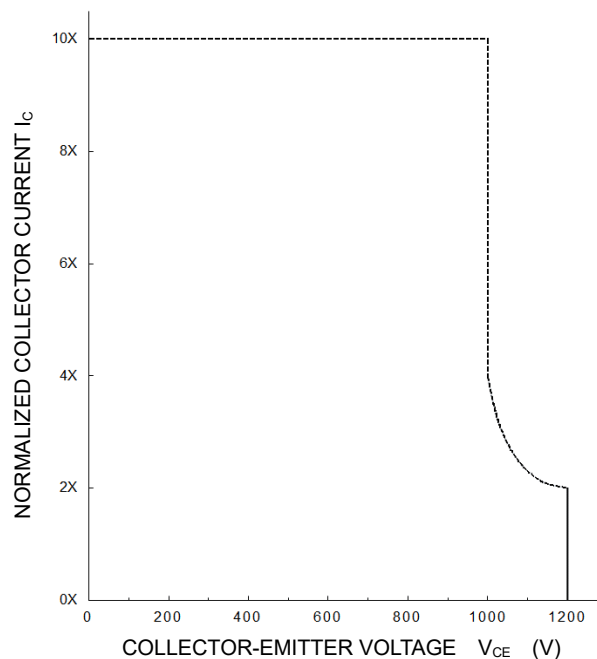
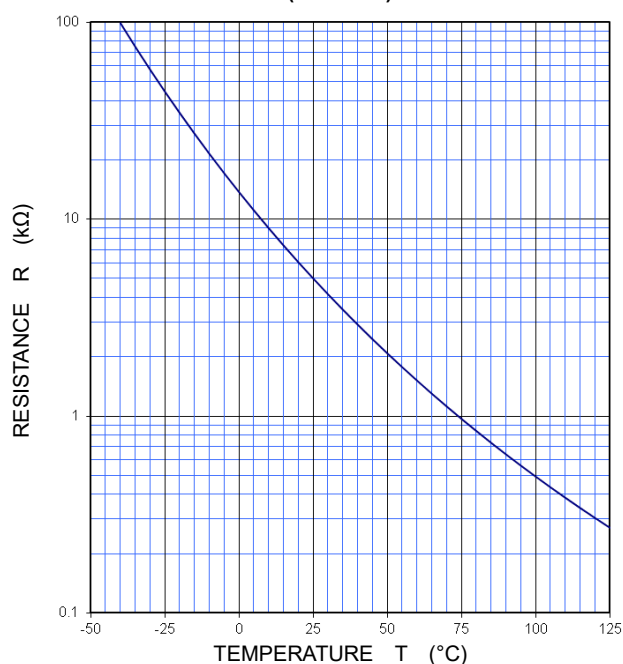
INSULATED TYPE

**PERFORMANCE CURVES****INVERTER PART****TURN-OFF SWITCHING SAFE OPERATING AREA  
(REVERSE BIAS SAFE OPERATING AREA)  
(MAXIMUM)**

$V_{CC} \leq 850 \text{ V}$ ,  $R_G = 2.0 \sim 20 \ \Omega$ ,  $V_{GE} = \pm 15 \text{ V}$ ,  
 ———:  $T_{vj} = 25 \sim 150 \text{ }^\circ\text{C}$  (Normal load operations (Continuous))  
 - - - - -:  $T_{vj} = 175 \text{ }^\circ\text{C}$  (Unusual load operations (Limited period))

**SHORT-CIRCUIT SAFE OPERATING AREA  
(MAXIMUM)**

$V_{CC} \leq 800 \text{ V}$ ,  $R_G = 2.0 \sim 20 \ \Omega$ ,  $V_{GE} = \pm 15 \text{ V}$ ,  
 $T_{vj} = 25 \sim 150 \text{ }^\circ\text{C}$ ,  $t_W \leq 8 \ \mu\text{s}$ , Non-Repetitive

**NTC thermistor part****TEMPERATURE CHARACTERISTICS  
(TYPICAL)**

Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

## CM1000DX-24T/CM1000DXP-24T

HIGH POWER SWITCHING USE

INSULATED TYPE

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**Keep safety first in your circuit designs!**

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