

<IGBT Modules>

CM200DY-40TA

HIGH POWER SWITCHING USE
INSULATED TYPE



dual switch (half-bridge)

Collector Current I_c **2 0 0 A**
 Collector-emitter voltage V_{CES} **2 0 0 0 V**
 Maximum junction temperature T_{vjmax} **1 7 5 °C**

- Flat base type
- Nickel-plating tab terminals
- RoHS Directive compliant
- UL Recognition under 1557, File No E323585

APPLICATION

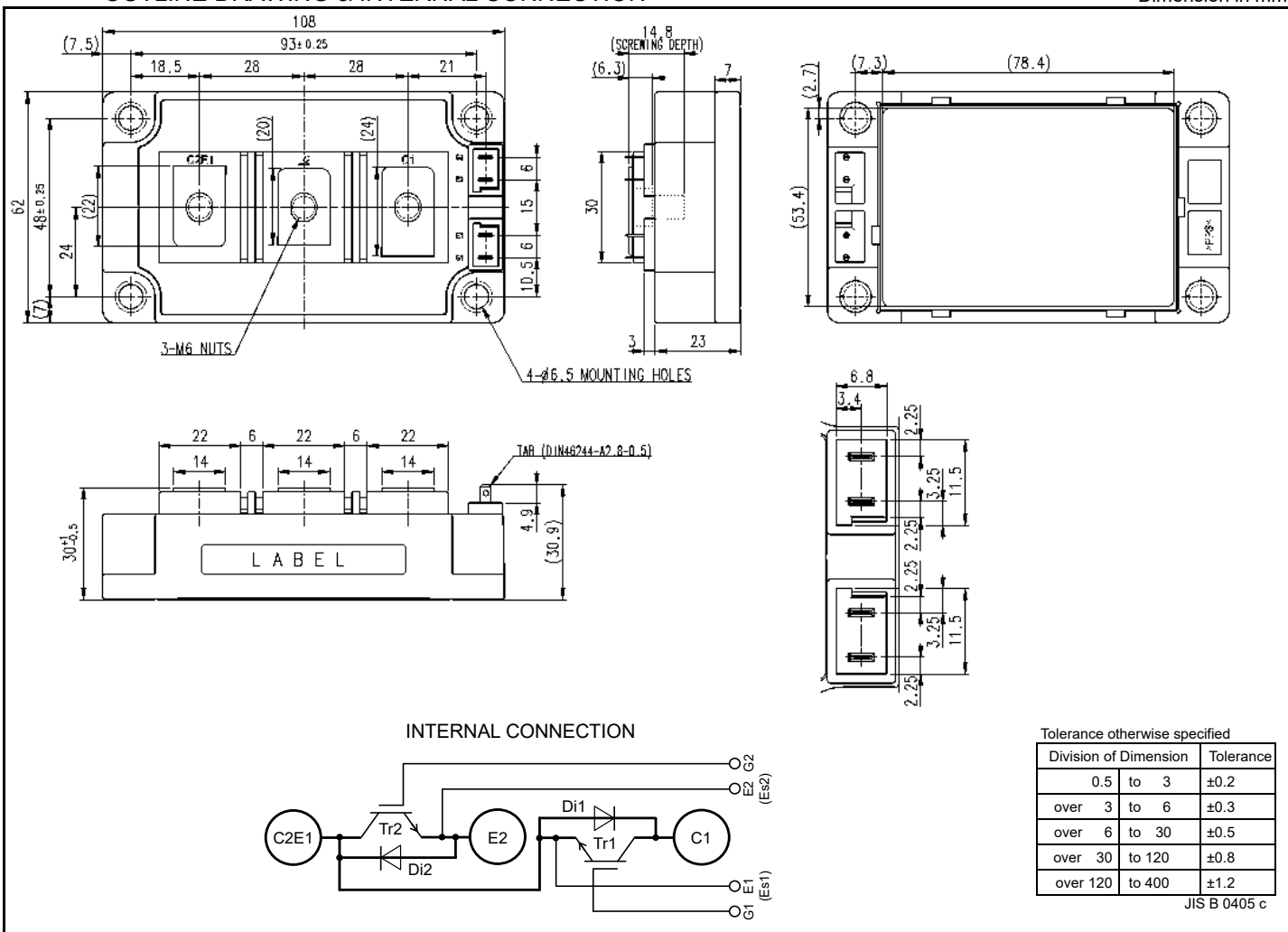
- Medium voltage drive, etc.

OPTION

- V_{CESat} selection for parallel connection
- PC-TIM (Phase Change Thermal Interface Material) pre-apply

OUTLINE DRAWING & INTERNAL CONNECTION

Dimension in mm



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MAXIMUM RATINGS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	2000	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I _C	Collector current	DC, T _C =140 °C (Note2, 4)	200	A
I _{CRM}		Pulse, Repetitive (Note3)	400	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	2170	W
I _E (Note1)	Emitter current	DC (Note2)	200	A
I _{ERM} (Note1)		Pulse, Repetitive (Note3)	400	
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note8)	175	°C
T _{Cmax}	Maximum case temperature	(Note4, 8)	150	
T _{vjop}	Operating junction temperature	Continuous operation (Note8)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +150	

ELECTRICAL CHARACTERISTICS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
V _{GE(th)}	Gate-emitter threshold voltage	I _C =20 mA, V _{CE} =10 V		5.5	6.0	6.6	V
V _{CEsat} (Terminal)	Collector-emitter saturation voltage	I _C =200 A, V _{GE} =15 V, Refer to the figure of test circuit (Note5)	T _{vj} =25 °C	-	1.80	2.10	V
			T _{vj} =125 °C	-	2.15	-	
			T _{vj} =150 °C	-	2.20	-	
V _{CEsat} (Chip)		I _C =200 A, V _{GE} =15 V, (Note5)	T _{vj} =25 °C	-	1.75	2.00	V
			T _{vj} =125 °C	-	2.10	-	
			T _{vj} =150 °C	-	2.15	-	
C _{ies}	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	55	nF
C _{oes}	Output capacitance			-	-	1.0	
C _{res}	Reverse transfer capacitance			-	-	0.4	
Q _G	Gate charge	V _{CC} =1300 V, I _C =200 A, V _{GE} =15 V		-	1.6	-	μC
t _{d(on)}	Turn-on delay time	V _{CC} =1300 V, I _C =200 A, V _{GE} =±15 V, R _G =0 Ω, Inductive load		-	-	800	ns
t _r	Rise time			-	-	150	
t _{d(off)}	Turn-off delay time			-	-	900	
t _f	Fall time			-	-	2300	
V _{EC} (Note1) (Terminal)	Emitter-collector voltage	I _E =200 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T _{vj} =25 °C	-	2.05	2.90	V
			T _{vj} =125 °C	-	2.35	-	
			T _{vj} =150 °C	-	2.35	-	
V _{EC} (Note1) (Chip)		I _E =200 A, G-E short-circuited, (Note5)	T _{vj} =25 °C	-	2.00	2.75	V
			T _{vj} =125 °C	-	2.30	-	
			T _{vj} =150 °C	-	2.30	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =1300 V, I _E =200 A, V _{GE} =±15 V, R _G =0 Ω, Inductive load		-	-	1000	ns
Q _{rr} (Note1)	Reverse recovery charge			-	52.5	-	μC
E _{on}	Turn-on switching energy per pulse	V _{CC} =1300 V, I _C =I _E =200 A, V _{GE} =±15 V, R _G =0 Ω, T _{vj} =150 °C, Inductive load		-	97.1	-	mJ
E _{off}	Turn-off switching energy per pulse			-	163.2	-	
E _{rr} (Note1)	Reverse recovery energy per pulse			-	64.4	-	
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	0.40	-	mΩ
r _g	Internal gate resistance	Per switch		-	3.8	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

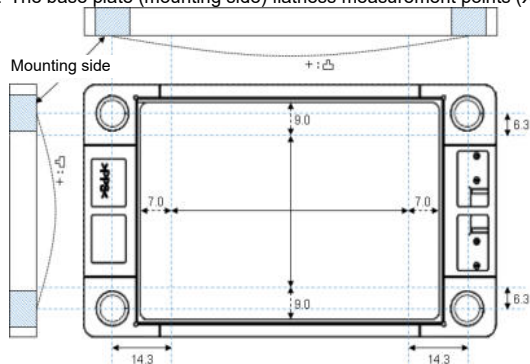
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	69	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)	-	-	123	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module.	-	13.3	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M_t	Mounting torque	Main terminals M 6 screw	3.5	4.0	4.5	N·m
M_s		Mounting to heat sink M 6 screw	3.5	4.0	4.5	
d_s	Creepage distance	Terminal to terminal	17.3	-	-	mm
		Terminal to base plate	25.3	-	-	
d_a	Clearance	Terminal to terminal	12.6	-	-	mm
		Terminal to base plate	21.8	-	-	
e_c	Flatness of base plate	On the centerline X, Y (Note7)	0	-	+200	μm
m	mass	—	-	260	-	g

*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU)2015/863.

- Note 1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed T_{vjmax} rating.
4. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips.
Refer to the figure of chip location.
5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
6. Typical value by using thermally conductive grease of $\lambda=3.0 \text{ W/(m}\cdot\text{K)}/D_{(c-s)}=50 \text{ }\mu\text{m}$.
7. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



8. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition (T_{vjmax} , T_{vjop} , T_{Cmax}) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

<IGBT Modules>

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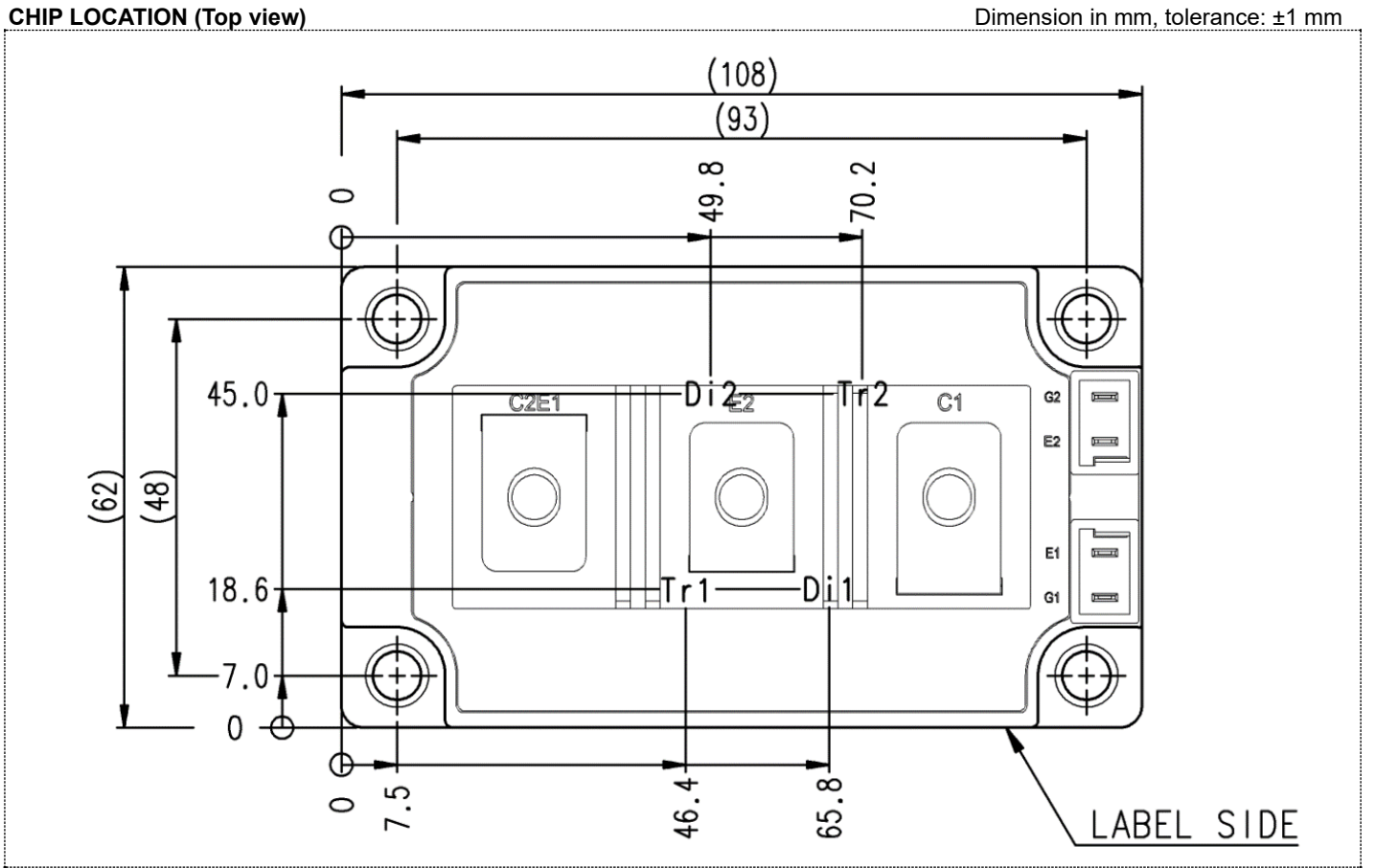
HIGH POWER SWITCHING USE

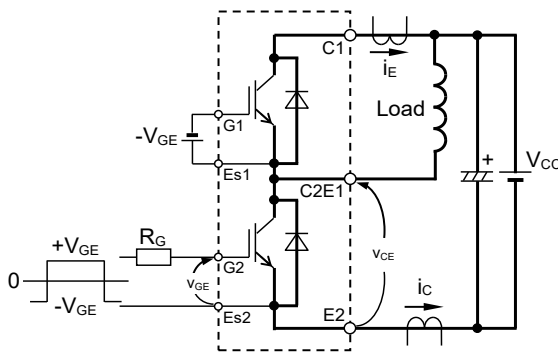
INSULATED TYPE

RECOMMENDED OPERATING CONDITIONS

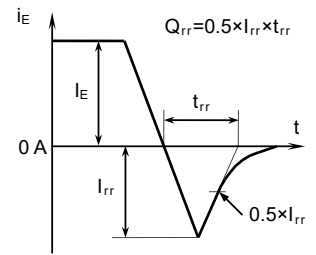
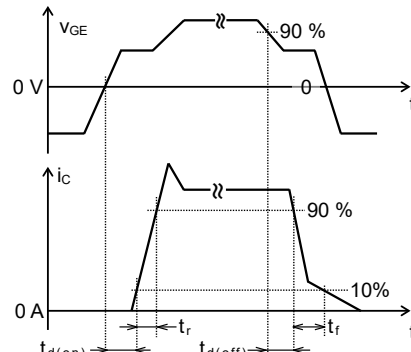
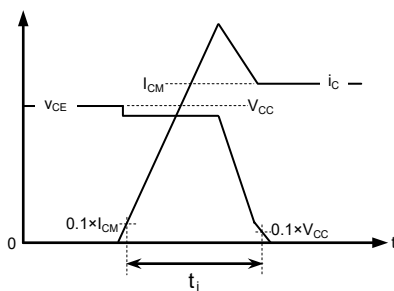
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across C1-E2 terminals	-	1300	1500	V
V_{GEon}	Gate-emitter drive voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	0	-	10	Ω

CHIP LOCATION (Top view)

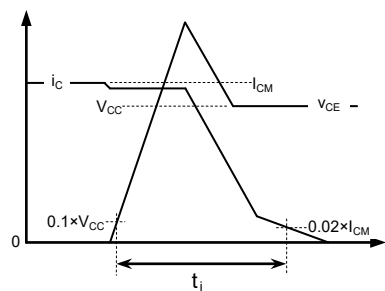


TEST CIRCUIT AND WAVEFORMS

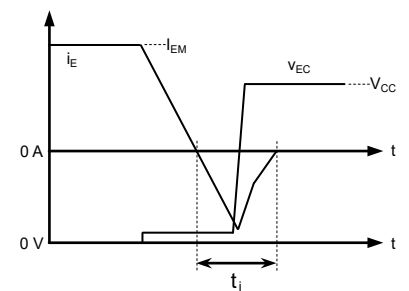
Switching characteristics test circuit and waveforms

 t_{rr} , Q_{rr} characteristics test waveform

IGBT Turn-on switching energy

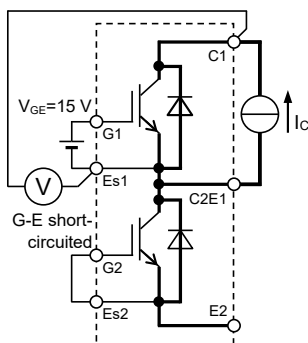


IGBT Turn-off switching energy

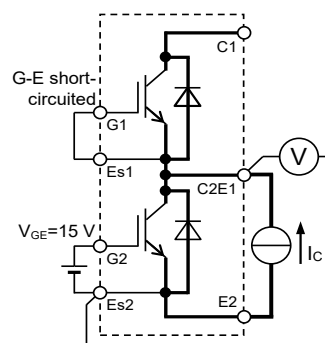


FWD Reverse recovery energy

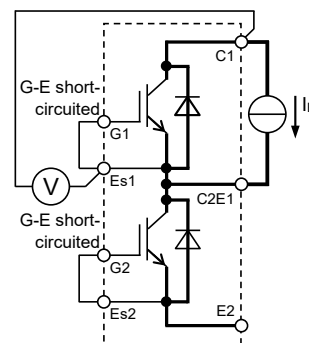
Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



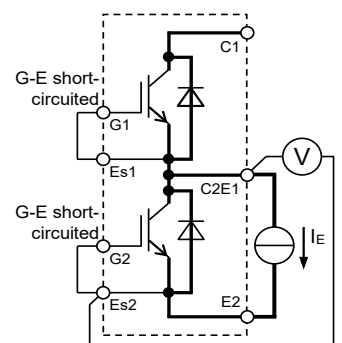
Tr1

 V_{CEsat} characteristics test circuit

Tr2



Di1

 V_{EC} characteristics test circuit

Di2

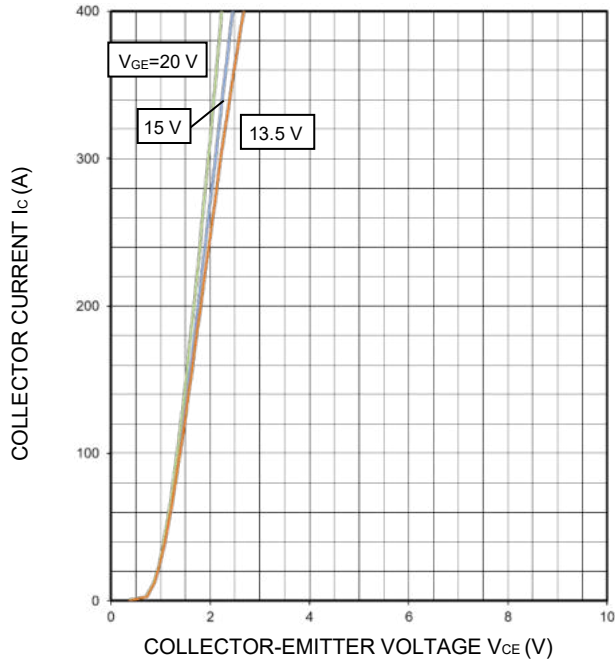
PERFORMANCE CURVES

INVERTER PART

OUTPUT CHARACTERISTICS

(TYPICAL)

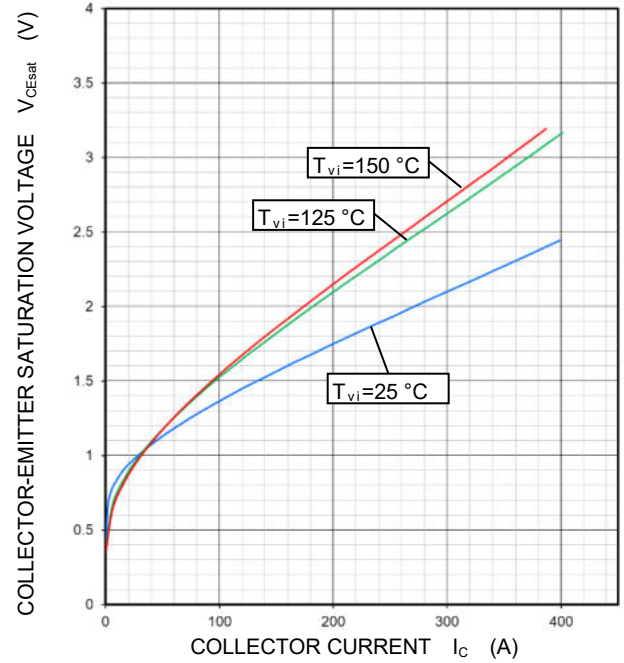
$T_{vj}=25^{\circ}\text{C}$ (chip)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS

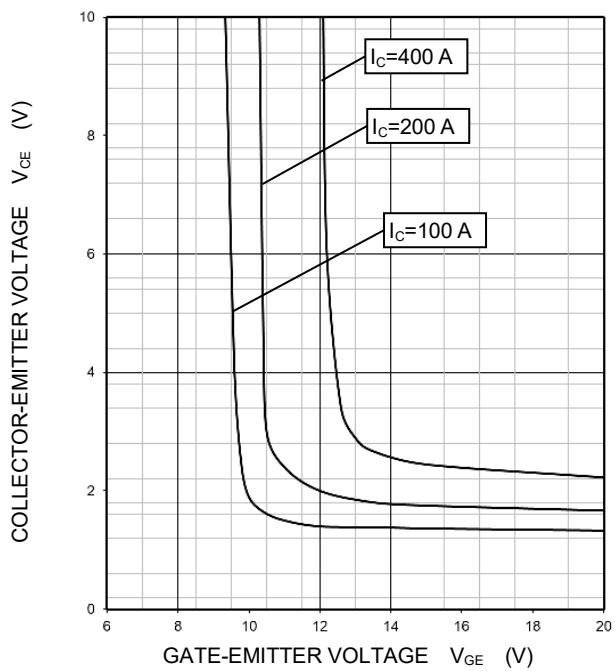
(TYPICAL)

$V_{GE}=15\text{ V}$ (chip)



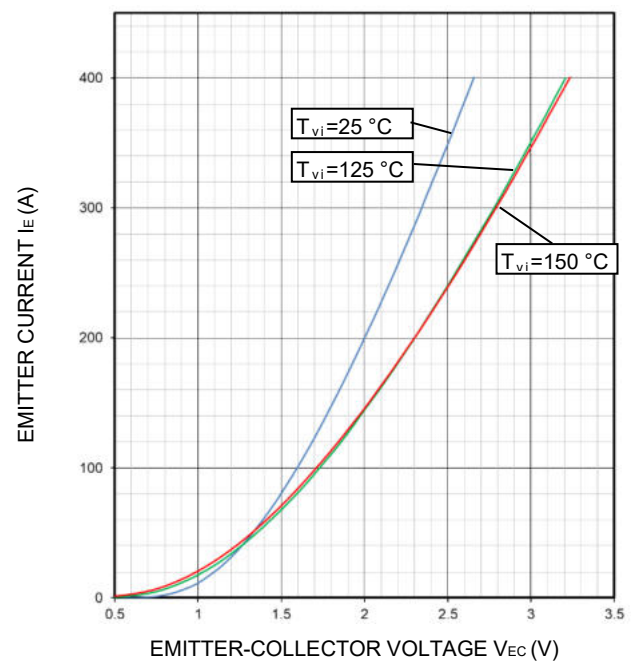
COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)

$T_{vj}=25^{\circ}\text{C}$ (chip)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

G-E short-circuited (chip)

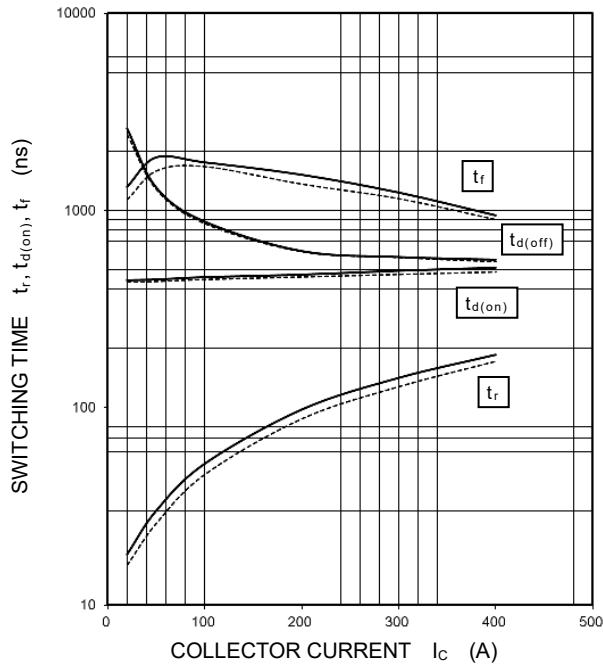


PERFORMANCE CURVES

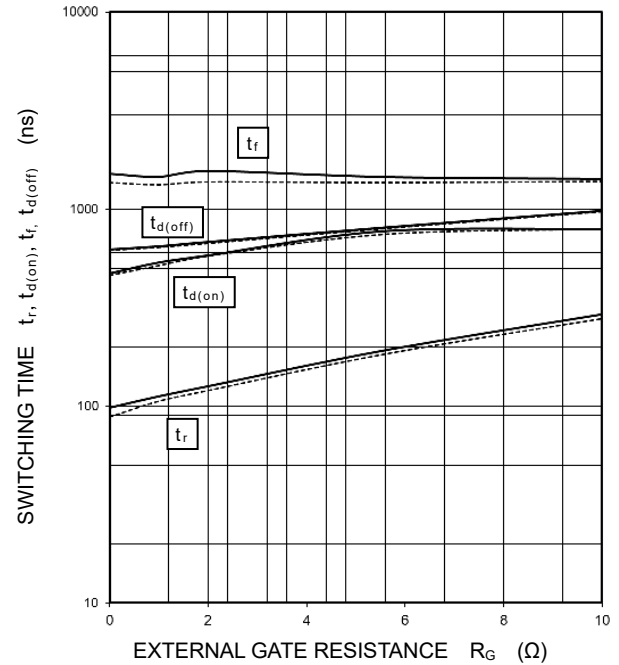
INVERTER PART

HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)

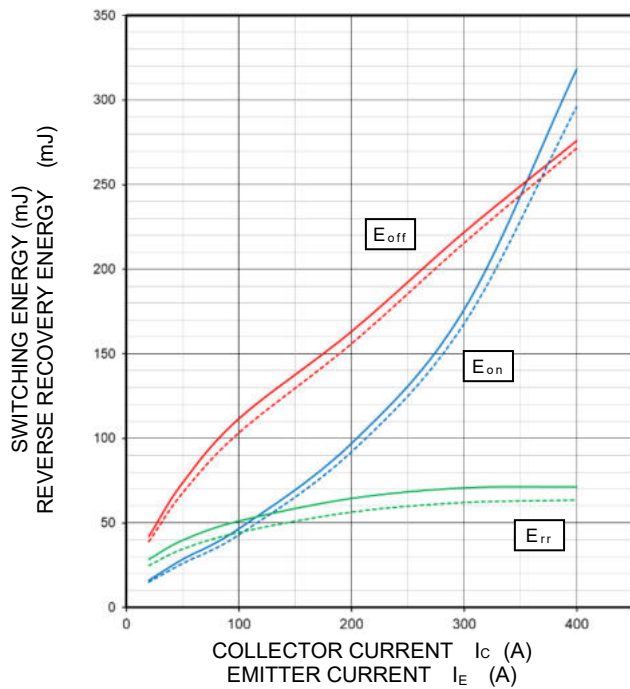
$V_{CC}=1300\text{ V}$, $R_G=0\ \Omega$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD
 —: $T_{vj}=150\text{ }^\circ\text{C}$, - - - : $T_{vj}=125\text{ }^\circ\text{C}$

HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)

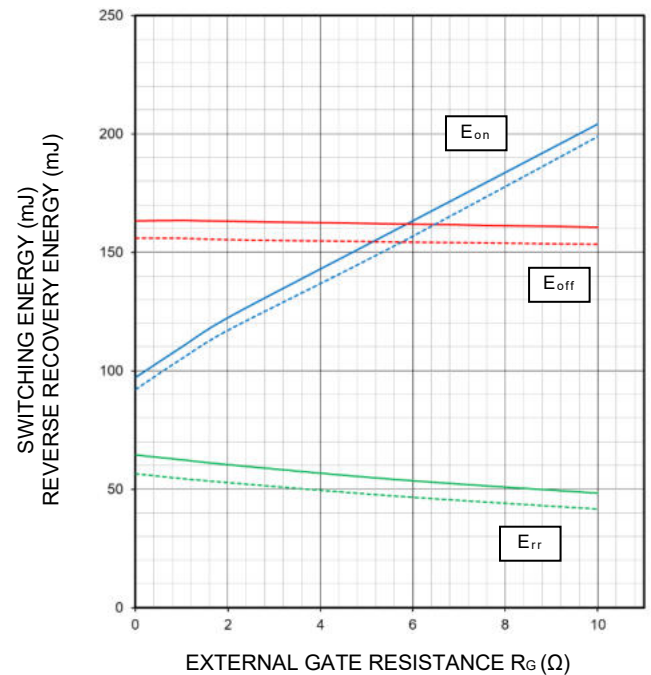
$V_{CC}=1300\text{ V}$, $I_c=200\text{ A}$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD
 —: $T_{vj}=150\text{ }^\circ\text{C}$, - - - : $T_{vj}=125\text{ }^\circ\text{C}$

HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)

$V_{CC}=1300\text{ V}$, $R_G=0\ \Omega$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD
 —: $T_{vj}=150\text{ }^\circ\text{C}$, - - - : $T_{vj}=125\text{ }^\circ\text{C}$,
 PER PULSE

HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)

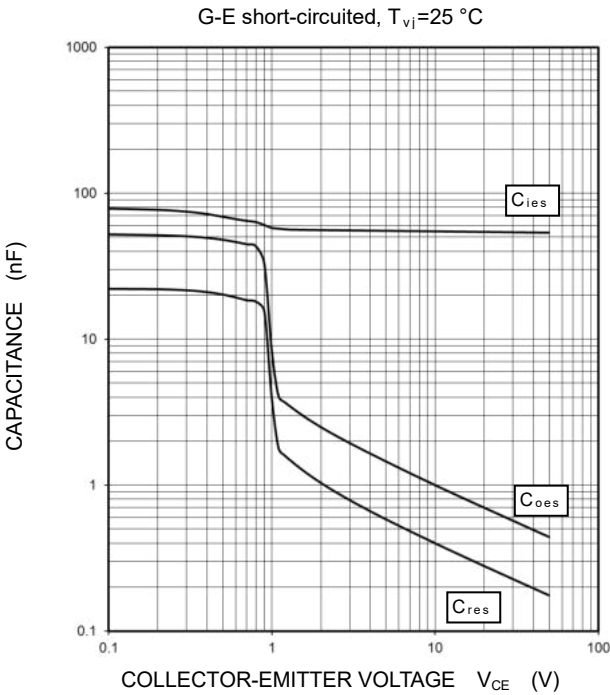
$V_{CC}=1300\text{ V}$, $I_c/I_E=200\text{ A}$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD,
 —: $T_{vj}=150\text{ }^\circ\text{C}$, - - - : $T_{vj}=125\text{ }^\circ\text{C}$,
 PER PULSE



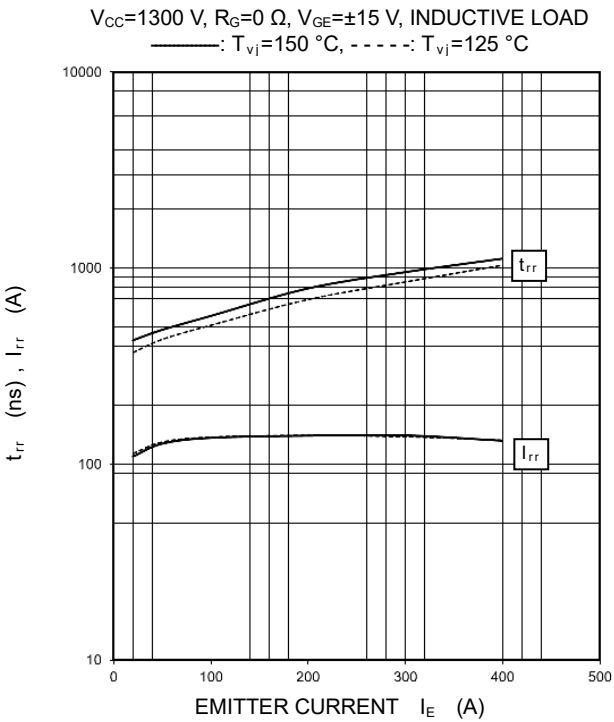
PERFORMANCE CURVES

INVERTER PART

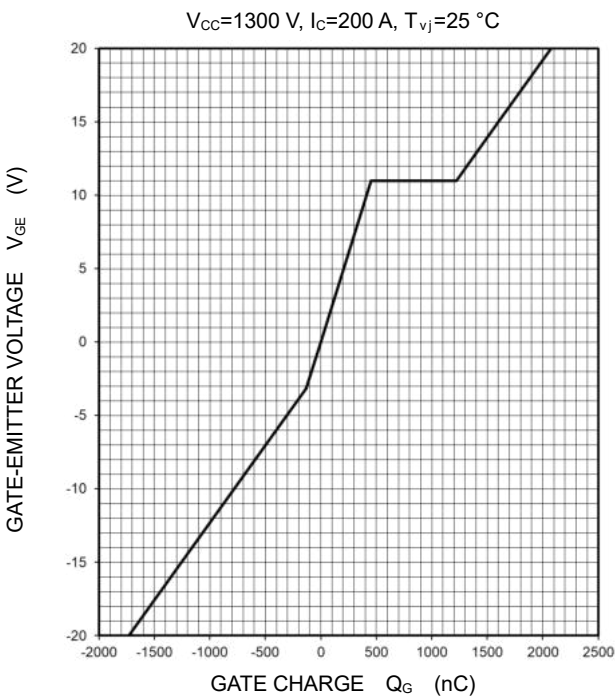
CAPACITANCE CHARACTERISTICS
(TYPICAL)



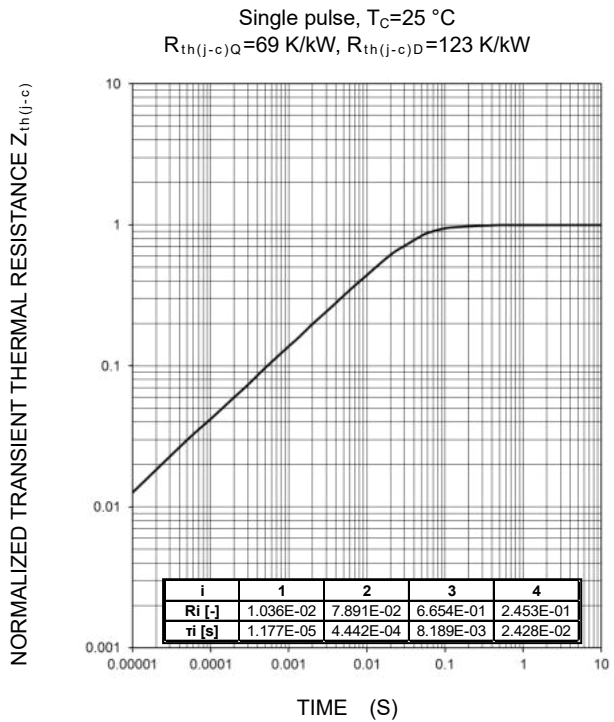
FREE WHEELING DIODE
REVERSE RECOVERY CHARACTERISTICS
(TYPICAL)



GATE CHARGE CHARACTERISTICS
(TYPICAL)

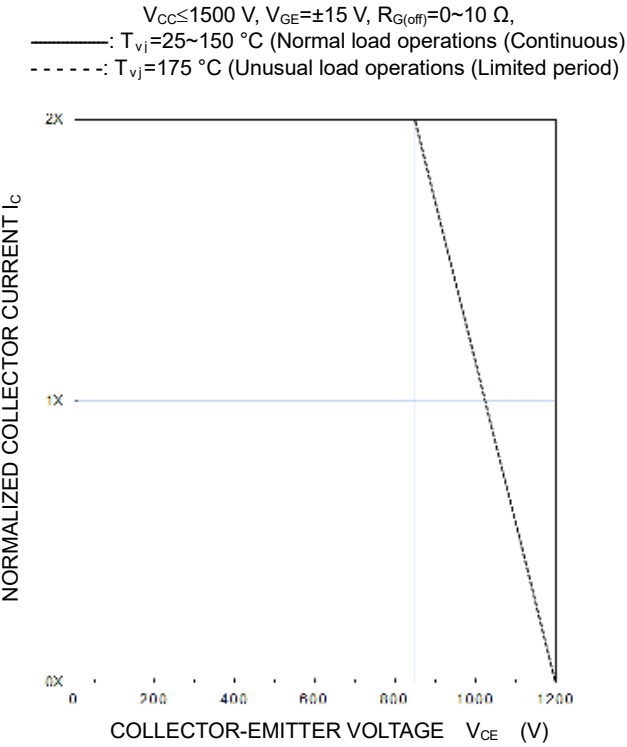


TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS
(MAXIMUM)

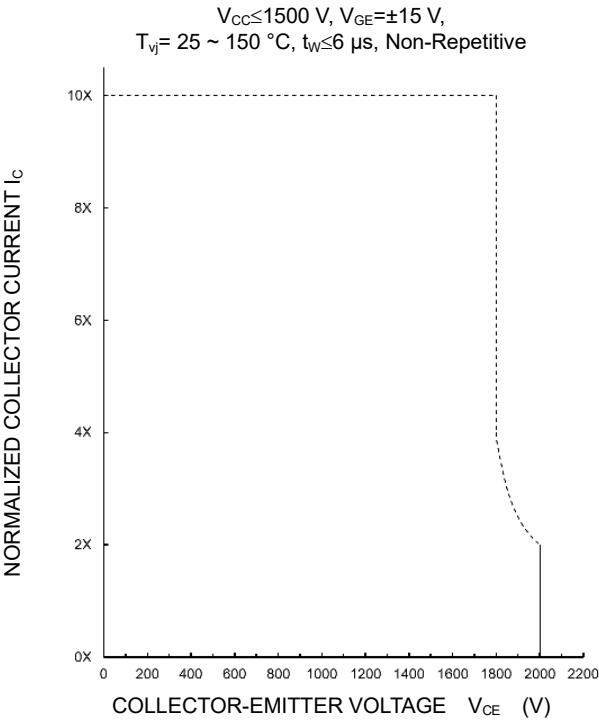


PERFORMANCE CURVES

TURN-OFF SWITCHING SAFE OPERATING AREA
(REVERSE BIAS SAFE OPERATING AREA)
(MAXIMUM)



SHORT-CIRCUIT SAFE OPERATING AREA
(MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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CM200DY-40TA

HIGH POWER SWITCHING USE

INSULATED TYPE

Keep safety first in your circuit designs!

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