

&lt;IGBT Modules&gt;

# CM300DY-24T

 HIGH POWER SWITCHING USE  
INSULATED TYPE


dual switch (half-bridge)

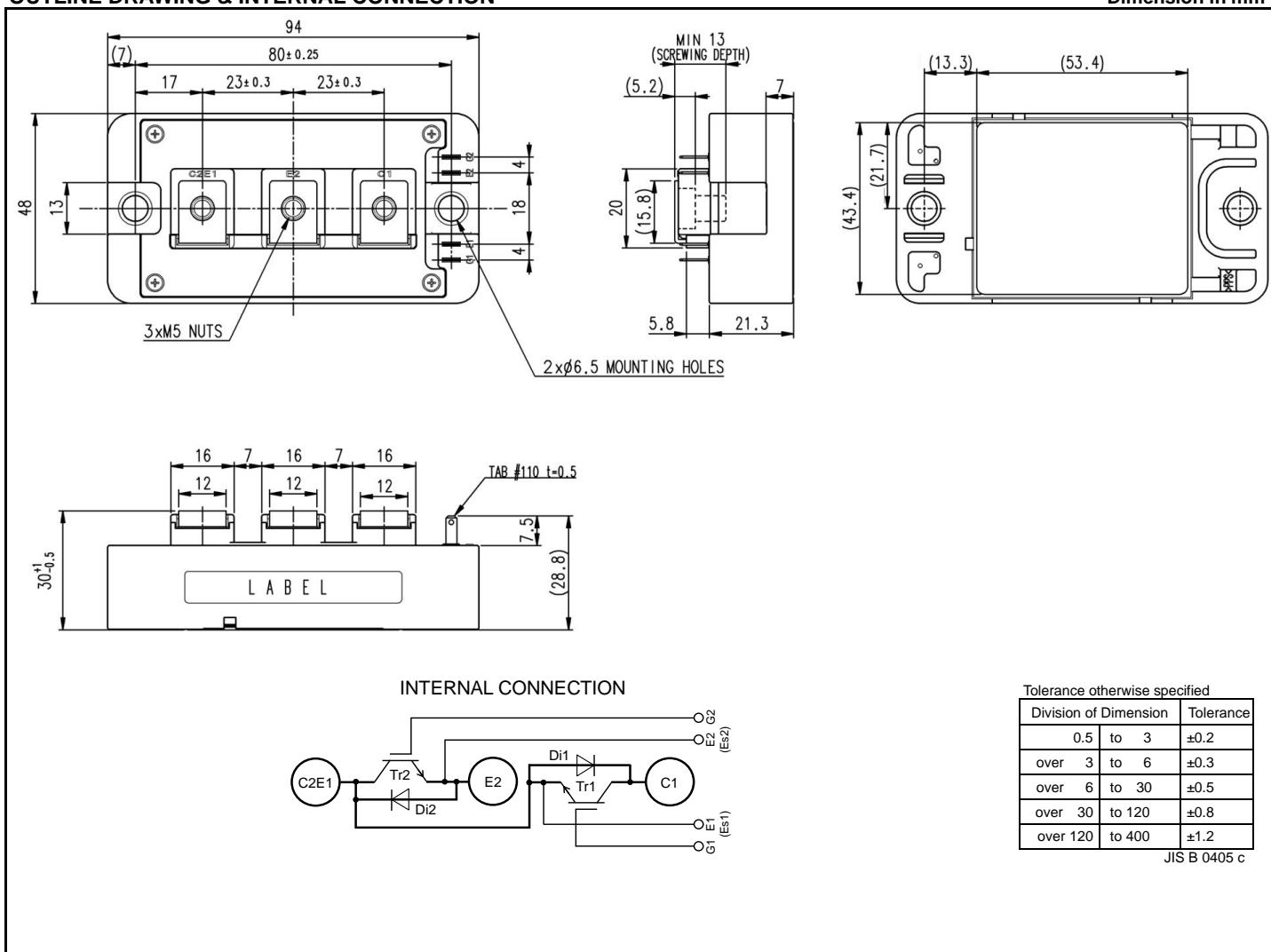
Collector current $I_C$ .....	3 0 0 A
Collector-emitter voltage $V_{CES}$ .....	1 2 0 0 V
Maximum junction temperature $T_{vjmax}$ .....	1 7 5 °C
<ul style="list-style-type: none"> <li>Flat base type</li> <li>Nickel-plating tab terminals</li> <li>RoHS Directive compliant</li> <li>UL Recognized under UL1557, File No.E323585</li> </ul>	

**APPLICATION**

AC Motor Control, Motion/Servo Control, Power supply, etc.

**OPTION** (Below options are available.)

- PC-TIM (Phase Change Thermal Interface Material) pre-apply
- $V_{CEsat}$  selection for parallel connection

**OUTLINE DRAWING & INTERNAL CONNECTION**


**MAXIMUM RATINGS (T<sub>vj</sub>=25 °C, unless otherwise specified)**

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	± 20	V
I <sub>C</sub>	Collector current	DC, T <sub>C</sub> =145 °C* (Note2, 4)	300	A
I <sub>CRM</sub>		Pulse, Repetitive (Note3)	600	
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	3260	W
I <sub>E</sub> (Note1)	Emitter current	DC (Note2)	300	A
I <sub>ERM</sub> (Note1)		Pulse, Repetitive (Note3)	600	
V <sub>isol</sub>	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T <sub>jmax</sub>	Maximum junction temperature	Instantaneous event (overload) (Note8)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4,8)	150*	
T <sub>jop</sub>	Operating junction temperature	Continuous operation (under switching) (Note8)	-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +150*	

**ELECTRICAL CHARACTERISTICS (T<sub>vj</sub>=25 °C, unless otherwise specified)**

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited	-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited	-	-	0.5	µA
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	I <sub>C</sub> =30 mA, V <sub>CE</sub> =10 V	5.4	6.0	6.6	V
V <sub>CESat</sub> (Terminal)	Collector-emitter saturation voltage	I <sub>C</sub> =300 A, V <sub>GE</sub> =15 V, Refer to the figure of test circuit (Note5)	T <sub>vj</sub> =25 °C	-	1.70	2.00
			T <sub>vj</sub> =125 °C	-	1.95	-
			T <sub>vj</sub> =150 °C	-	2.00	-
V <sub>CESat</sub> (Chip)		I <sub>C</sub> =300 A, V <sub>GE</sub> =15 V, (Note5)	T <sub>vj</sub> =25 °C	-	1.55	1.80
			T <sub>vj</sub> =125 °C	-	1.75	-
			T <sub>vj</sub> =150 °C	-	1.80	-
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> =10 V, G-E short-circuited	-	-	61.5	nF
C <sub>oes</sub>	Output capacitance		-	-	1.8	
C <sub>res</sub>	Reverse transfer capacitance		-	-	0.8	
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =300 A, V <sub>GE</sub> =15 V	-	2.0	-	µC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =600 V, I <sub>C</sub> =300 A, V <sub>GE</sub> =±15 V, R <sub>G</sub> =1.1 Ω, Inductive load	-	-	500	ns
t <sub>r</sub>	Rise time		-	-	150	
t <sub>d(off)</sub>	Turn-off delay time		-	-	600	
t <sub>f</sub>	Fall time		-	-	300	
V <sub>EC</sub> (Note.1) (Terminal)	Emitter-collector voltage	I <sub>E</sub> =300 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T <sub>vj</sub> =25 °C	-	1.80	2.20
			T <sub>vj</sub> =125 °C	-	1.95	-
			T <sub>vj</sub> =150 °C	-	1.95	-
V <sub>EC</sub> (Note.1) (Chip)		I <sub>E</sub> =300 A, G-E short-circuited, (Note5)	T <sub>vj</sub> =25 °C	-	1.65	2.00
			T <sub>vj</sub> =125 °C	-	1.65	-
			T <sub>vj</sub> =150 °C	-	1.65	-
t <sub>rr</sub> (Note1)	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>E</sub> =300 A, V <sub>GE</sub> =±15 V, R <sub>G</sub> =1.1 Ω, Inductive load	-	-	400	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge		-	30	-	µC
E <sub>on</sub>	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =300 A, V <sub>GE</sub> =±15 V, R <sub>G</sub> =1.1 Ω, T <sub>vj</sub> =150 °C, Inductive load	-	27.9	-	mJ
E <sub>off</sub>	Turn-off switching energy per pulse		-	29.9	-	
E <sub>rr</sub> (Note1)	Reverse recovery energy per pulse		-	18.3	-	
R <sub>CC+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =25 °C (Note4)	-	0.3	-	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch	-	1.5	-	Ω

\*: The value of PC-TIM applied module is limited by the heat resistant temperature of PC-TIM.

## THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)		-	-	46	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)		-	-	81	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module	Thermal grease applied (Note4,6,8)	-	24	-	K/kW

## MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
$M_t$	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N·m
$M_s$	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N·m
$d_s$	Creepage distance	Terminal to terminal		18	-	-	mm
		Terminal to base plate		21.1	-	-	
$d_a$	Clearance	Terminal to terminal		9.6	-	-	mm
		Terminal to base plate		16.7	-	-	
$e_c$	Flatness of base plate	On the centerline (Note7)		$\pm 0$	-	+200	$\mu\text{m}$
$m$	mass	-		-	155	-	g

\*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature ( $T_{vj}$ ) should not increase beyond  $T_{vj\max}$  rating.

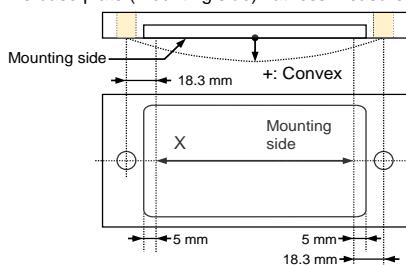
3. Pulse width and repetition rate should be such that the device junction temperature ( $T_{vj}$ ) dose not exceed  $T_{vj\max}$  rating.

4. Case temperature ( $T_c$ ) and heat sink temperature ( $T_s$ ) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6. Reference value. Thermally conductive grease of thermal conductivity  $\lambda=3.0 \text{ W}/(\text{m}\cdot\text{K})/D_{(C-S)}=50 \mu\text{m}$ .

7. The base plate (mounting side) flatness measurement points (X) are shown in the following figure.



8. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition ( $T_{vj\max}$ ,  $T_{vj\text{op}}$ ,  $T_{C\max}$ ) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

CM300DY-24T

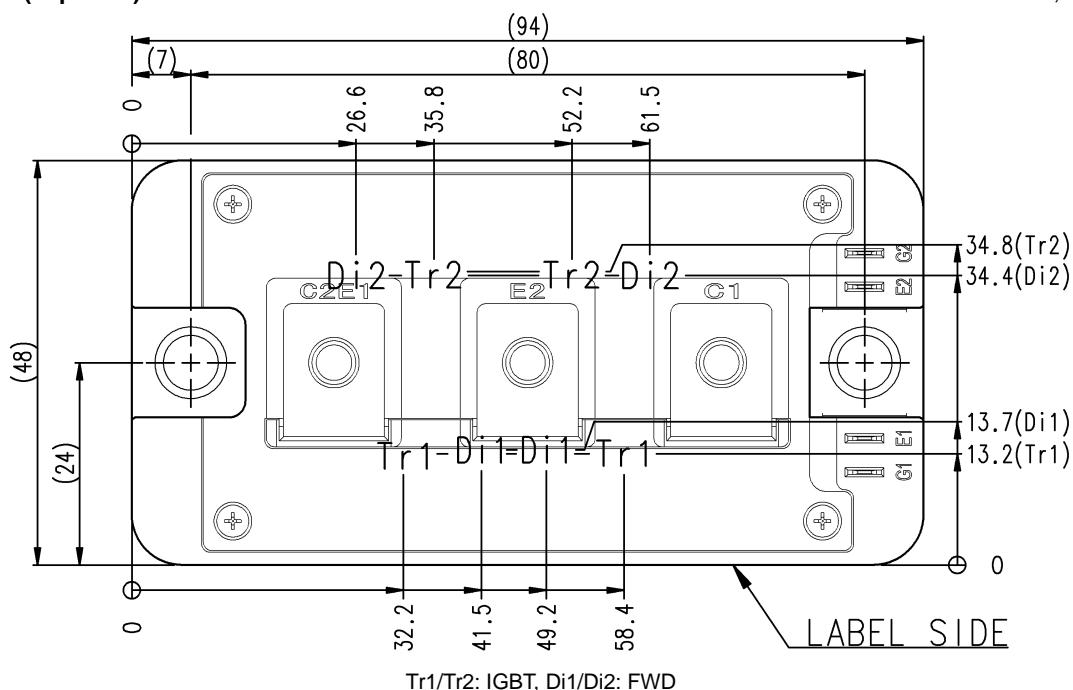
## HIGH POWER SWITCHING USE INSULATED TYPE

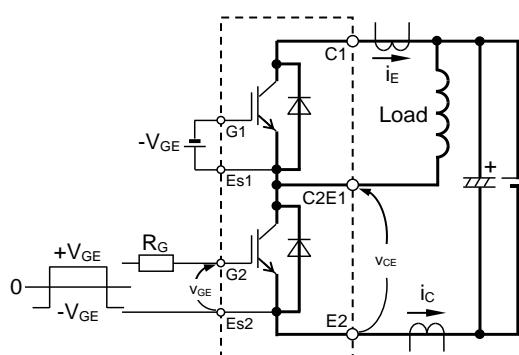
## RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{CC}$	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
$V_{GEon}$	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
$R_G$	External gate resistance	Per switch	1.1	-	22	$\Omega$

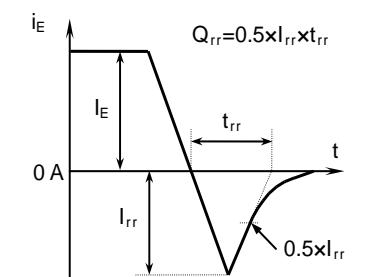
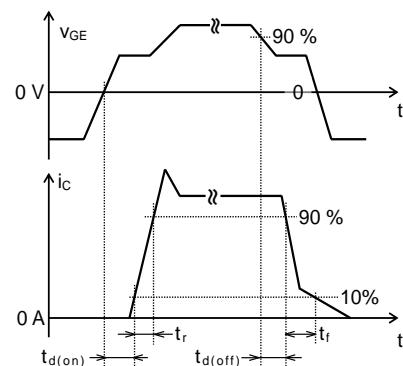
### CHIP LOCATION (Top view)

Dimension in mm, tolerance:  $\pm 1$  mm

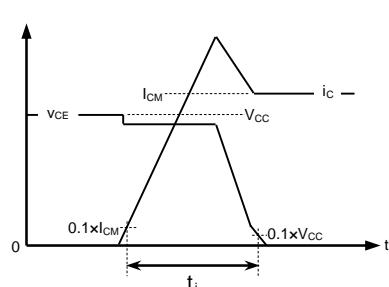


**TEST CIRCUIT AND WAVEFORMS**

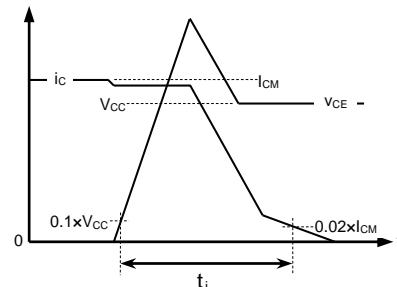
Switching characteristics test circuit and waveforms



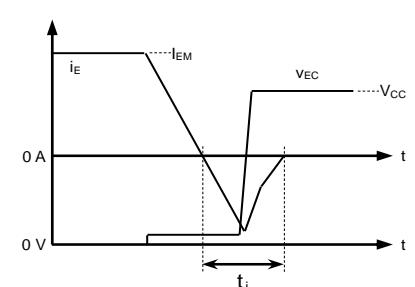
t\_rr, Q\_rr characteristics test waveform



IGBT Turn-on switching energy

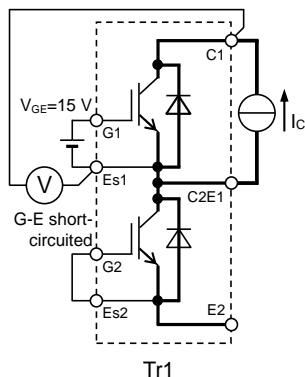


IGBT Turn-off switching energy

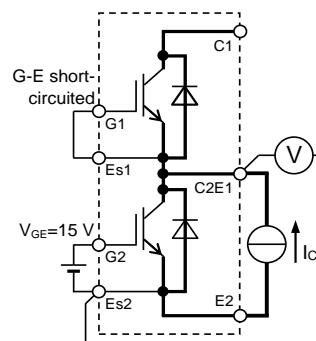


FWD Reverse recovery energy

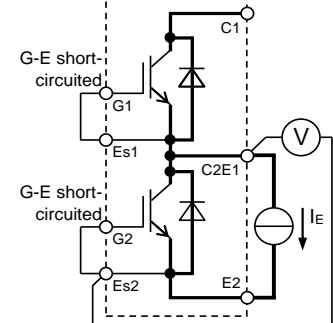
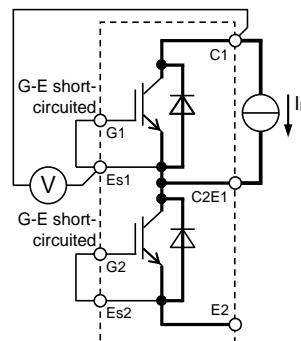
Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

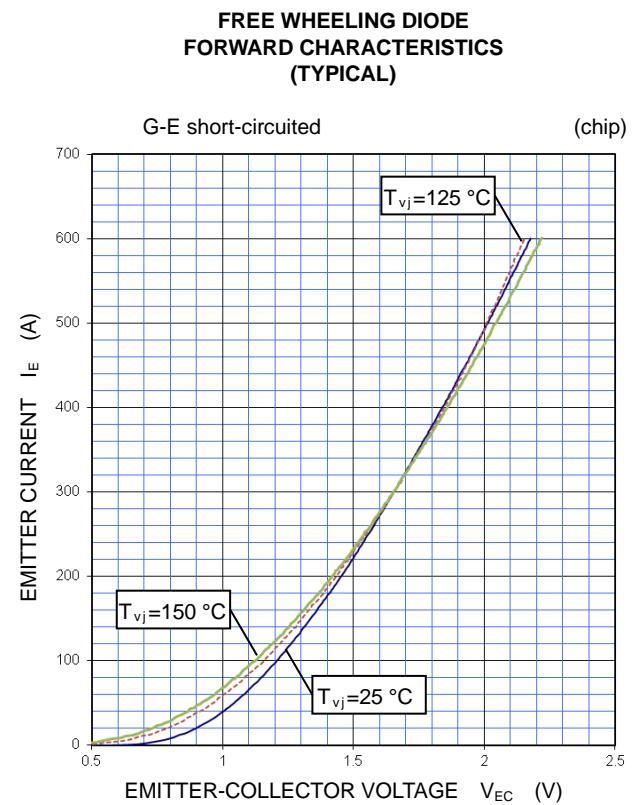
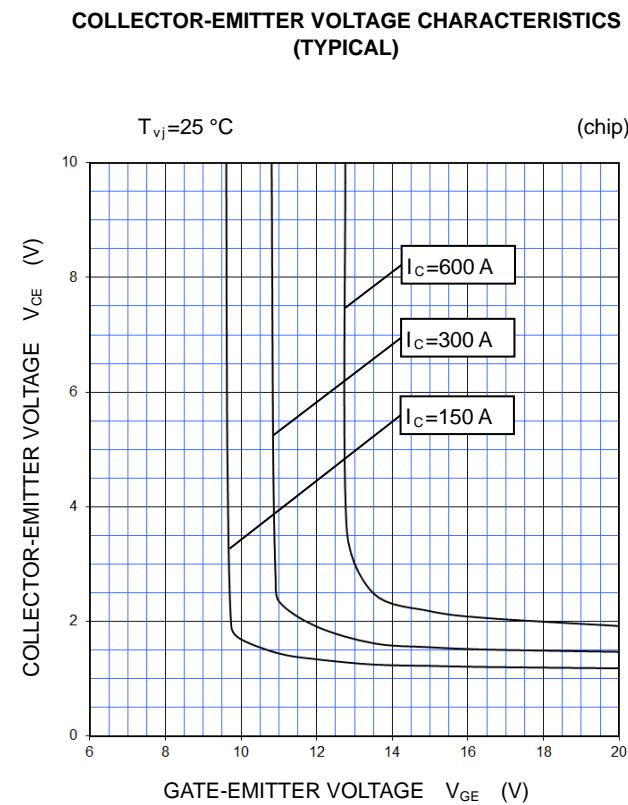
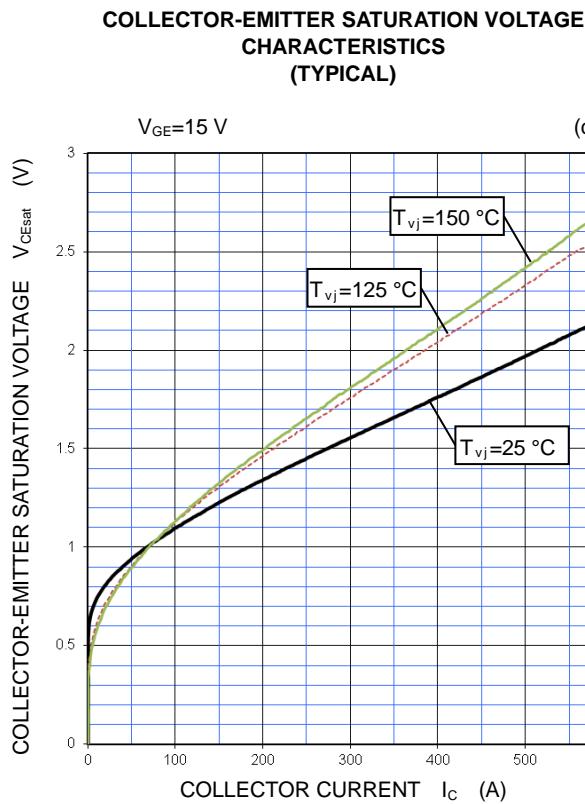
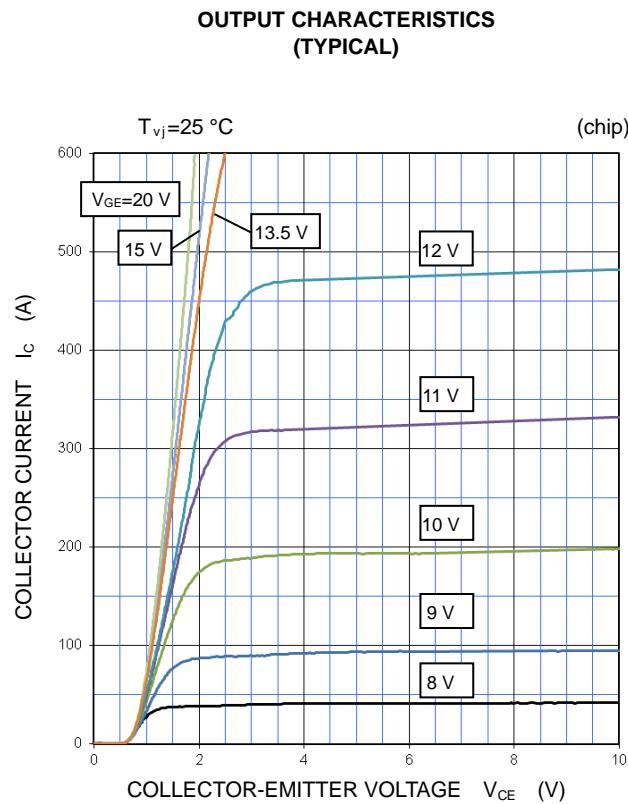
**TEST CIRCUIT**

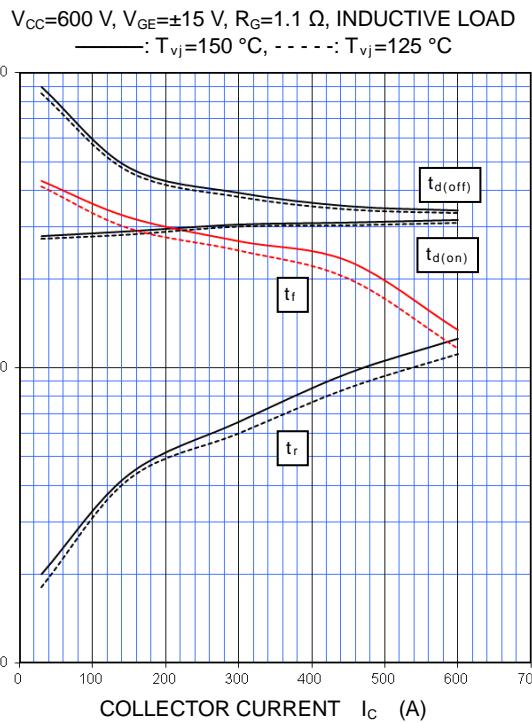
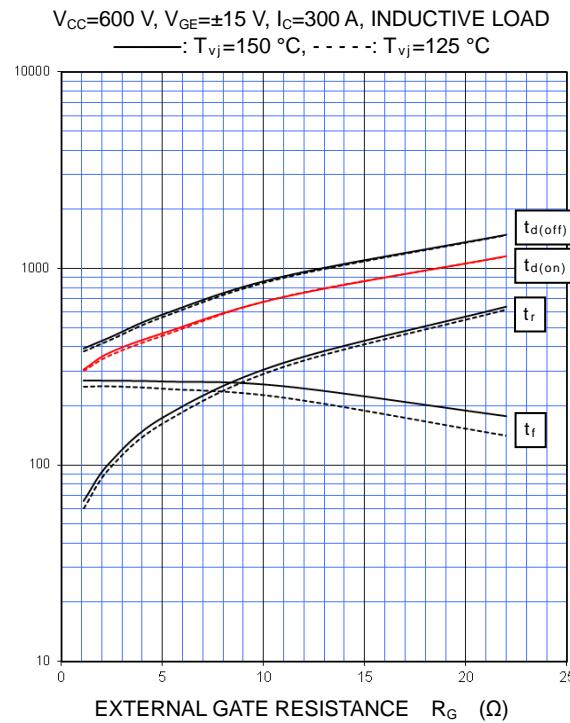
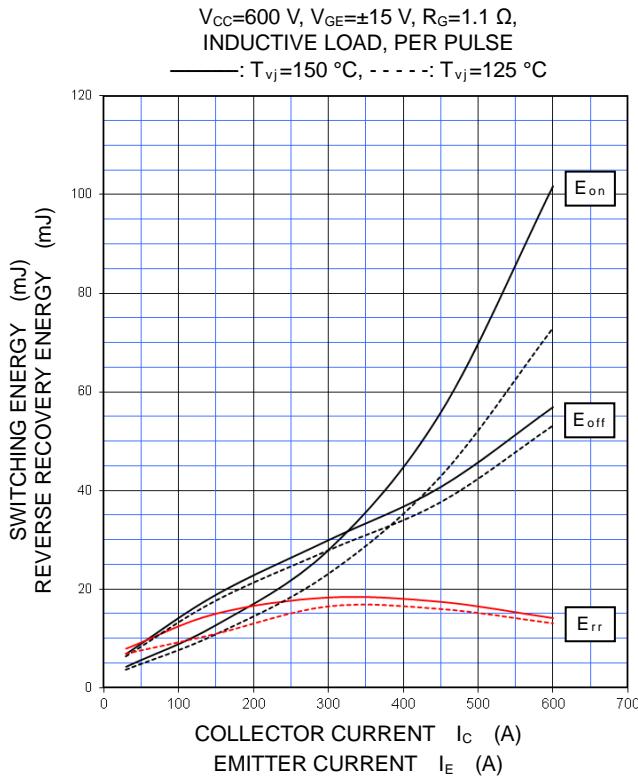
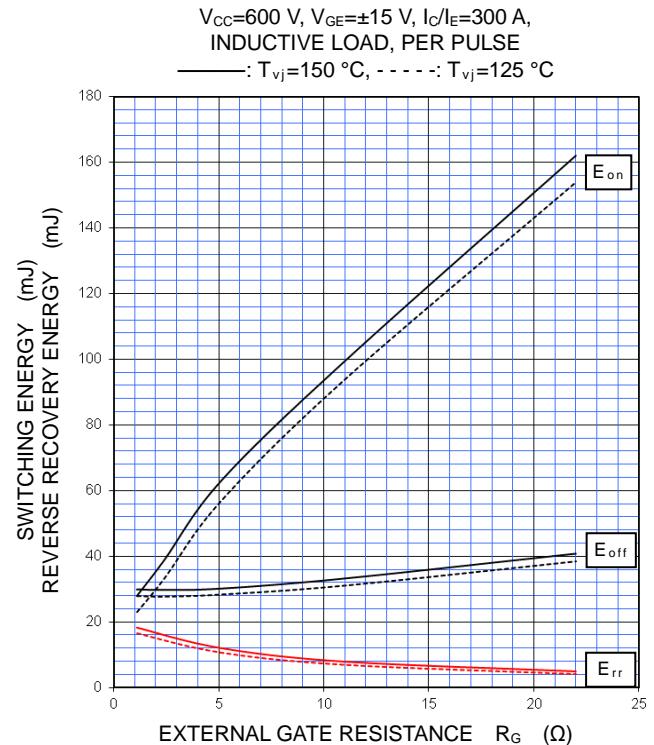
V\_CEsat characteristics test circuit

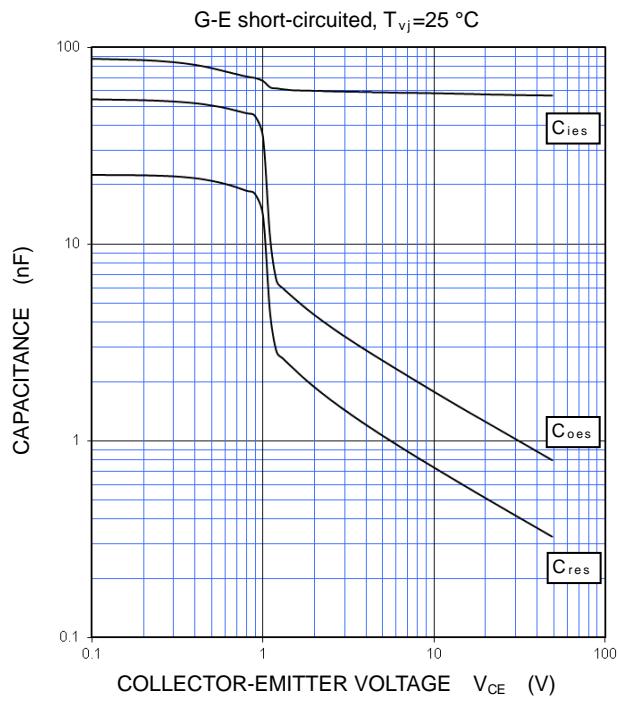
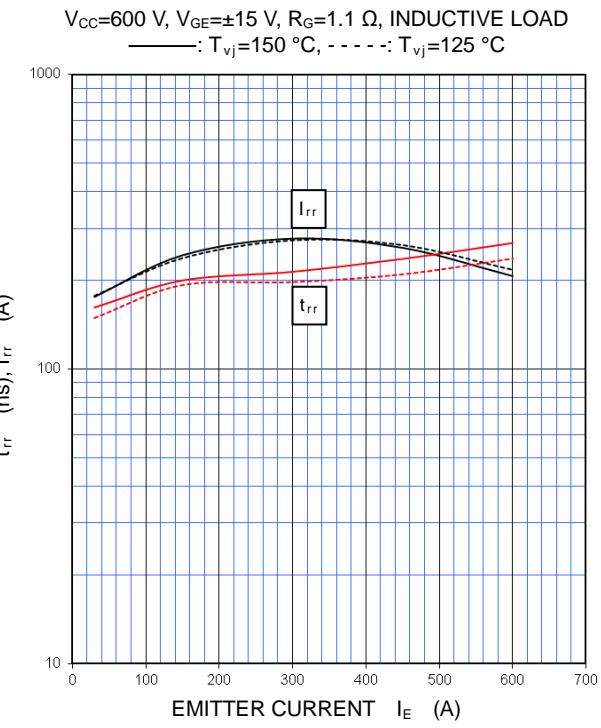
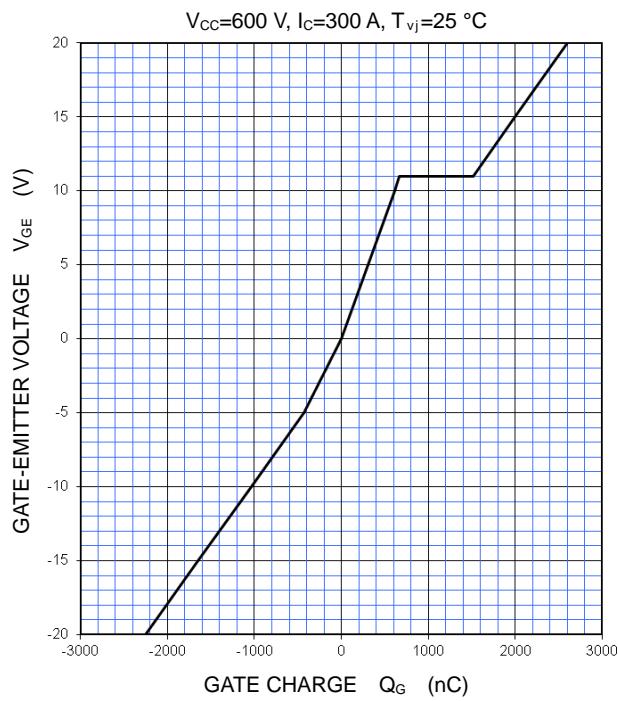
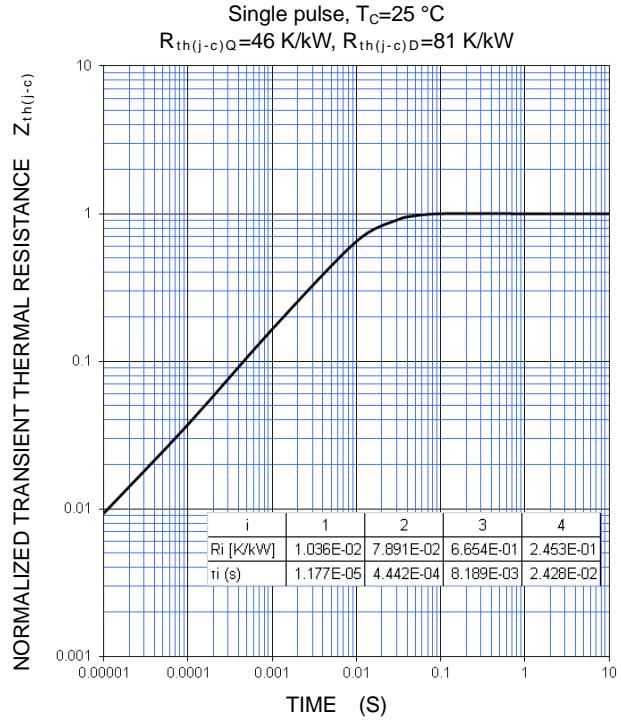


V\_EC characteristics test circuit



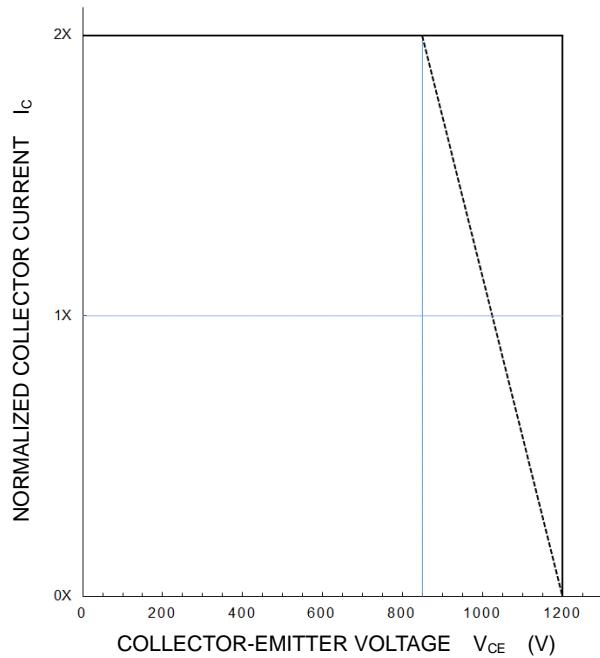
**PERFORMANCE CURVES**

**PERFORMANCE CURVES****HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)****HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)****HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)****HALF-BRIDGE SWITCHING CHARACTERISTICS  
(TYPICAL)**

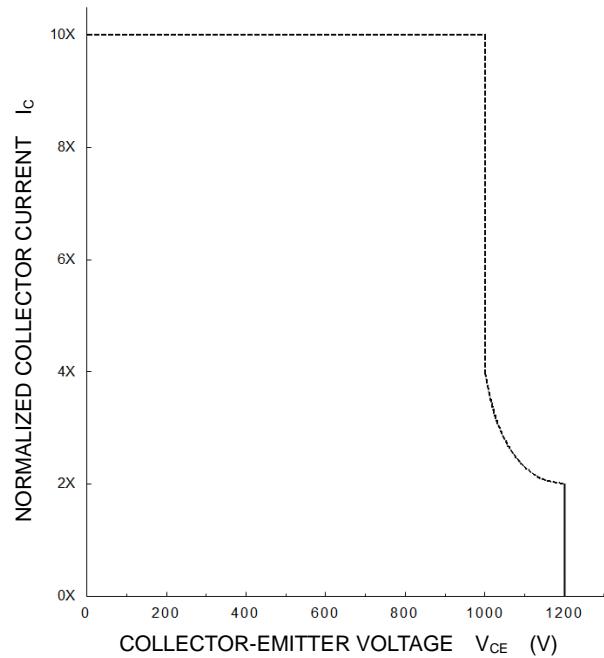
**PERFORMANCE CURVES****CAPACITANCE CHARACTERISTICS  
(TYPICAL)****FREE WHEELING DIODE  
REVERSE RECOVERY CHARACTERISTICS  
(TYPICAL)****GATE CHARGE CHARACTERISTICS  
(TYPICAL)****TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS  
(MAXIMUM)**

**PERFORMANCE CURVES****TURN-OFF SWITCHING SAFE OPERATING AREA  
(REVERSE BIAS SAFE OPERATING AREA)  
(MAXIMUM)**

$V_{CC} \leq 850$  V,  $V_{GE} = \pm 15$  V,  $R_G = 1.1 \sim 22 \Omega$ ,  
 ———:  $T_{vj} = 25 \sim 150$  °C (Normal load operations (Continuous))  
 - - - - -:  $T_{vj} = 175$  °C (Unusual load operations (Limited period))

**SHORT-CIRCUIT SAFE OPERATING AREA  
(MAXIMUM)**

$V_{CC} \leq 800$  V,  $V_{GE} = \pm 15$  V,  $R_G = 1.1 \sim 22 \Omega$ ,  
 $T_{vj} = 25 \sim 150$  °C,  $t_W \leq 8 \mu s$ , Non-Repetitive



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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