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MITSUBISHI ELECTRIC

<IGBT Modules>

CM50MXUB-13T/CM50MXUBP-13T

HIGH POWER SWITCHING USE
INSULATED TYPE

MXUB		Collector current I_C ..... **50** A Collector-emitter voltage V_{CES} ..... **650** V Maximum junction temperature T_{vjmax} ..... **175** °C ●Flat base type ●Copper base plate (Nickel-plating) ●RoHS Directive compliant ●Tin-plating pin terminals
MXUBP		Collector current I_C ..... **50** A Collector-emitter voltage V_{CES} ..... **650** V Maximum junction temperature T_{vjmax} ..... **175** °C ●Flat base type ●Copper base plate (Nickel-plating) ●RoHS Directive compliant ●Tin-plating pressfit terminals
CIB (Converter+Inverter+Chopper Brake)		●UL Recognized under UL1557, File No. E323585

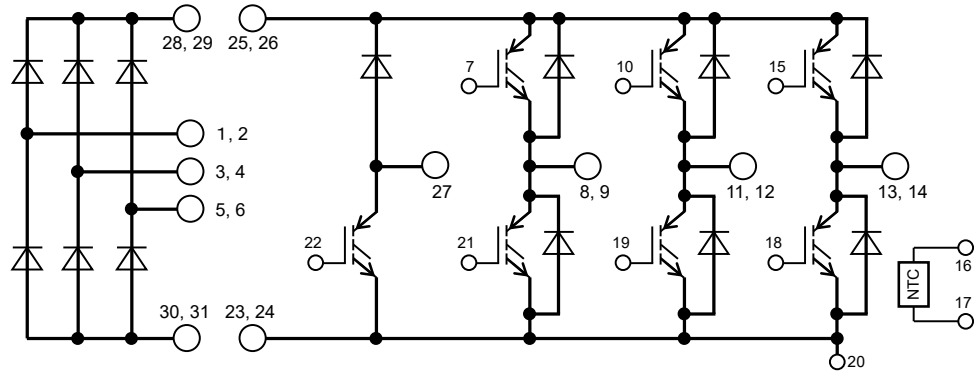
APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

OPTION (Below options are available.)

●PC-TIM (Phase Change Thermal Interface Material) pre-apply

INTERNAL CONNECTION



Terminal code

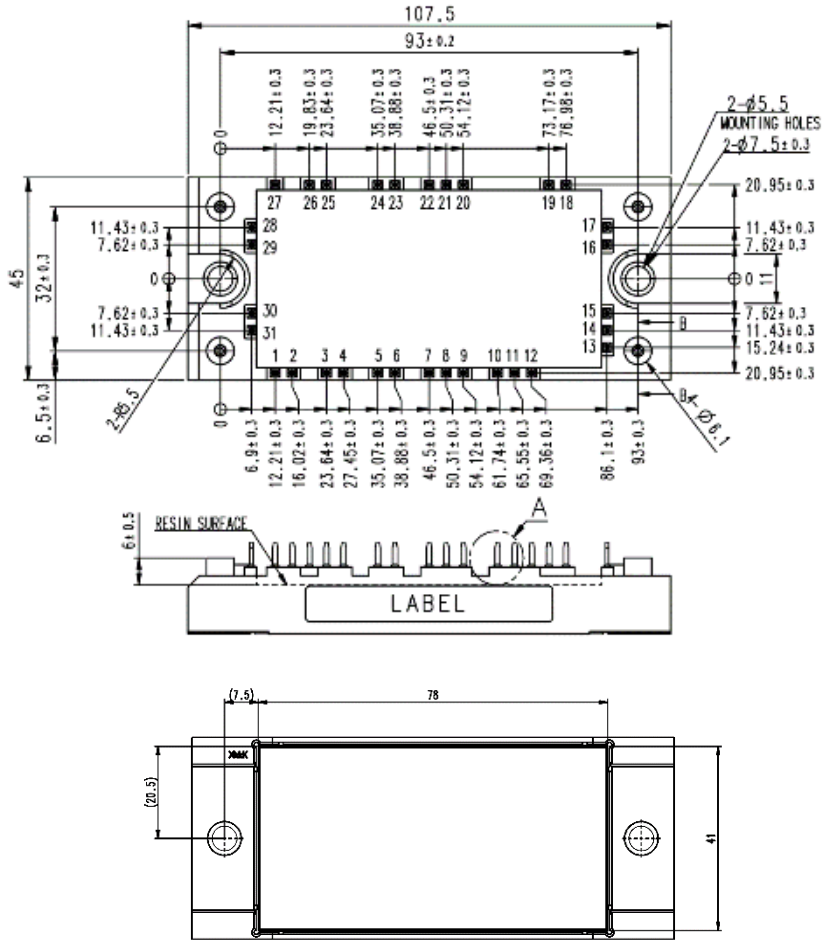
1	R	16	TH1	31	N
2	R	17	TH2		
3	S	18	GwN		
4	S	19	GvN		
5	T	20	E		
6	T	21	GuN		
7	GuP	22	GB		
8	U	23	N1		
9	U	24	N1		
10	GvP	25	P1		
11	V	26	P1		
12	V	27	B		
13	W	28	P		
14	W	29	P		
15	GwP	30	N		

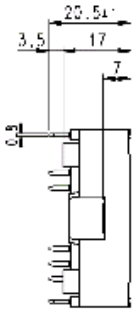
CM50MXUB-13T/CM50MXUBP-13T

HIGH POWER SWITCHING USE
INSULATED TYPE

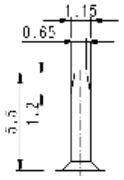
OUTLINE DRAWING **Dimension in mm**

MXUB

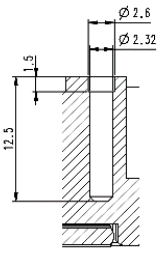




SECTION A



SECTION B



Tolerance otherwise specified

Division of Dimension		Tolerance
0.5	to 3	±0.2
over 3	to 6	±0.3
over 6	to 30	±0.5
over 30	to 120	±0.8
over 120	to 400	±1.2

CM50MXUB-13T/CM50MXUBP-13T

HIGH POWER SWITCHING USE
INSULATED TYPE

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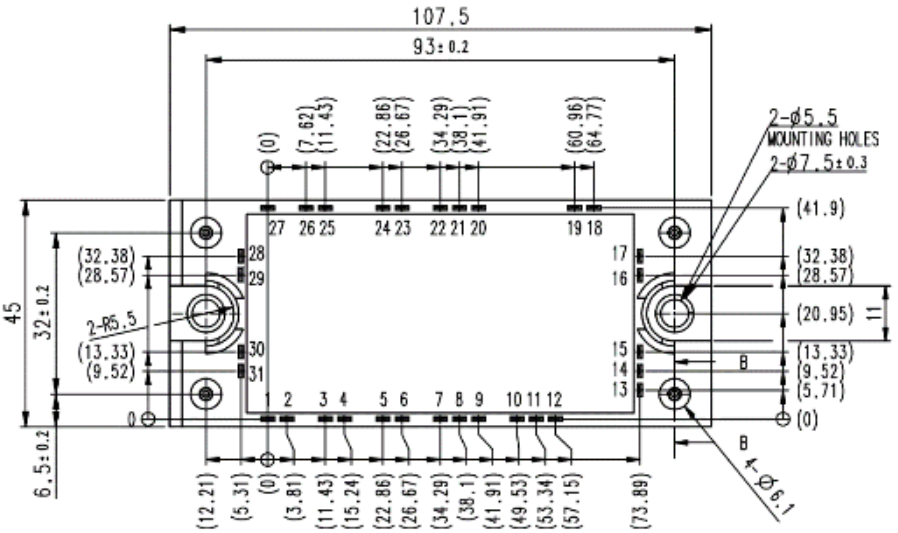
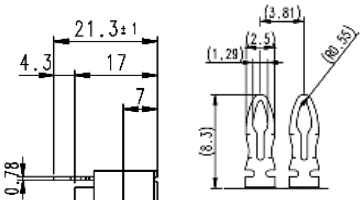
OUTLINE DRAWING **Dimension in mm**

MXUBP



SECTION A



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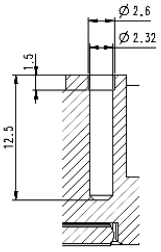
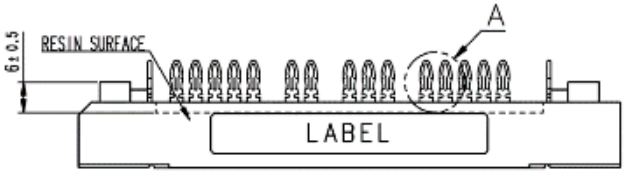
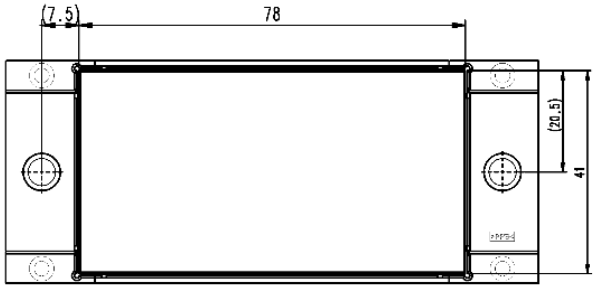
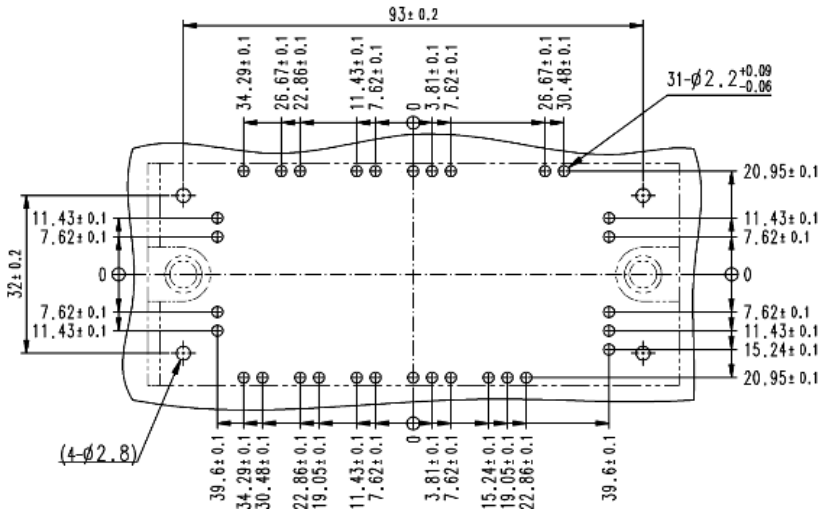






PCB DRILL HOLE PATTERN



Tolerance otherwise specified

Division of Dimension		Tolerance
0.5	to 3	±0.2
over 3	to 6	±0.3
over 6	to 30	±0.5
over 30	to 120	±0.8
over 120	to 400	±1.2

MAXIMUM RATINGS (T_{vj}=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	650	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, T_C=125°C (Note2, 4)	50	A
I_{CRM}		Pulse, Repetitive (Note3)	100	
P_{tot}	Total power dissipation	T_C=25 °C (Note2, 4)	270	W
I_E (Note1)	Emitter current	DC (Note2)	50	A
I_{ERM} (Note1)		Pulse, Repetitive (Note3)	100	
T_{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note9)	175	°C

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	650	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, T_C=125 °C (Note2, 4)	35	A
I_{CRM}		Pulse, Repetitive (Note3)	70	
P_{tot}	Total power dissipation	T_C=25 °C (Note2, 4)	240	W
V_{RRM}	Repetitive peak reverse voltage	G-E short-circuited	650	V
I_F	Forward current	DC (Note2)	35	A
I_{FRM}		Pulse, Repetitive (Note3)	70	
T_{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note9)	175	°C

CONVERTER PART DIODE

Symbol	Item	Conditions		Rating	Unit
V_{RRM}	Repetitive peak reverse voltage	-		800	V
E_a	Recommended AC input voltage	RMS		220	V
I_o	DC output current	3-phase full wave rectifying, T_C=125 °C (Note4)		50	A
I_{FSM}	Surge forward current	The sine half wave 1 cycle peak value, f=60 Hz, non-repetitive	T_{vj}=25 °C	600	A
			T_{vj}=150 °C	480	
I^2t	Current square time	Value for one cycle of surge current	T_{vj}=25 °C	1500	A^2s
			T_{vj}=150 °C	960	
T_{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note9)		150	°C

MODULE

Symbol	Item	Conditions	Rating	Unit
V_{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T_{Cmax}	Maximum case temperature	(Note4, 9)	125	°C
T_{vjop}	Operating junction temperature	Continuous operation (under switching) (Note9)	-40 ~ +150	°C
T_{stg}	Storage temperature	-	-40 ~ +125	

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ELECTRICAL CHARACTERISTICS (T_{vj}=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I_{CES}	Collector-emitter cut-off current	$V_{CE}=V_{CES}$, G-E short-circuited		-	-	1.0	mA
I_{GES}	Gate-emitter leakage current	$V_{GE}=V_{GES}$, C-E short-circuited		-	-	0.5	µA
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C=5.0 mA, V_{CE}=10 V		5.4	6.0	6.6	V
V_{CEsat} (Terminal)	Collector-emitter saturation voltage	I_C=50 A, V_{GE}=15 V, Refer to the figure of test circuit (Note5)	T_{vj}=25 °C	-	1.40	1.85	V
			T_{vj}=125 °C	-	1.55	-	
			T_{vj}=150 °C	-	1.60	-	
V_{CEsat} (Chip)		I_C=50 A, V_{GE}=15 V, (Note5)	T_{vj}=25 °C	-	1.30	1.55	V
			T_{vj}=125 °C	-	1.35	-	
			T_{vj}=150 °C	-	1.35	-	
C_{ies}	Input capacitance	V_{CE}=10 V, G-E short-circuited		-	-	6.7	nF
C_{oes}	Output capacitance			-	-	0.3	
C_{res}	Reverse transfer capacitance			-	-	0.1	
Q_G	Gate charge	V_{CC}=300 V, I_C=50 A, V_{GE}=15 V		-	0.21	-	µC
$t_{d(on)}$	Turn-on delay time	V_{CC}=300 V, I_C=50 A, V_{GE}=±15 V, R_G=12 Ω, Inductive load		-	-	400	ns
t_r	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	400	
t_f	Fall time			-	-	600	
V_{EC} (Note1) (Terminal)	Emitter-collector voltage	I_E=50 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T_{vj}=25 °C	-	1.50	2.00	V
			T_{vj}=125 °C	-	1.60	-	
			T_{vj}=150 °C	-	1.65	-	
V_{EC} (Note1) (Chip)		I_E=50 A, G-E short-circuited, (Note5)	T_{vj}=25 °C	-	1.35	1.75	V
			T_{vj}=125 °C	-	1.35	-	
			T_{vj}=150 °C	-	1.35	-	
t_{rr} (Note1)	Reverse recovery time	V_{CC}=300 V, I_E=50 A, V_{GE}=±15 V, R_G=12 Ω, Inductive load		-	-	400	ns
Q_{rr} (Note1)	Reverse recovery charge			-	4.0	-	µC
E_{on}	Turn-on switching energy per pulse	V_{CC}=300 V, I_C=I_E=50 A, V_{GE}=±15 V, R_G=12Ω, T_{vj}=150 °C, Inductive load		-	1.6	-	mJ
E_{off}	Turn-off switching energy per pulse			-	2.6	-	
E_{rr} (Note1)	Reverse recovery energy per pulse			-	2.4	-	mJ
r_g	Internal gate resistance	Per switch		-	0	-	Ω

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I_{CES}	Collector-emitter cut-off current	$V_{CE}=V_{CES}$, G-E short-circuited		-	-	1.0	mA
I_{GES}	Gate-emitter leakage current	$V_{GE}=V_{GES}$, C-E short-circuited		-	-	0.5	µA
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C=3.5 mA, V_{CE}=10 V		5.4	6.0	6.6	V
V_{CEsat} (Terminal)	Collector-emitter saturation voltage	I_C=35 A, V_{GE}=15 V, Refer to the figure of test circuit (Note5)	T_{vj}=25 °C	-	1.35	1.80	V
			T_{vj}=125 °C	-	1.45	-	
			T_{vj}=150 °C	-	1.50	-	
V_{CEsat} (Chip)		I_C=35 A, V_{GE}=15 V, (Note5)	T_{vj}=25 °C	-	1.25	1.50	V
			T_{vj}=125 °C	-	1.30	-	
			T_{vj}=150 °C	-	1.30	-	
C_{ies}	Input capacitance	V_{CE}=10 V, G-E short-circuited		-	-	4.7	nF
C_{oes}	Output capacitance			-	-	0.2	
C_{res}	Reverse transfer capacitance			-	-	0.1	
Q_G	Gate charge	V_{CC}=300 V, I_C=35 A, V_{GE}=15 V		-	0.14	-	µC
$t_{d(on)}$	Turn-on delay time	V_{CC}=300 V, I_C=35 A, V_{GE}=±15 V, R_G=18 Ω, Inductive load		-	-	400	ns
t_r	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	400	
t_f	Fall time			-	-	600	

ELECTRICAL CHARACTERISTICS (cont.; T_{vj}=25 °C, unless otherwise specified)

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
E_{on}	Turn-on switching energy per pulse	V_{CC}=300 V, V_{GE}=±15 V, T_{vj}=150 °C, Inductive load	I_C=35 A, R_G=18 Ω	-	0.9	-	mJ
E_{off}	Turn-off switching energy per pulse			-	1.9	-	
E_{rr}	Reverse recovery energy per pulse		I_E=35 A, R_G=18 Ω	-	1.8	-	
r_g	Internal gate resistance	-		-	0	-	Ω
I_{RRM}	Reverse current	V_R=V_{RRM}, G-E short-circuited		-	-	1.0	mA
V_F (Terminal)	Forward voltage	I_F=35 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T_{vj}=25 °C	-	1.55	2.10	V
			T_{vj}=125 °C	-	1.65	-	
			T_{vj}=150 °C	-	1.70	-	
V_F (Chip)		I_F=35 A, G-E short-circuited, (Note5)	T_{vj}=25 °C	-	1.45	1.85	V
			T_{vj}=125 °C	-	1.50	-	
			T_{vj}=150 °C	-	1.50	-	
t_{rr}	Reverse recovery time	V_{CC}=300 V, I_F=35 A, V_{GE}=±15 V, R_G=15 Ω, Inductive load		-	-	400	ns
Q_{rr}	Reverse recovery charge			-	3.0	-	μC

CONVERTER PART DIODE

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I_{RRM}	Repetitive peak reverse current	V_R=V_{RRM}, T_{vj}=150 °C		-	-	20	mA
V_F (Terminal)	Forward voltage	I_F=50A	T_{vj}=25 °C	-	1.20	1.65	V
			T_{vj}=150 °C	-	1.15	-	
V_F (chip)			T_{vj}=25 °C	-	1.10	1.35	
			T_{vj}=150 °C	-	1.05	-	

NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R_{25}	Zero-power resistance	T_C=25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R_{100}=493 Ω, T_C=100 °C (Note4)	-7.3	-	+7.8	%
$B_{(25/50)}$	B-constant	Approximate by equation (Note6)	-	3375	-	K
P_{25}	Power dissipation	T_C=25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	554	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)	-	-	720	
$R_{th(j-c)Q}$		Junction to case, Brake IGBT (Note4)	-	-	625	
$R_{th(j-c)D}$		Junction to case, Brake DIODE (Note4)	-	-	1346	
$R_{th(j-c)D}$		Junction to case, per Converter DIODE (Note4)			834	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7, 9)	-	20.2	-	K/kW

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MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
M_s	Mounting torque	Mounting to heat sink M 5 screw		2.5	3.0	3.5	N·m
d_s	Creepage distance	Solder pin type(MXUB)	Terminal to terminal	10.9	-	-	mm
			Terminal to base plate	18.3	-	-	
		Pressfit pin type(MXUBP)	Terminal to terminal	5.2	-	-	
			Terminal to base plate	16.1	-	-	
d_a	Clearance	Solder pin type(MXUB)	Terminal to terminal	6.5	-	-	
			Terminal to base plate	13.1	-	-	
		Pressfit pin type(MXUBP)	Terminal to terminal	5.0	-	-	
			Terminal to base plate	16.1	-	-	
e_c	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+200	μm
m	mass	-		-	165	-	g

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across P-N(P1-N1) terminals	-	300	450	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G*P-*/G*N-E/GB-E terminals (*=U,V,W)	13.5	15.0	16.5	V
R_G	External gate resistance	Inverter IGBT, Per switch	12	-	120	Ω
		Brake IGBT	18	-	180	

*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.

3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed T_{vjmax} rating.

4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

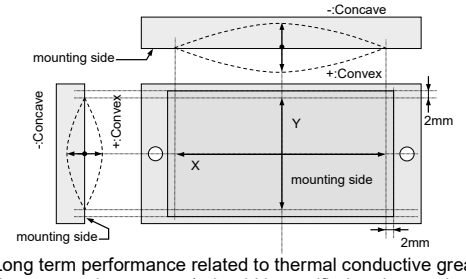
6. $B_{(25/50)} = \ln(\frac{R_{25}}{R_{50}}) / (\frac{1}{T_{25}} - \frac{1}{T_{50}})$

   R_{25}: resistance at absolute temperature T_{25} [K]; T_{25}=25 [°C]+273.15=298.15 [K]

   R_{50}: resistance at absolute temperature T_{50} [K]; T_{50}=50 [°C]+273.15=323.15 [K]

7. Reference value. Thermally conductive grease of thermal conductivity λ=0.9 W/(m·K) and thickness $D_{(C-S)}$=50 μm.

8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



9. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under user's specific application conditions. Each temperature condition ($T_{vj\,max}$, $T_{vj\,op}$, $T_{C\,max}$) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

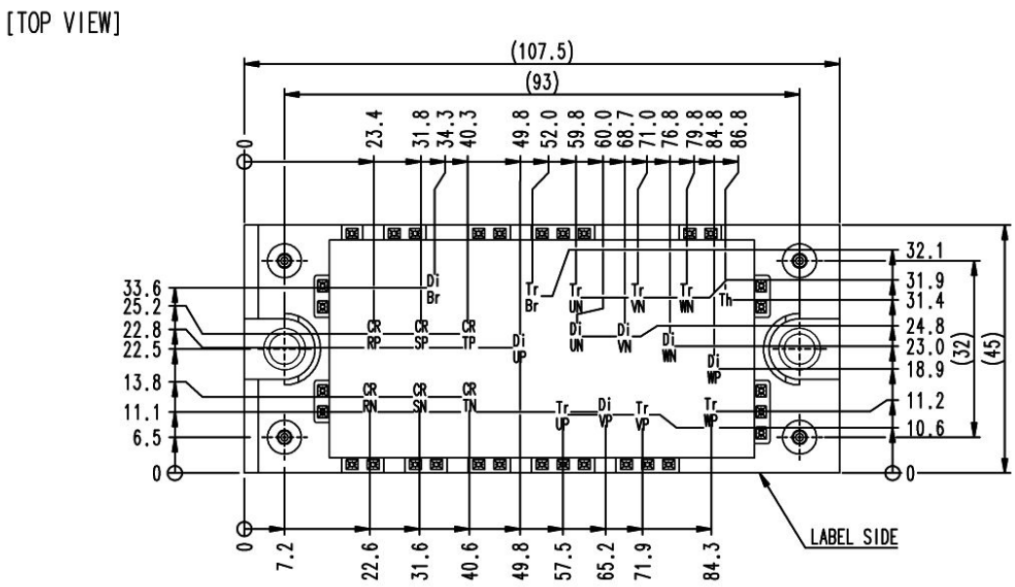
10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness : t1.6

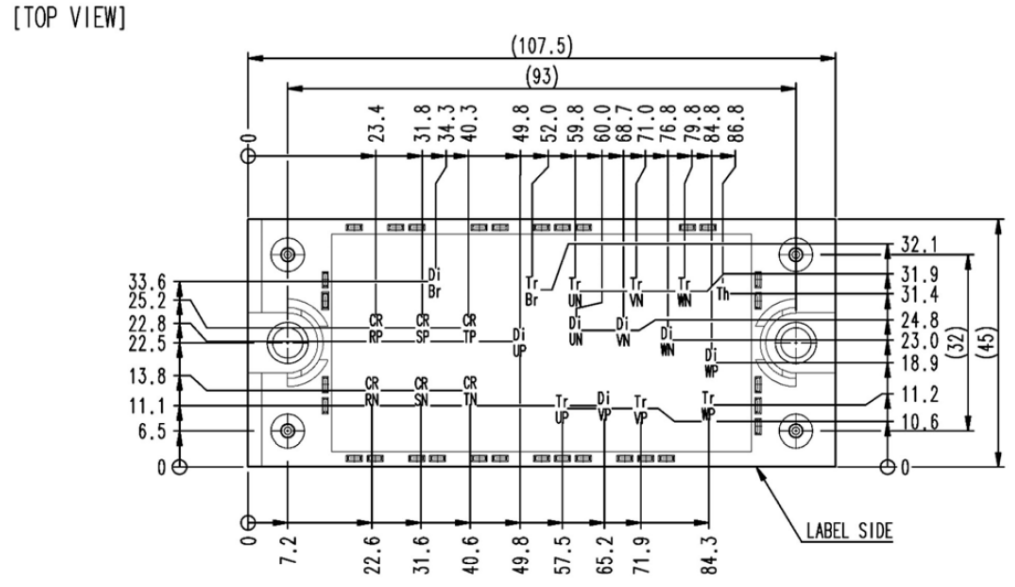
	Type	Manufacturer	Size	Tightening torque (N・m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	by handwork (equivalent to 30 rpm by mechanical screw driver) ~ 600 rpm (by mechanical screw driver)
(2)	PT®		K25×10	0.75 ± 0.075 N・m	
(3)	DELTA PT®		25×8	0.55 ± 0.055 N・m	
(4)	DELTA PT®		25×10	0.75 ± 0.075 N・m	
(5)	B1 tapping screw	-	φ2.6×10	0.75 ± 0.075 N・m	
			φ2.6×12		

CHIP LOCATION (Top view) Dimension in mm, tolerance: ±1 mm

MXUB



MXUBP



Tr*P/Tr*N/TrBr: IGBT, Di*P/Di*N: DIODE (*=U/V/W), DiBr: BRAKE DIODE,
CR*P/CR*N: CONVERTER DIODE (*=R/S/T), Th: NTC thermistor

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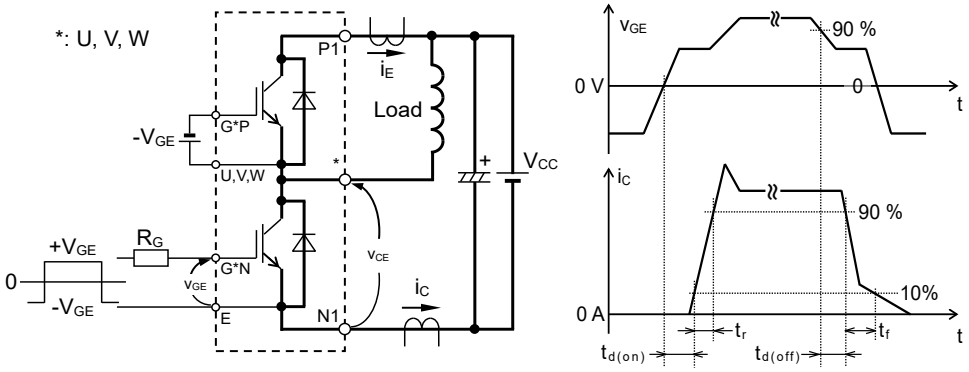
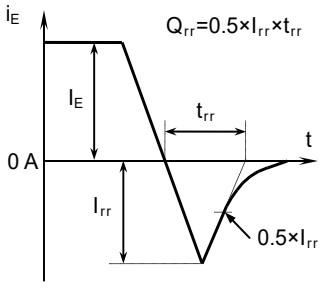
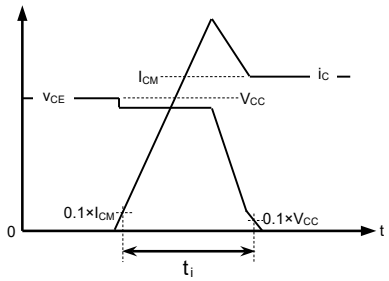
TEST CIRCUIT AND WAVEFORMS



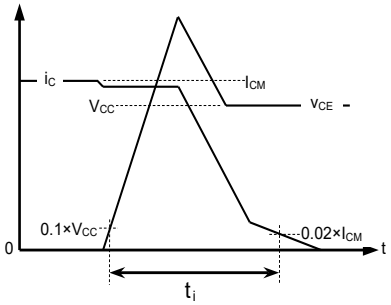
Switching characteristics test circuit and waveforms



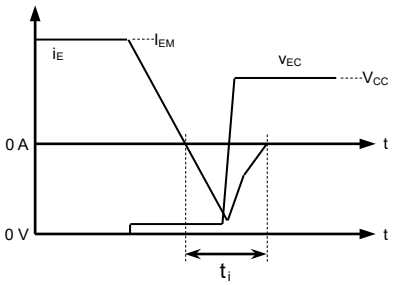
t_{rr}, Q_{rr} characteristics test waveform



IGBT Turn-on switching energy



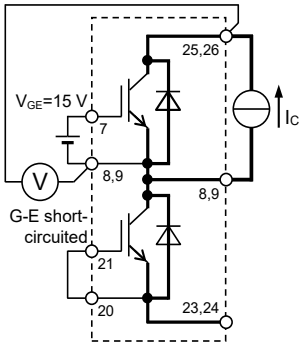
IGBT Turn-off switching energy



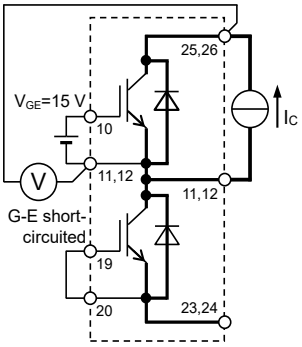
FWD Reverse recovery energy

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

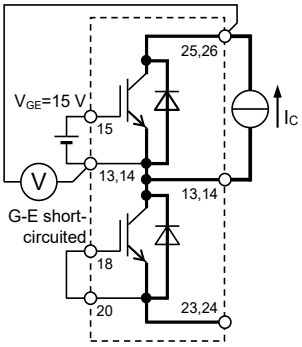
TEST CIRCUIT



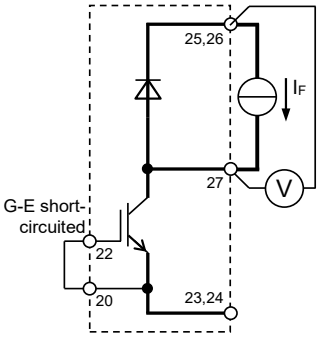
TrUP



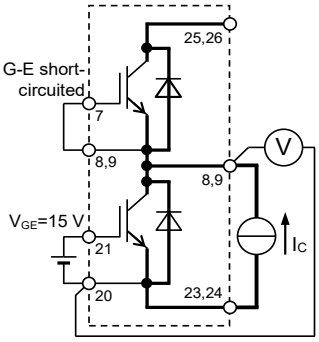
TrVP



TrWP

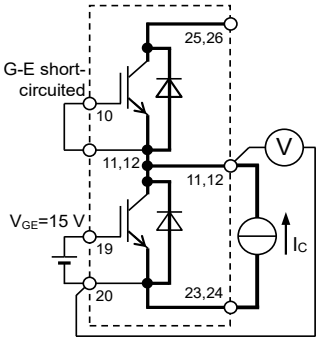


Brake DIODE



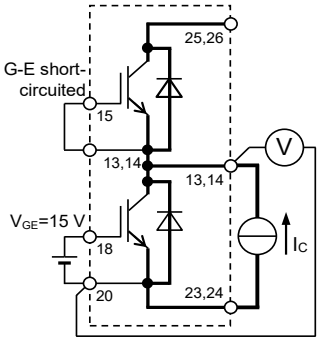
TrUN

Gate-emitter short-circuited: GVP-V, GVN-E, GWP-W, GWN-E, GB-E



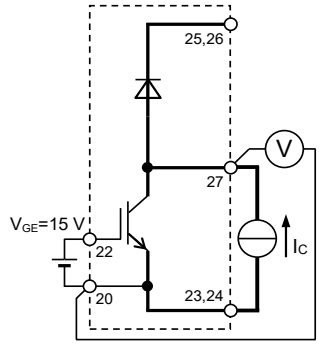
TrVN

Gate-emitter short-circuited: GUP-U, GUN-E, GWP-W, GWN-E, GB-E



TrWN

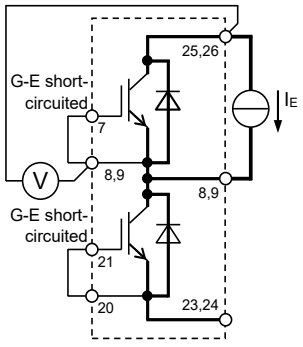
Gate-emitter short-circuited: GUP-U, GUN-E, GVP-V, GVN-E, GB-E



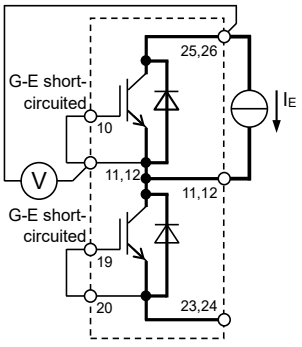
Brake IGBT

Gate-emitter short-circuited: GUP-U, GUN-E, GVP-V, GVN-E, GWP-W, GWN-E

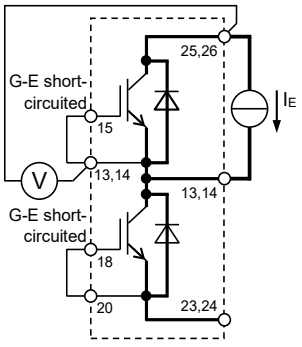
V_{CEsat} /BRAKE DIODE V_F characteristics test circuit



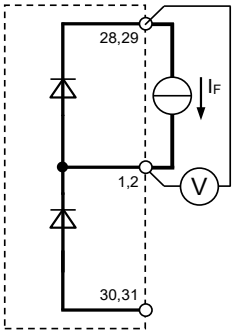
DiUP

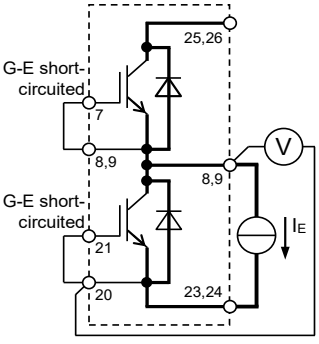


DiVP



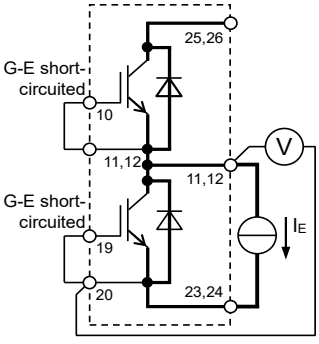
DiWP





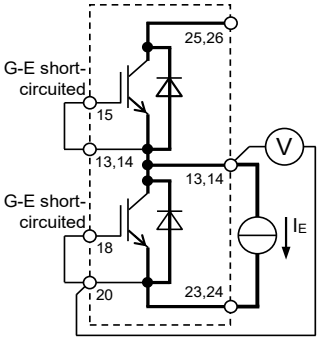
DiUN

Gate-emitter short-circuited: GVP-V, GVN-E, GWP-W, GWN-E, GB-E



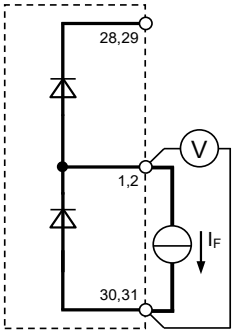
DiVN

Gate-emitter short-circuited: GUP-U, GUN-E, GWP-W, GWN-E, GB-E



DiWN

Gate-emitter short-circuited: GUP-U, GUN-E, GVP-V, GVN-E, GB-E



CONVERTER DIODE (ex.phase-R)

V_{EC} / CONVERTER DIODE V_F characteristics test circuit

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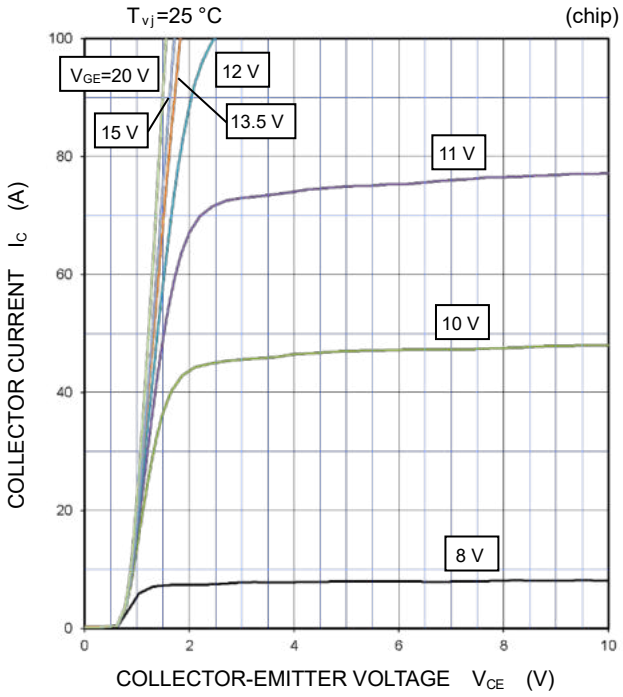
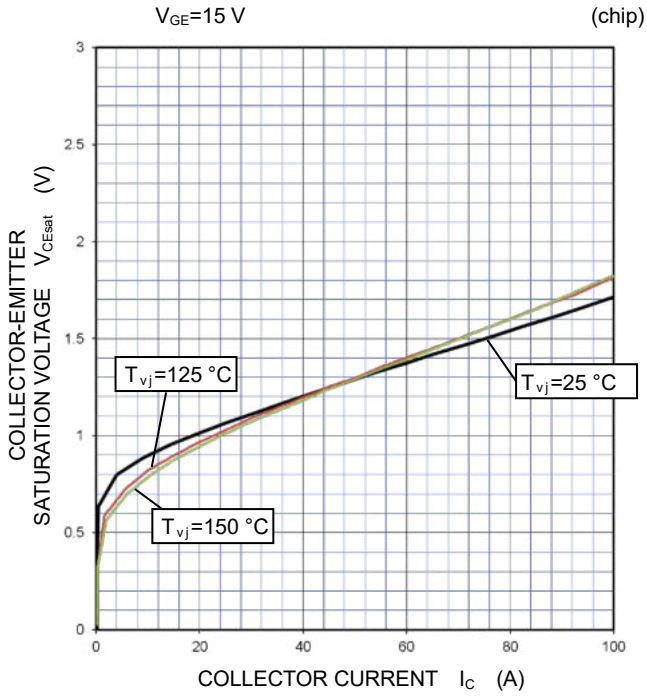
PERFORMANCE CURVES

INVERTER PART

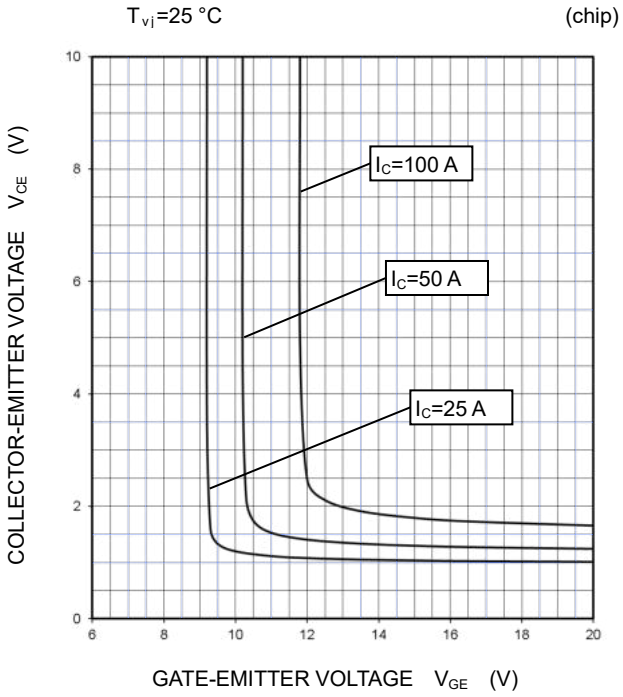
OUTPUT CHARACTERISTICS (TYPICAL)



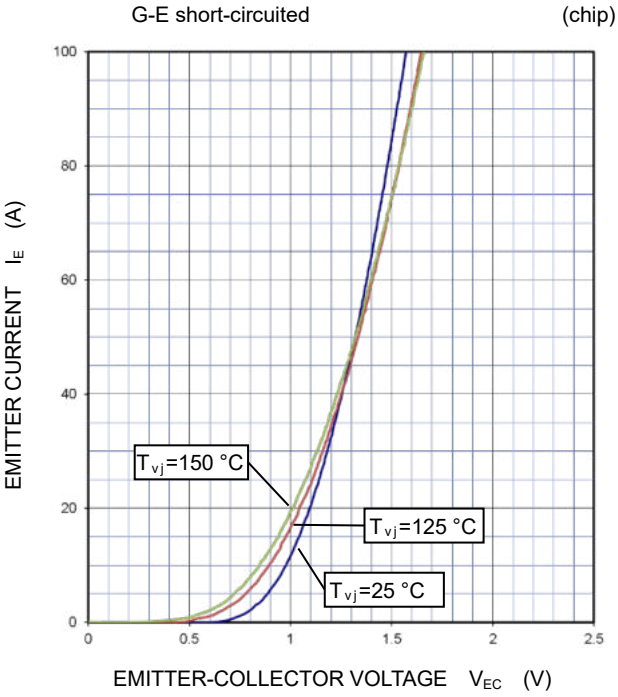
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

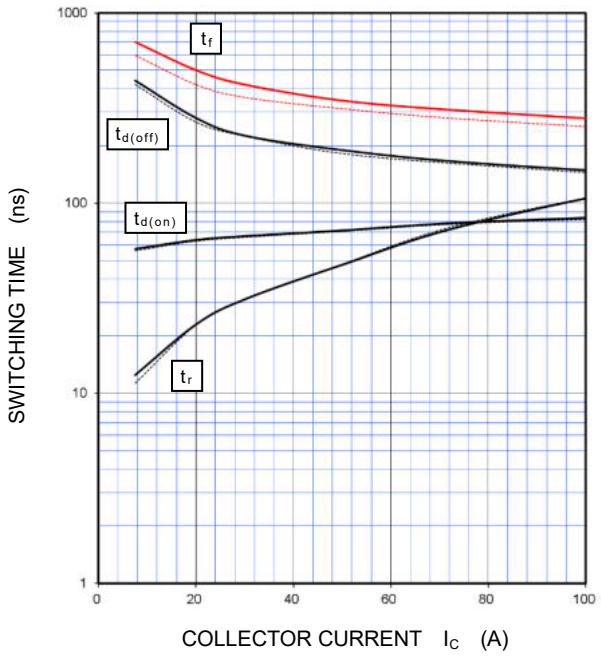


PERFORMANCE CURVES

INVERTER PART

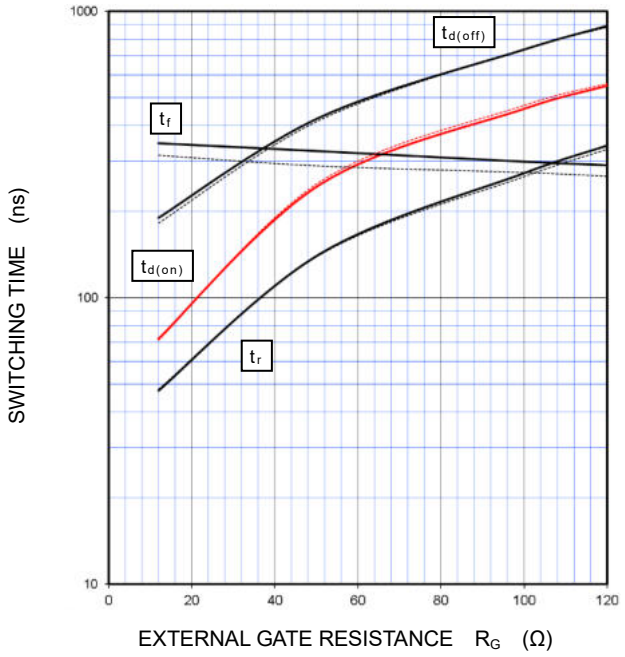
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, R_G=12 Ω, V_{GE}=±15 V, INDUCTIVE LOAD
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



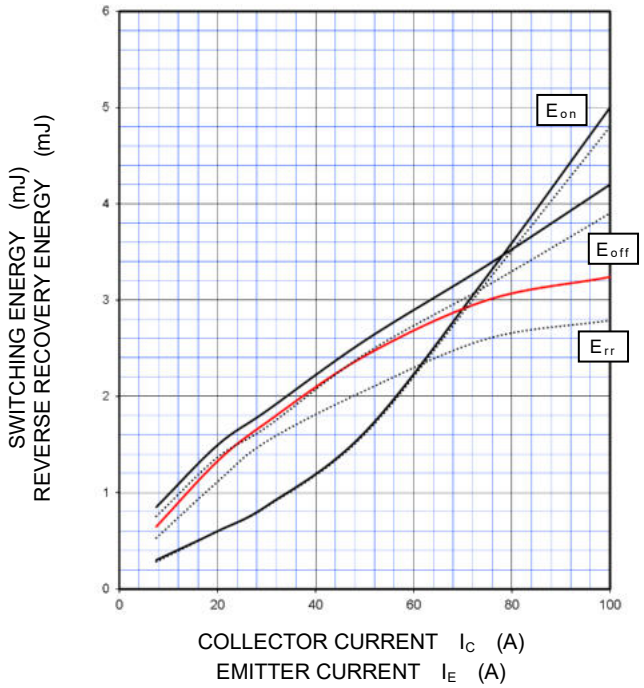
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, I_C=50 A, V_{GE}=±15 V, INDUCTIVE LOAD
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



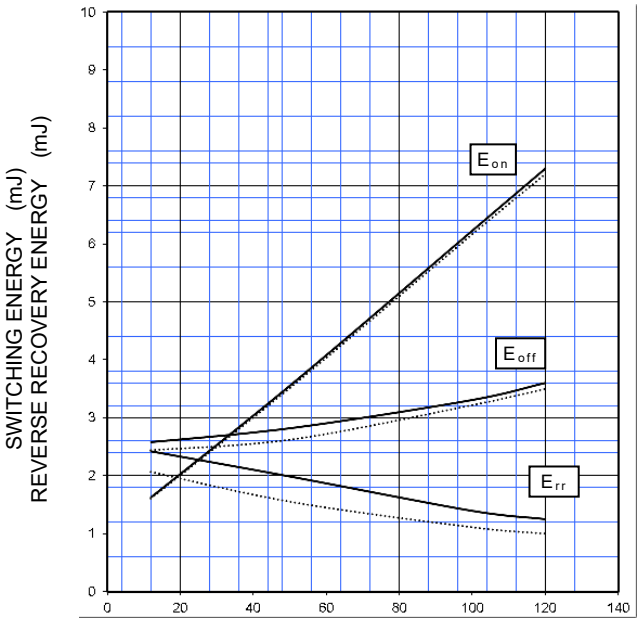
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, R_G=12 Ω, V_{GE}=±15 V, INDUCTIVE LOAD,
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C, PER PULSE



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, I_C/I_E=50 A, V_{GE}=±15 V, INDUCTIVE LOAD,
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C, PER PULSE



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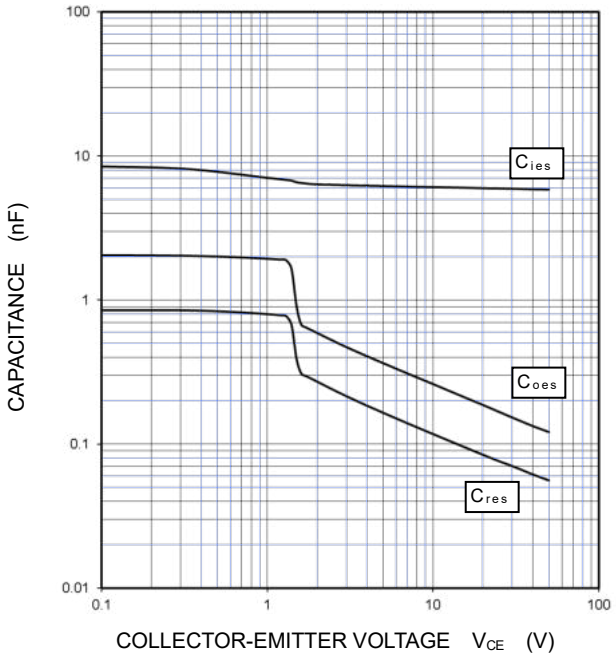
PERFORMANCE CURVES

INVERTER PART

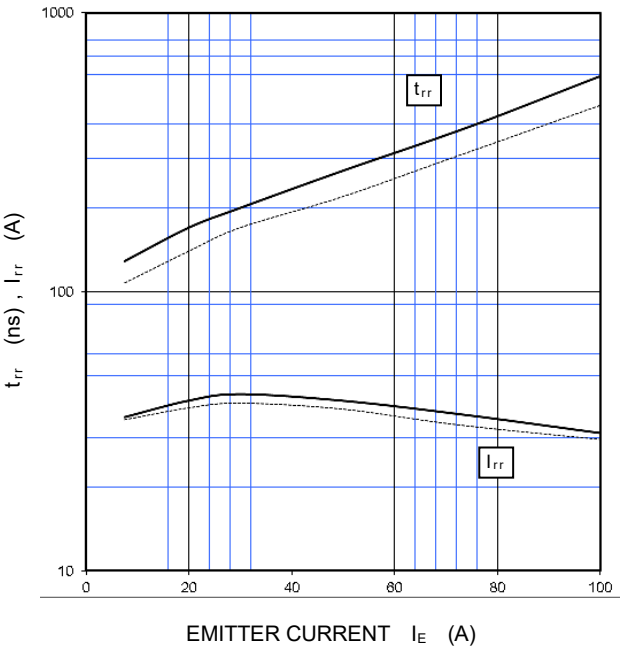
CAPACITANCE CHARACTERISTICS (TYPICAL)

G-E short-circuited, T_{vj}=25 °C



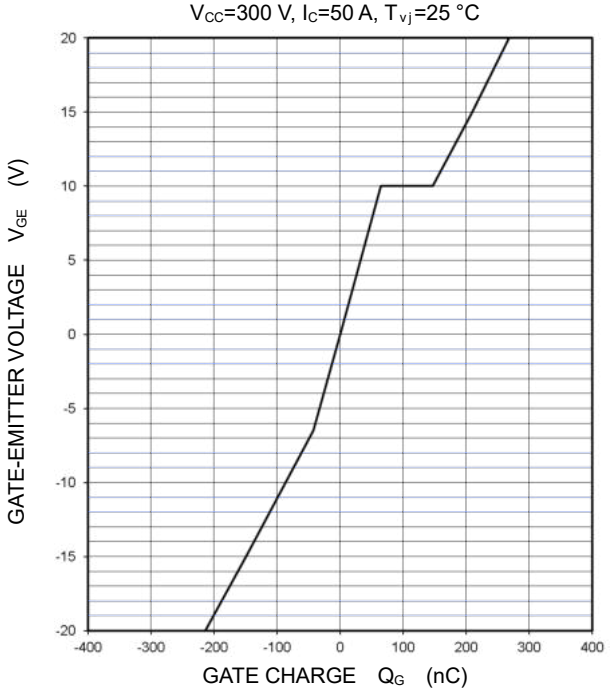
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, R_G=12 Ω, V_{GE}=±15 V, INDUCTIVE LOAD
————: T_j=150 °C, - - - - -: T_j=125 °C



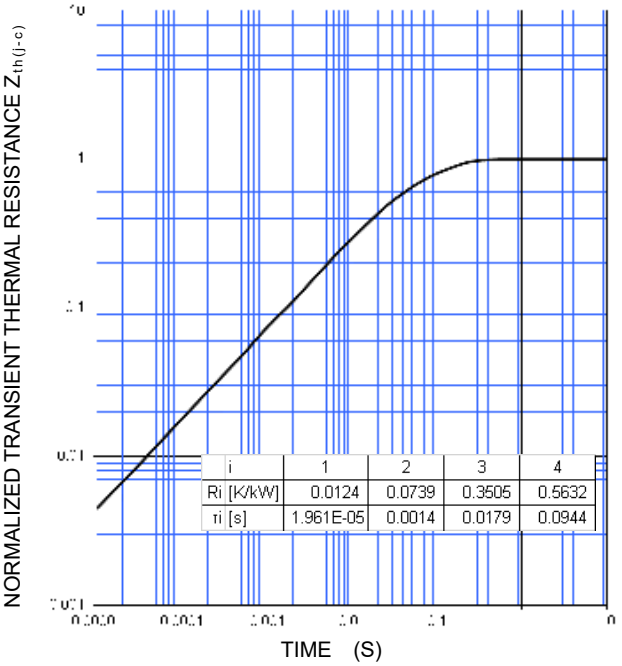
GATE CHARGE CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, I_C=50 A, T_{vj}=25 °C



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C=25 °C
$R_{th(j-c)Q}$=554 K/kW, $R_{th(j-c)D}$=720 K/kW



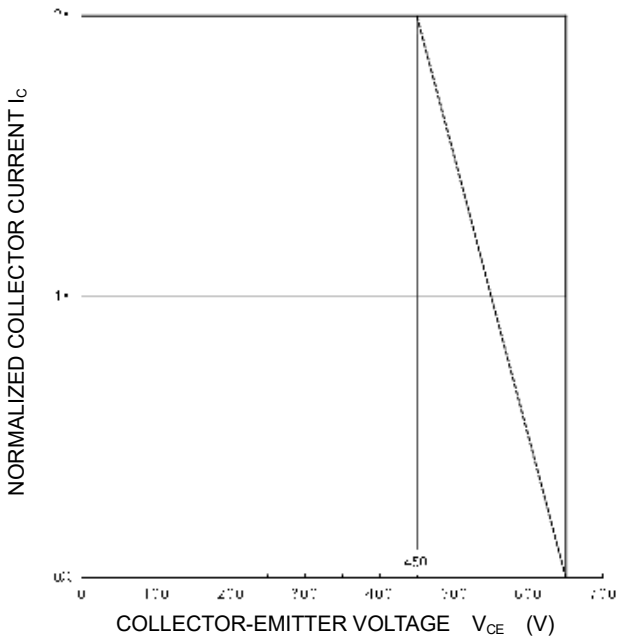
i	1	2	3	4
Ri [K/kW]	0.0124	0.0739	0.3505	0.5632
ri [s]	1.961E-05	0.0014	0.0179	0.0944

PERFORMANCE CURVES

INVERTER PART

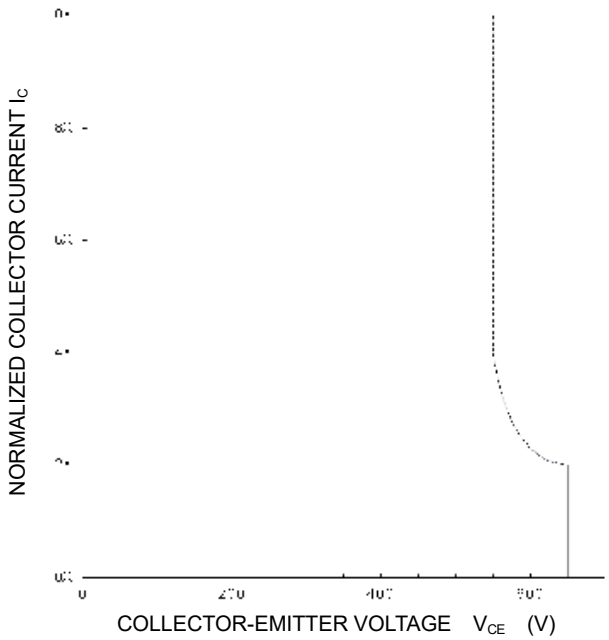
TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

V_{CC}≤450 V, R_G=12~120 Ω, V_{GE}=±15 V,
————: T_{vj}=25~150 °C (Normal load operations (Continuous)
- - - - - -: T_{vj}=175 °C (Unusual load operations (Limited period)



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

V_{CC}≤400 V, R_G=12~120 Ω, V_{GE}=±15 V,
T_{vj}= 25 ~ 150 °C, t_W≤8 μs, Non-Repetitive



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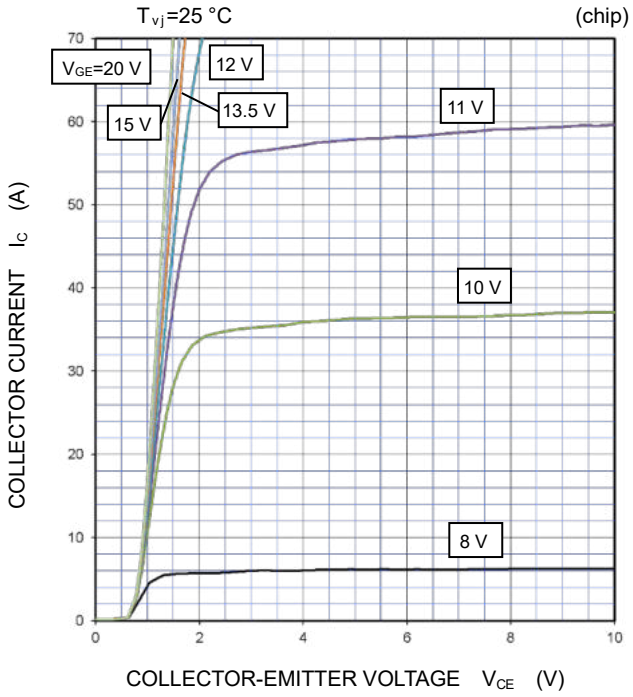
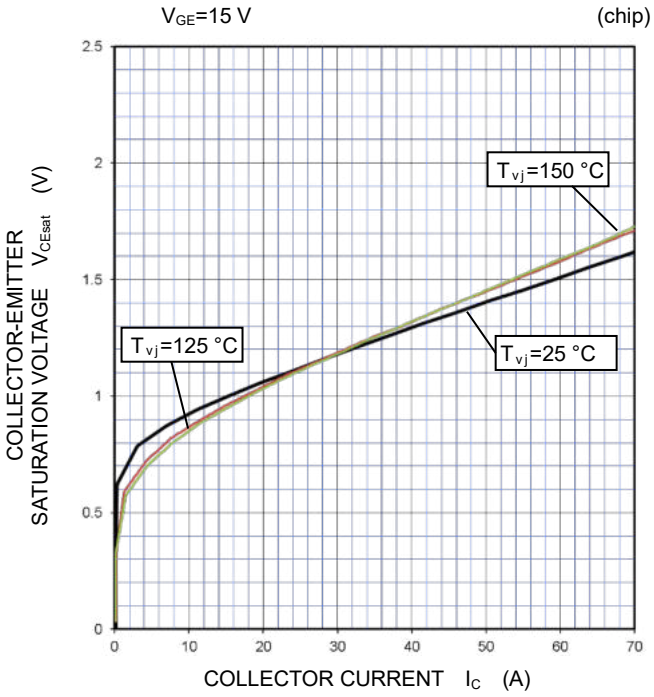
PERFORMANCE CURVES

BRAKE PART

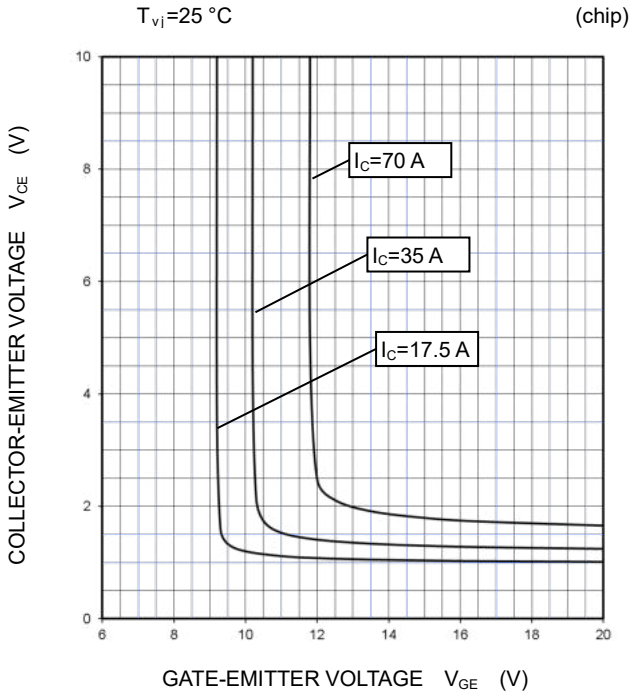
OUTPUT CHARACTERISTICS (TYPICAL)



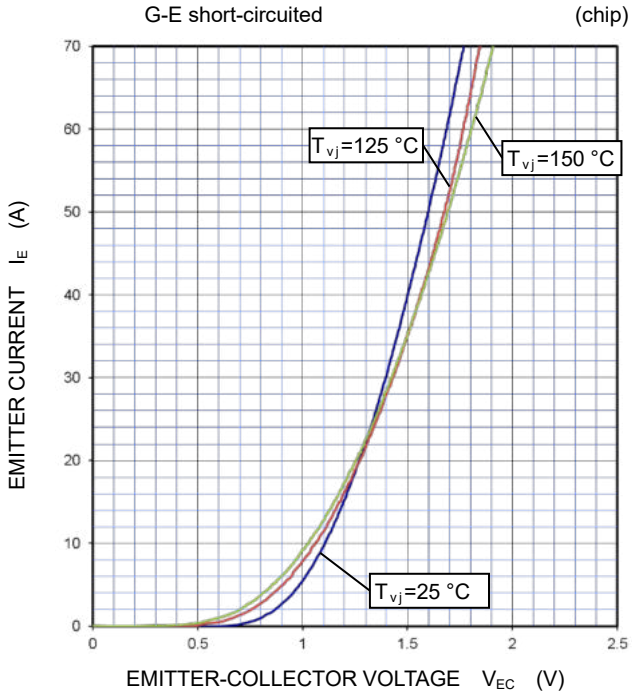
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



DIODE FORWARD CHARACTERISTICS (TYPICAL)

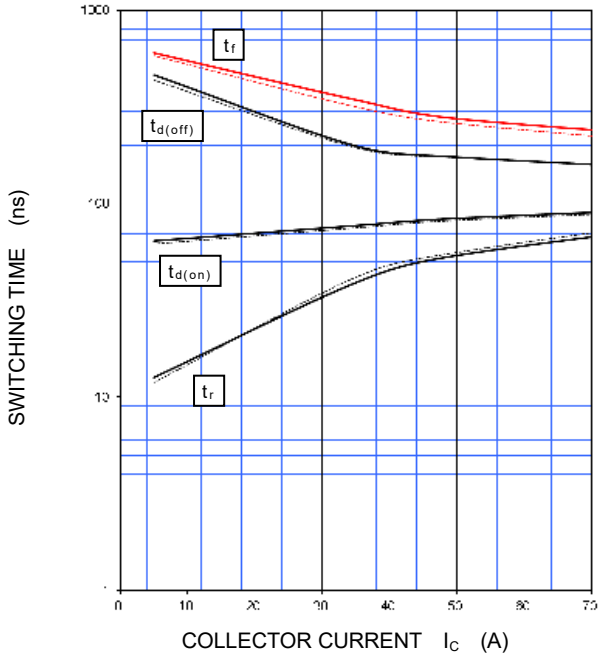


PERFORMANCE CURVES

BRAKE PART

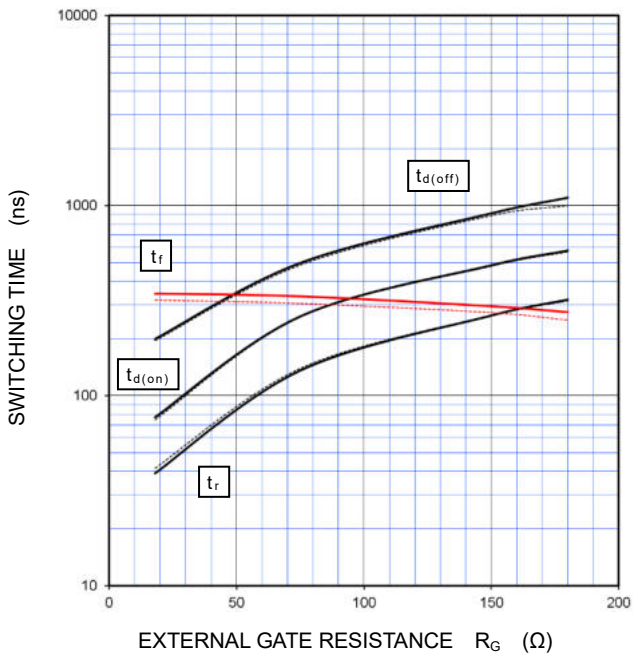
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, R_G=18 Ω, V_{GE}=±15 V, INDUCTIVE LOAD
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



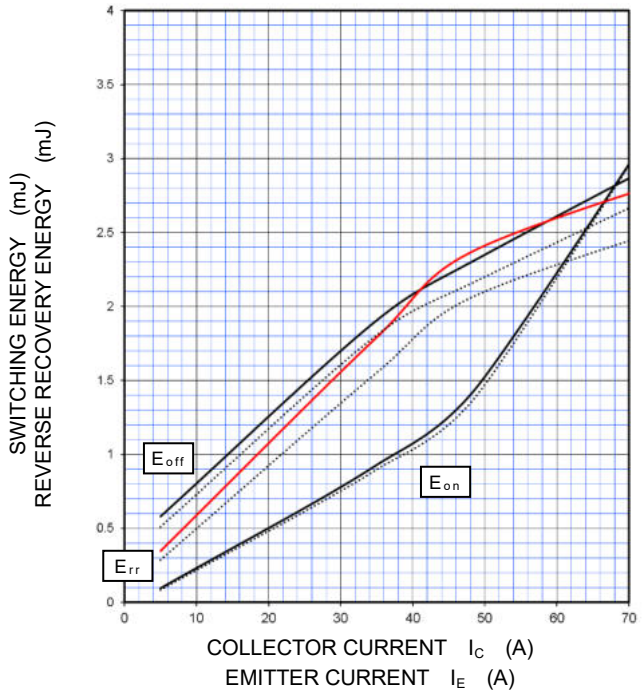
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, I_C=35 A, V_{GE}=±15 V, INDUCTIVE LOAD
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



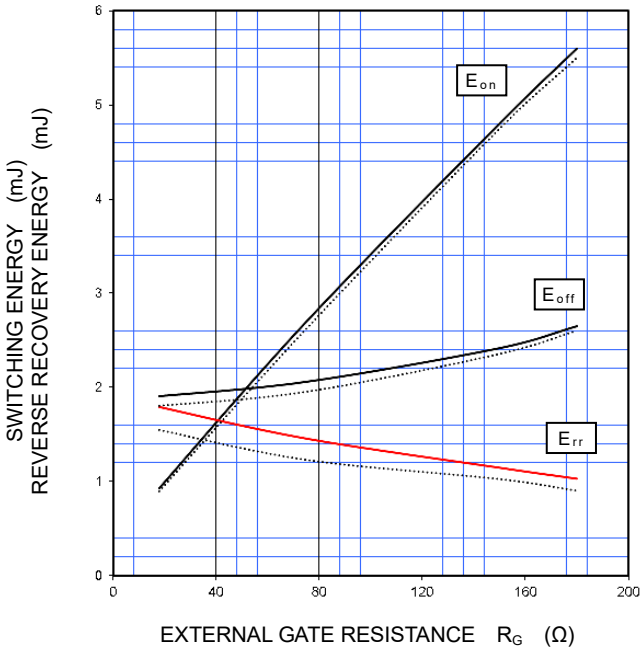
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, R_G=18 Ω, V_{GE}=±15 V, INDUCTIVE LOAD,
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C, PER PULSE



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, I_C/I_E=35 A, V_{GE}=±15 V, INDUCTIVE LOAD,
——————: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C, PER PULSE



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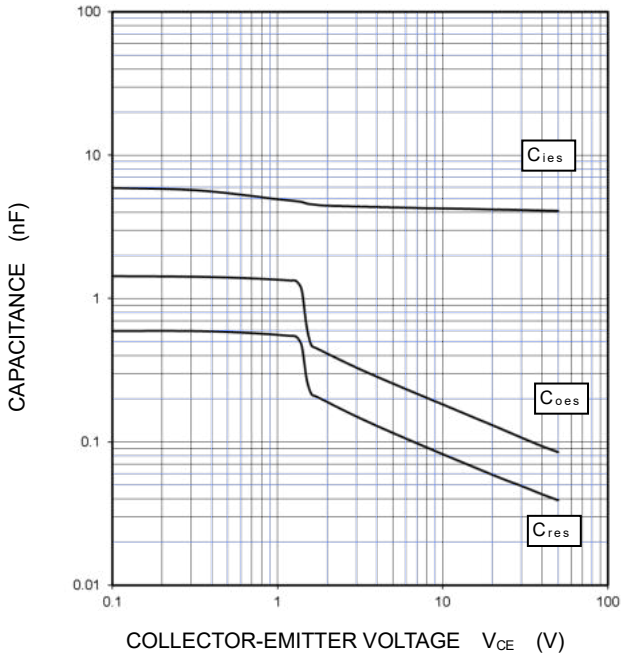
PERFORMANCE CURVES

BRAKE PART

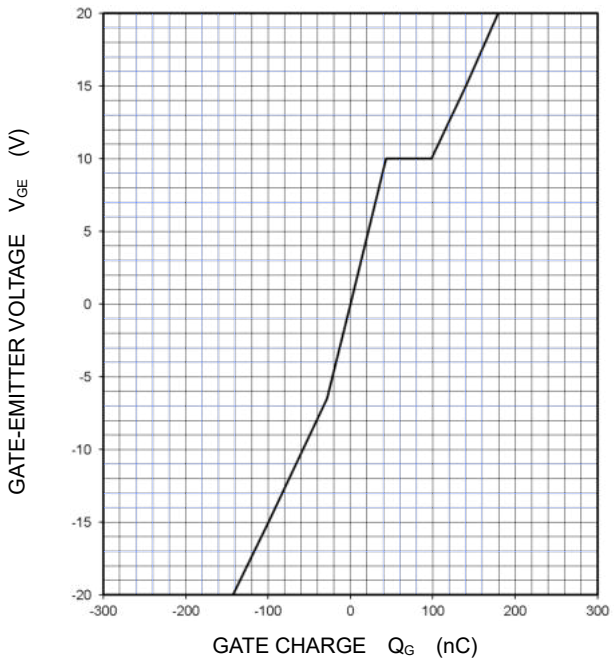
CAPACITANCE CHARACTERISTICS (TYPICAL)

G-E short-circuited, T_{vj}=25 °C



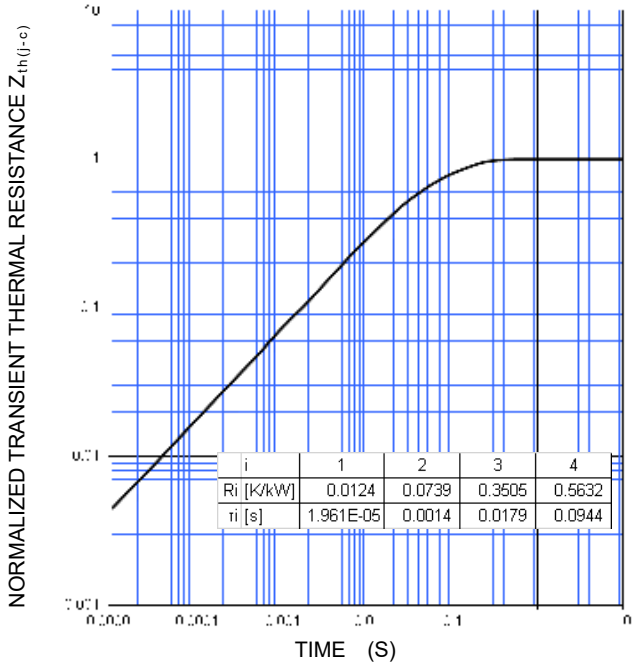
GATE CHARGE CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, I_C=35 A, T_{vj}=25 °C



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C=25 °C
$R_{th(j-c)Q}$=625 K/kW, $R_{th(j-c)D}$=1346 K/kW

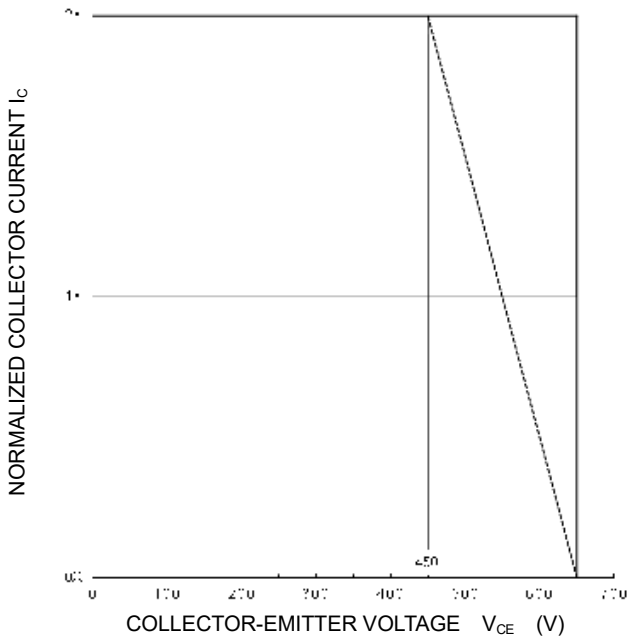


PERFORMANCE CURVES

BRAKE PART

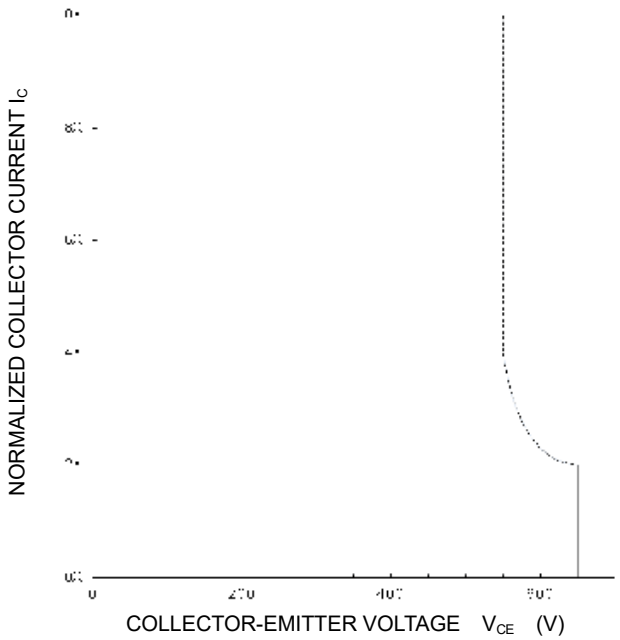
TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

V_{CC}≤450 V, R_G=18~180 Ω, V_{GE}=±15 V,
————: T_{vj}=25~150 °C (Normal load operations (Continuous)
- - - - - -: T_{vj}=175 °C (Unusual load operations (Limited period)



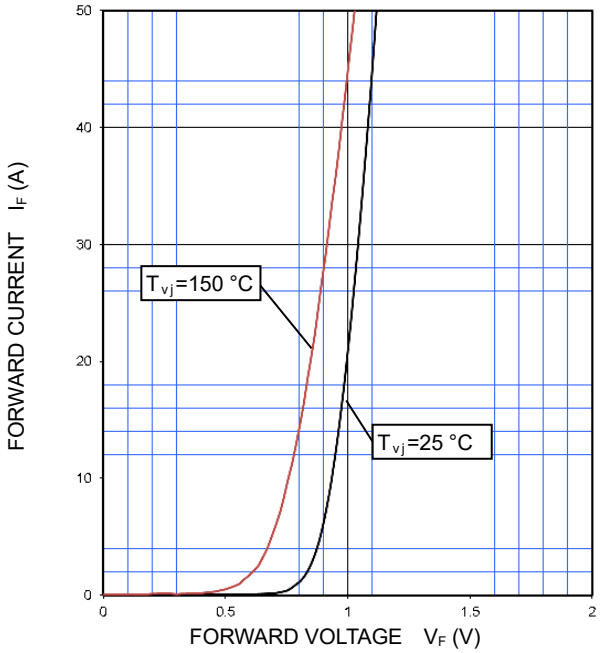
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

V_{CC}≤400 V, R_G=18~180 Ω, V_{GE}=±15 V,
T_{vj}= 25 ~ 150 °C, t_W≤8 μs, Non-Repetitive



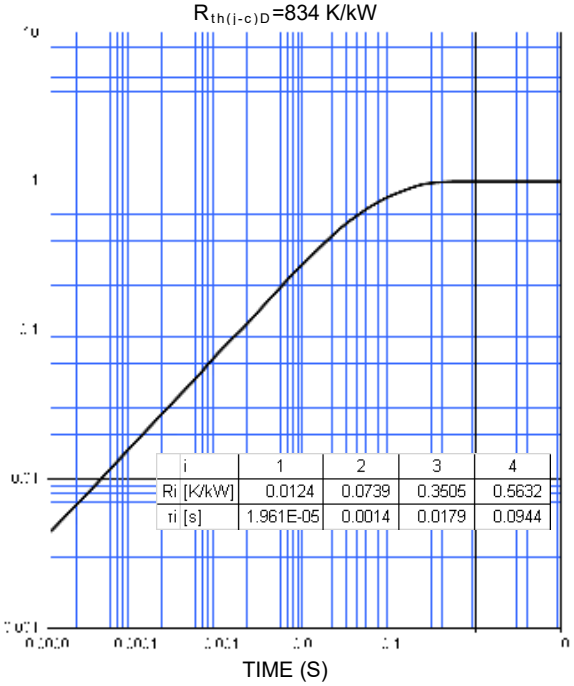
CONVERTER PART

CONVERTER DIODE FORWARD CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C=25 °C
$R_{th(j-c)D}$=834 K/kW



i	1	2	3	4
Ri [K/kW]	0.0124	0.0739	0.3505	0.5632
τi [s]	1.961E-05	0.0014	0.0179	0.0944

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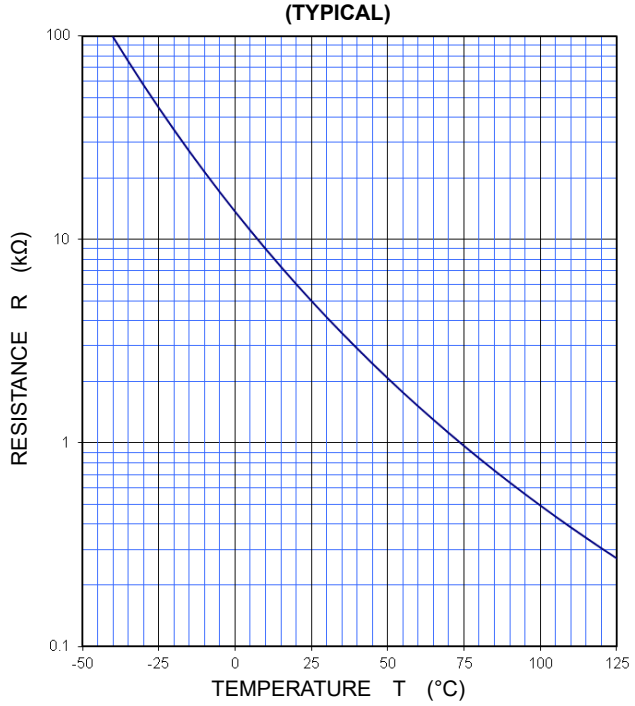
PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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