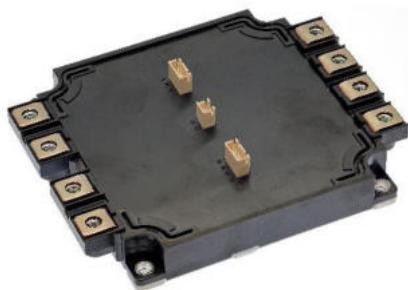


<Full SiC Power Modules>

FMF1200DXZ-24B

 HIGH POWER SWITCHING USE
INSULATED TYPE


Dual switch (Half-Bridge)

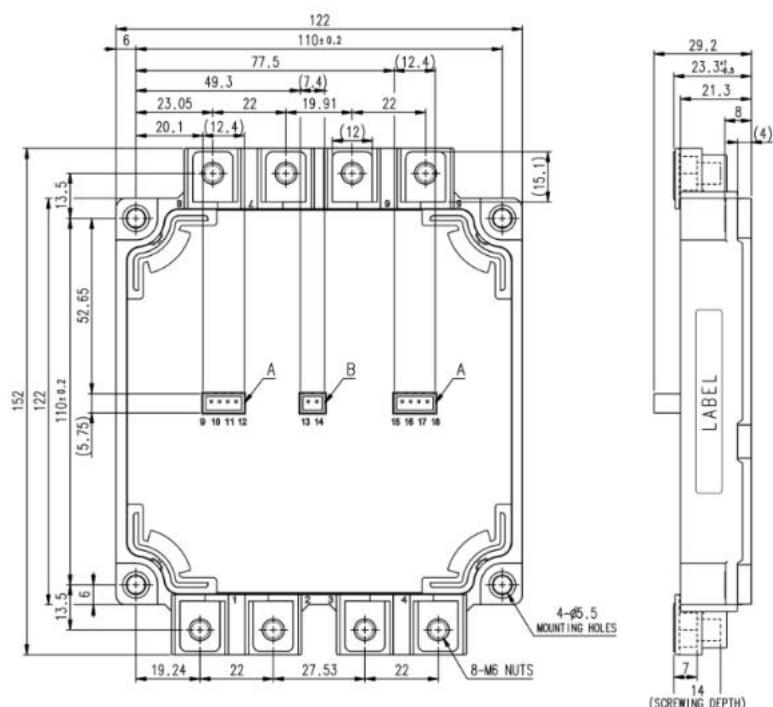
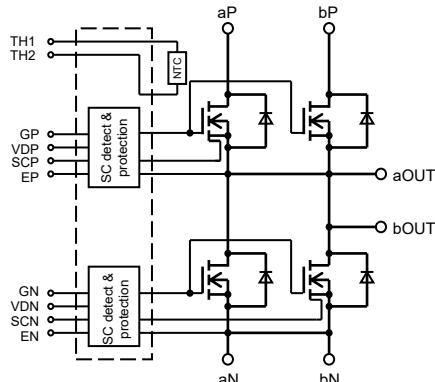
Drain current I_D 1200 A
 Drain-Source voltage V_{DSX} 1200 V
 Maximum junction temperature T_{vjmax} 175 °C
 •Silicon Carbide MOSFET + Silicon Carbide Schottky Barrier Diode
 •Flat base Type
 •Copper base plate
 •RoHS Directive compliant
 •Recognized under UL1557, File E323585

APPLICATION

AC Motor Control, Power supply, etc.

OUTLINE DRAWING & INTERNAL CONNECTION

Dimension in mm


INTERNAL CONNECTION


Terminal	code	Terminal	code	Tolerance otherwise specified
				Division of Dimension Tolerance
1	aP	10	SCP	0.5 to 3 ±0.2
2	aN	11	GP	over 3 to 6 ±0.3
3	bN	12	EP	over 6 to 30 ±0.5
4	bP	13	TH1	over 30 to 120 ±0.8
5	bOUT	14	TH2	over 120 to 400 ±1.2
6	bOUT	15	EN	
7	aOUT	16	GN	
8	aOUT	17	SCN	
9	VDP	18	VDN	

 aP and bP, aN and bN, aOUT-bOUT
must be connected externally.

FMF1200DXZ-24BHIGH POWER SWITCHING USE
INSULATED TYPE**MAXIMUM RATINGS (T_{vj} = 25 °C, unless otherwise specified)**

Symbol	Item	Conditions	Rating	Unit
V _{DSX}	Drain-source voltage	V _{GS} =-15 V	1200	V
V _{GSS}	Gate-source voltage	D-S short-circuited	±20	V
I _D	Drain current	DC, T _C =61°C (Note.2)	1200	A
I _{DRM}	I _D	Pulse, Repetitive (Note.3), T _{vj} =150°C (Note.4)	1800	
P _{tot}	Total power dissipation	T _C =25 °C (Note.2)	4540	W
I _S (Note.1)	Source current	DC	1200	A
I _{SRM} (Note.1)	I _S	Pulse, Repetitive (Note.3), T _{vj} =150°C	1800	
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	5000	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note.10)	175	°C
T _{vjop}	Operating junction temperature	Continuous operation (under switching) (Note.10)	-40~+150	°C
T _{cmax}	Maximum case temperature	(Note.2, 10)	125	°C
T _{stg}	Storage temperature	-	-40~+125	°C

ELECTRICAL CHARACTERISTICS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions (note9)		Limits	Unit		
			Min.	Typ.			
I _{DSX}	Drain-source cut-off current	V _{DS} =V _{DSX} , V _{GS} =-15 V	-	-	mA		
		V _{DS} =800V, V _{GS} =-15 V	-	-			
V _{GS(th)}	Gate-source threshold voltage	I _D =360 mA, V _{DS} =10 V	1.8	2.5	3.2	V	
I _{GSS}	Gate-source leakage current	V _{GS} =V _{GSS} , D-S short-circuited	-	-	0.5	µA	
V _{DS(on)} (terminal)	Drain-source on-state voltage	I _D =1200 A, V _{GS} =15V (Note.6)	T _{vj} =25 °C	-	1.60	2.36	V
			T _{vj} =125 °C	-	2.03	-	
			T _{vj} =150 °C	-	2.12	-	
V _{DS(on)} (chip)	Drain-source on-state voltage	I _D =1200 A, V _{GS} =15V (Note.6)	T _{vj} =25 °C	-	1.30	-	V
			T _{vj} =125 °C	-	1.73	-	
			T _{vj} =150 °C	-	1.82	-	
r _{DS(on)} (chip)	Drain-source on-state resistance	I _D =1200 A, V _{GS} =15V (Note.6)	T _{vj} =25 °C	-	1.1	-	mΩ
			T _{vj} =125 °C	-	1.4	-	
			T _{vj} =150 °C	-	1.5	-	
C _{iss}	Input capacitance	V _{DS} =10 V, V _{GS} =0V	-	89.2	-	nF	
C _{oss}	Output capacitance		-	64.7	-		
C _{rss}	Reverse transfer capacitance		-	5.3	-		
Q _G	Gate charge	V _{DD} =600 V, I _D =1200 A, V _{GS} =0→15 V	-	2570	-	nC	
t _{d(on)}	Turn-on delay time	V _{DD} =600 V, I _D =1200 A, V _{GS} =±15 V, T _{vj} =150°C, R _G =1.0Ω, L _{s_ext} =16nH, Inductive load, per pulse	-	350	-	ns	
t _r	Rise time		-	120	-		
t _{d(off)}	Turn-off delay time		-	440	-		
t _f	Fall time		-	80	-		
E _{on}	Turn-on switching energy		-	66	-	mJ	
E _{off}	Turn-off switching energy		-	54	-		
Q _C	Drain-source charge		-	6.3	-	µC	
V _{SD} (Note.1) (terminal)	Source-drain voltage	I _S =1200 A (Note.6) V _{GS} =-15 V	T _{vj} =25 °C	-	1.87	2.40	V
			T _{vj} =125 °C	-	2.63	-	
			T _{vj} =150 °C	-	2.82	-	
V _{SD} (Note.1) (chip)	Source-drain voltage	I _S =1200 A (Note.6) V _{GS} =-15 V	T _{vj} =25 °C	-	1.57	-	V
			T _{vj} =125 °C	-	2.33	-	
			T _{vj} =150 °C	-	2.52	-	
R _{DD'+SS'}	Internal lead resistance	P-EP, OUT-EN terminals, per switch	-	0.25	-	mΩ	
L _s	Internal stray inductance	P-N	-	15	-	nH	
r _{g(on)}	Internal gate resistance (on)	Per switch	-	0.65	-	Ω	
r _{g(off)}	Internal gate resistance (off)	Per switch	-	3.07	-	Ω	

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance ^(Note. 2)	Junction to case, per inverter switch	-	-	33	K/kW
$R_{th(j-c)D}$		Junction to case, per inverter FWD	-	-	47	
$R_{th(c-s)}$	Contact thermal resistance ^(Note.2)	Case to heat sink, per 1 module, Thermal grease applied ^(Note.8, 10)	-	6	-	K/kW

NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R_{25}	Zero-power resistance	$T_c=25\text{ }^{\circ}\text{C}$ ^(Note.2)	4.85	5.00	5.15	k Ω
$\Delta R/R$	Deviation of resistance	$T_c=100\text{ }^{\circ}\text{C}$ ^(Note.2) , $R_{100}=493\text{ }\Omega$	-7.3	-	+7.8	%
$B_{(25/50)}$	B-constant	Approximate by equation ^(Note.7)	-	3375	-	K
P_{25}	Power dissipation	$T_c=25\text{ }^{\circ}\text{C}$ ^(Note.2)	-	-	10	mW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M_t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5
M_s		Mounting to heat sink	M 5 screw	2.5	3.0	3.5
m	mass	-	-	920	-	g
d_a	Clearance		10.0	-	-	mm
d_s	Creepage distance		17.5	-	-	mm
e_c	Flatness of base plate	On the centerline X, Y ^(Note.5)	-100	-	+120	μm
-	Connector insertion force	2 pin type	0	-	25	N
-		4 pin type	0	-	35	N

*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU)2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, source-drain free wheeling diode (FWD).

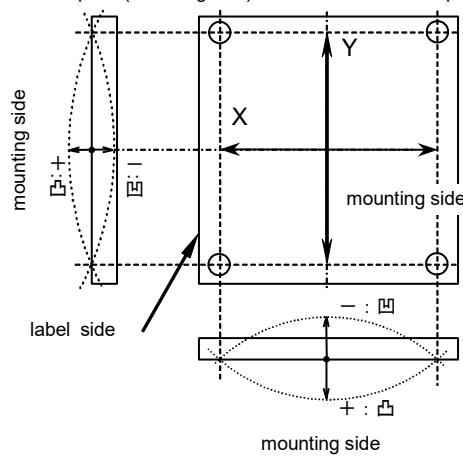
2. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips.

Refer to the figure of chip location.

3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) does not exceed $T_{vj\max}$ rating.

4. Junction temperature (T_{vj}) should not increase beyond $T_{vj\max}$ rating.

5. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



6. Pulse width and repetition rate should be such as to cause negligible temperature rise.

$$7. B_{(25/50)} = \ln\left(\frac{R_{25}}{R_{50}}\right) / \left(\frac{1}{T_{25}} - \frac{1}{T_{50}}\right)$$

R_{25} : resistance at absolute temperature T_{25} [K]; $T_{25}=25\text{ }^{\circ}\text{C}+273.15=298.15\text{ [K]}$

R_{50} : resistance at absolute temperature T_{50} [K]; $T_{50}=50\text{ }^{\circ}\text{C}+273.15=323.15\text{ [K]}$

8. Reference value. Thermally conductive grease of $\lambda=0.9\text{ W/(m}\cdot\text{K)}$ and thickness $D_{(c-s)}=100\mu\text{m}$.

9. Per switch (ex. Tr1 chips total in page.6)

10. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition ($T_{vj\max}$, $T_{vj\text{ op}}$, $T_c\max$) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

FMF1200DXZ-24BHIGH POWER SWITCHING USE
INSULATED TYPE**RECOMMENDED OPERATING CONDITIONS**

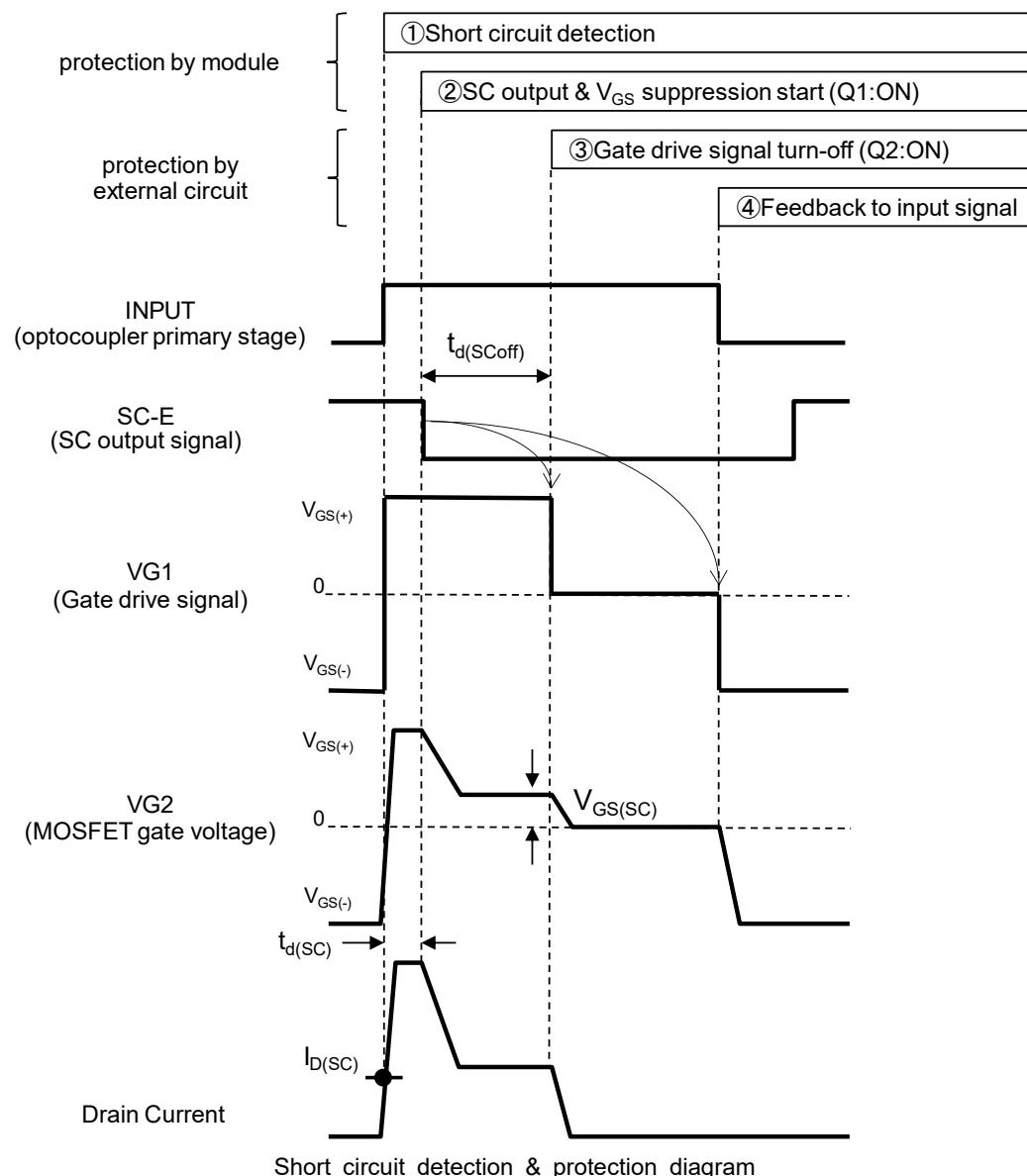
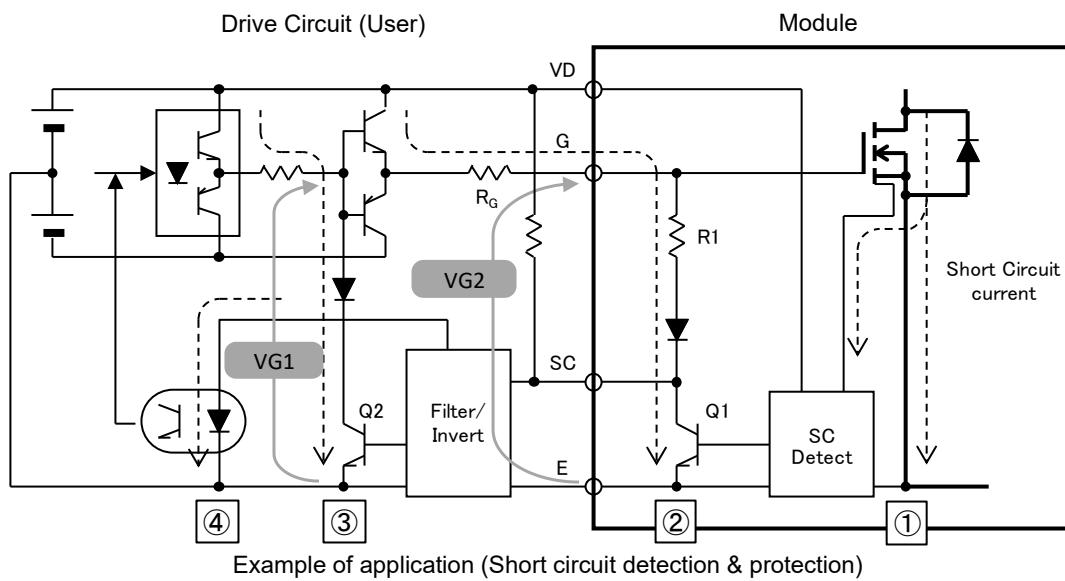
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{DD}	(DC) Supply voltage	Applied across aP-aN, bP-bN terminals	-	600	850	V
V_D	DC supply voltage (control)	Applied across VDP-EP, VDN-EN terminals	13.5	15.0	16.5	V
$V_{GS(+)}$	Gate-Source positive drive voltage	Applied across GP-EP, GN-EN terminals	13.5	15.0	16.5	V
$V_{GS(-)}$	Gate-Source negative drive voltage	Applied across GP-EP, GN-EN terminals	-16.5	-15.0	-7.0	V
R_G	External gate resistance ^(Note.11)	Per switch	1.0	-	5.0	Ω
f_c	Switching frequency	$V_{GS}=\pm 15V$, $R_G=1.0\Omega$, $V_{DD}=600V$, $T_{vj}=150^\circ C$	-	-	50	kHz
$t_{d(SCoff)}$	Gate cutoff delay time after SC output	$V_{GS}=15V$, $R_G=1.0\Omega$, $V_{DD}\leq 850V$, $T_{vj}=150^\circ C$	-	-	3	μs

Note 11. The value of external gate resistance should be considered the surge voltage not to exceed the rating voltage in the worst system condition.

SHORT CIRCUIT DETECTION & PROTECTION CHARACTERISTICS

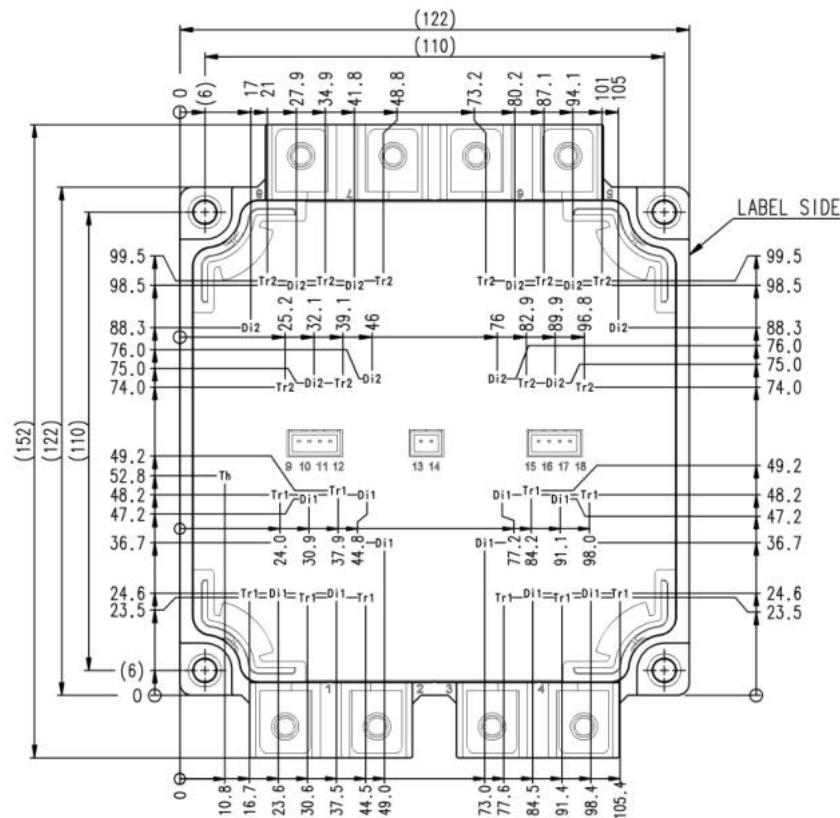
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$I_{D(SC)}$	SC detect drain current	$T_{vj}=150^\circ C$, $V_{GS}=15V$	1800	2400	-	A
$t_{d(SC)}$	SC detect delay time	$T_{vj}=150^\circ C$, $V_{GS}=15V$, $R_G=1.0\Omega$	-	1	-	μs
$V_{GS(SC)}$	SC protection gate limit voltage	$T_{vj}=150^\circ C$, $V_{GS}=15V$, $R_G=1.0\Omega$	-	5.8	-	V
R1	SC protection gate limit resistance	-	-	0.50	-	Ω

Refer to the circuit in page.5

SHORT CIRCUIT DETECTION & PROTECTION

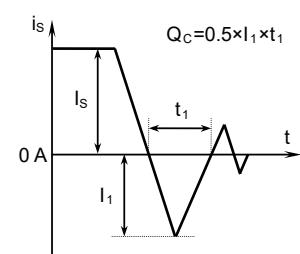
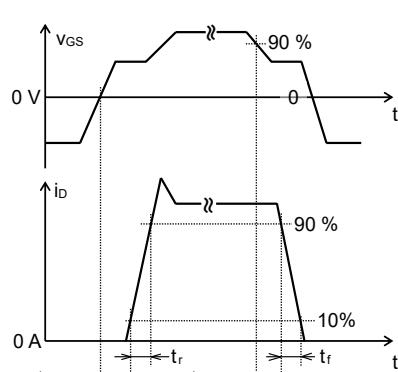
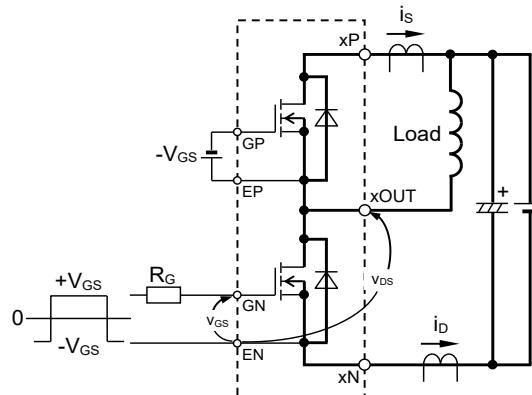
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ± 1 mm



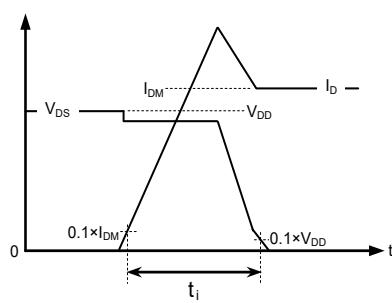
Tr1,Tr2: SiC-MOSFET, Di1,Di2: SiC-SBD, Th: NTC thermistor

TEST CIRCUIT AND WAVEFORMS

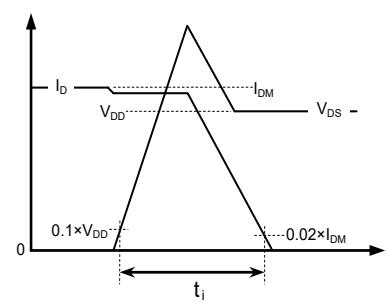


Switching characteristics test circuit and waveforms(x: connected a* and b*)

QC test waveform



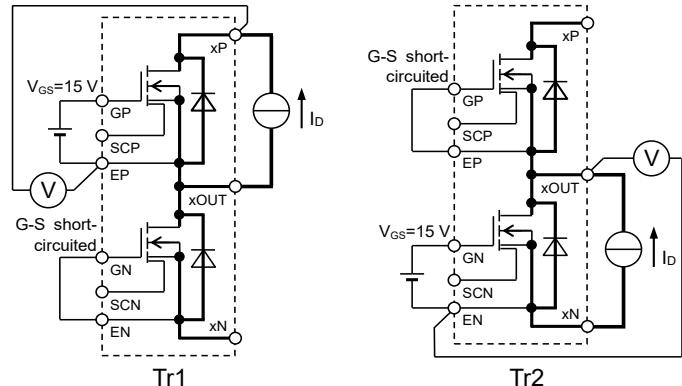
MOSFET Turn-on switching energy



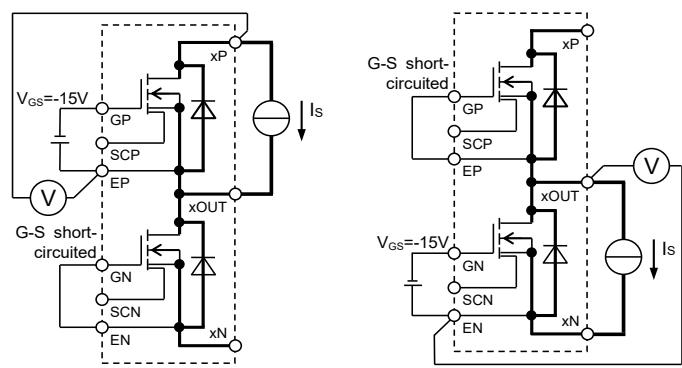
MOSFET Turn-off switching energy

Turn-on / Turn-off switching energy test waveforms (Integral time instruction drawing)

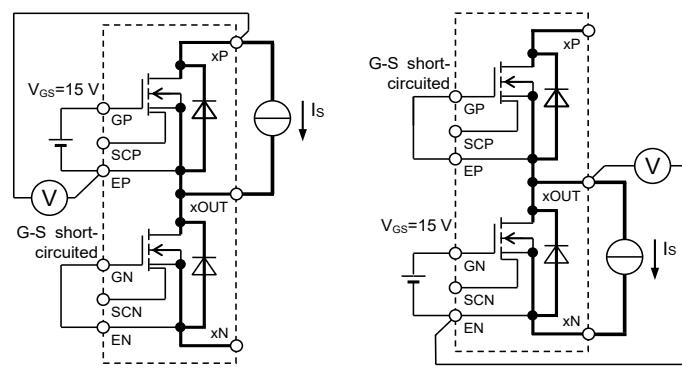
TEST CIRCUIT



V_{DS(on)} test circuit (x: Connected a* and b*)

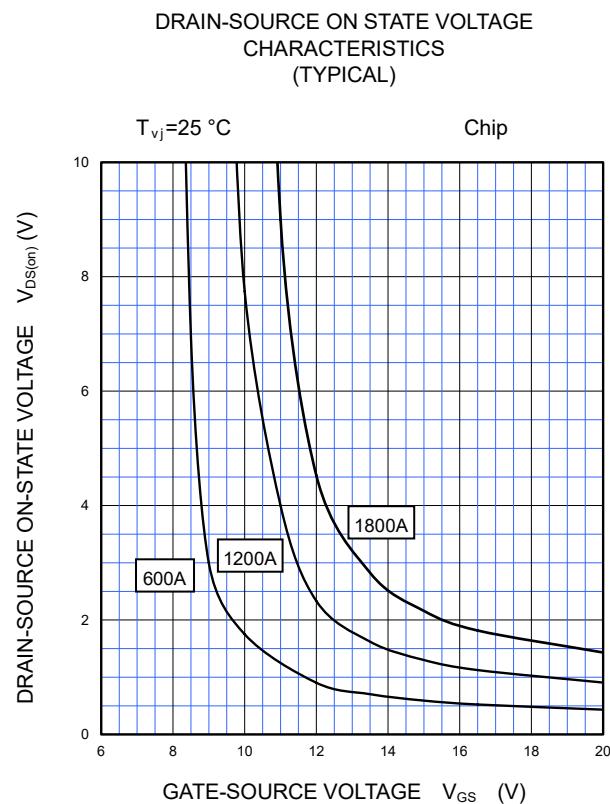
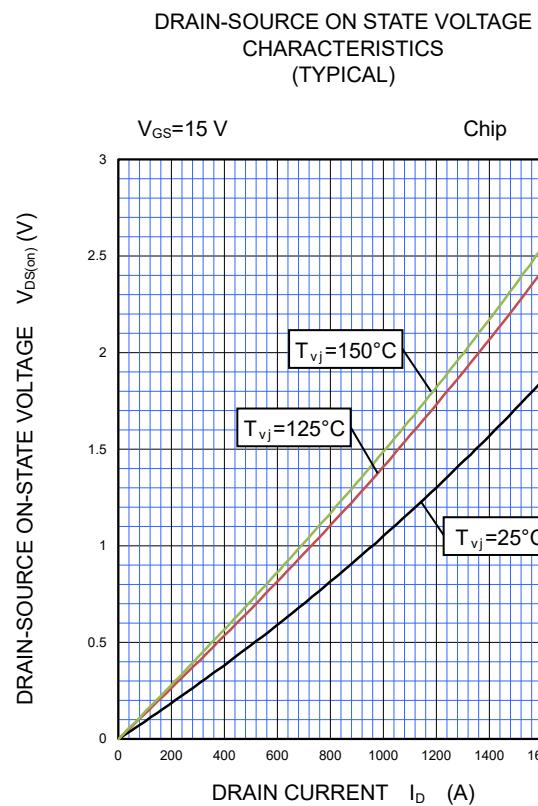
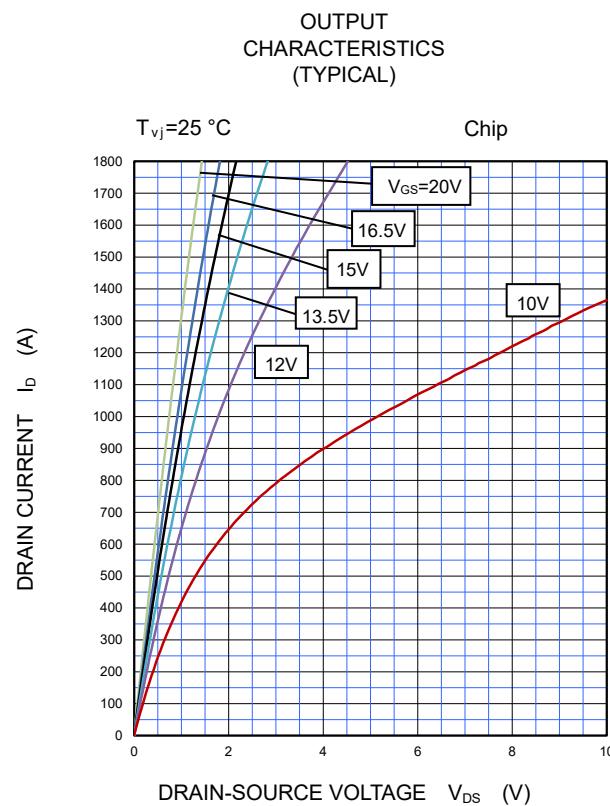


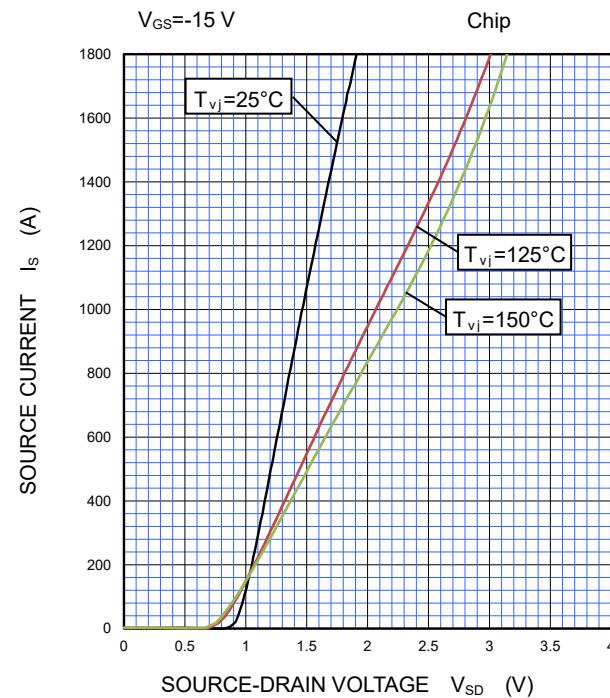
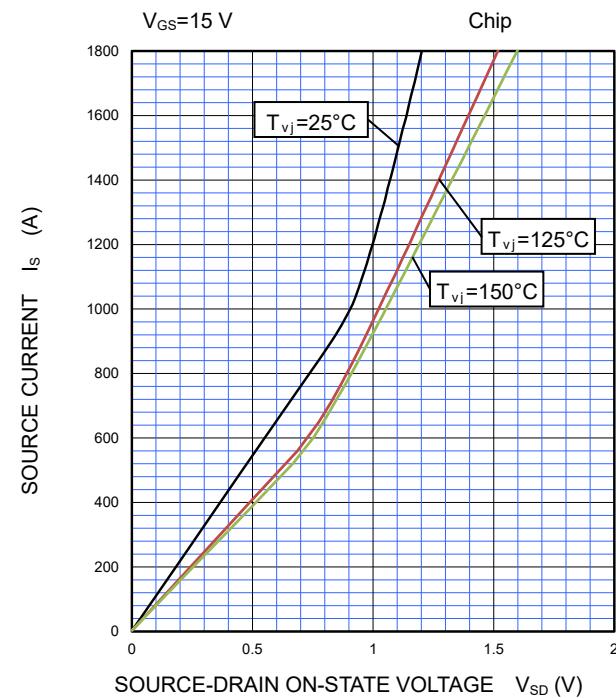
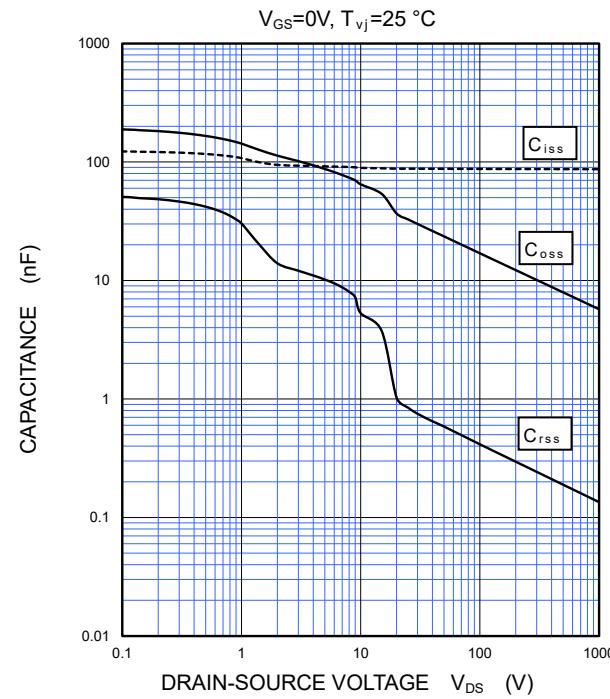
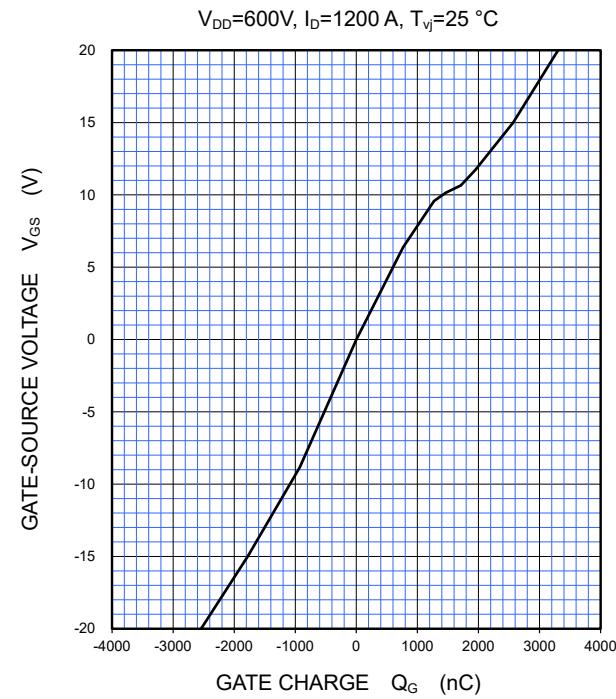
V_{SD} test circuit, V_{GS}=-15V (x: Connected a* and b*)



V_{SD} test circuit, V_{GS}=15V (x: Connected a* and b*)

PERFORMANCE CURVES

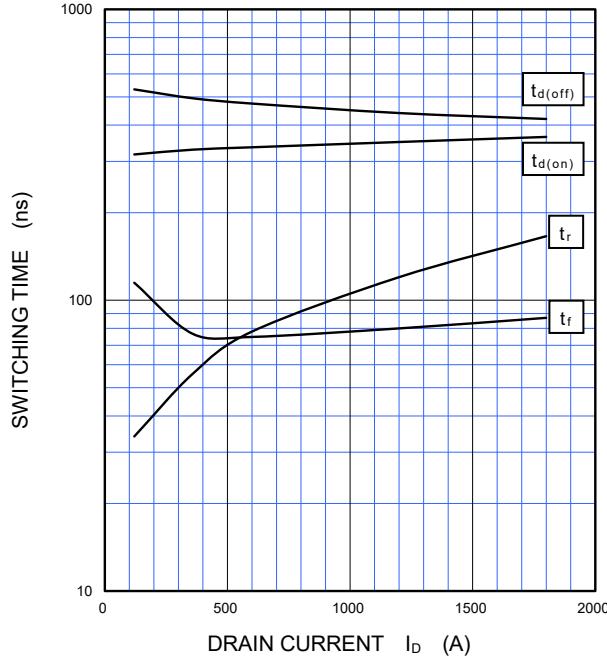


PERFORMANCE CURVESFREE WHEELING DIODE
FORWARD CHARACTERISTICS
(TYPICAL)SOURCE-DRAIN ON STATE VOLTAGE
CHARACTERISTICS
(TYPICAL)CAPACITANCE
CHARACTERISTICS
(TYPICAL)GATE CHARGE
CHARACTERISTICS
(TYPICAL)

PERFORMANCE CURVES

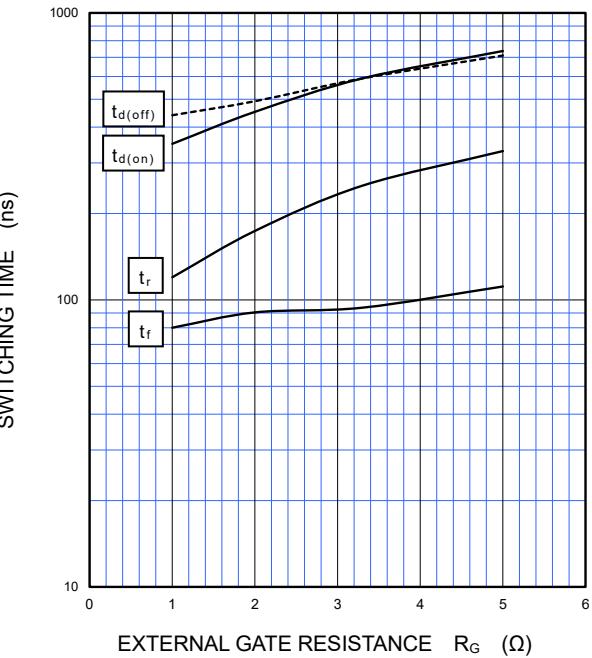
HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

$V_{DD}=600\text{ V}$, $V_{GS}=\pm 15\text{ V}$, $R_G=1.0\text{ }\Omega$, $L_{s,\text{ext}}=16\text{nH}$
 $T_{vj}=150\text{ }^\circ\text{C}$, INDUCTIVE LOAD



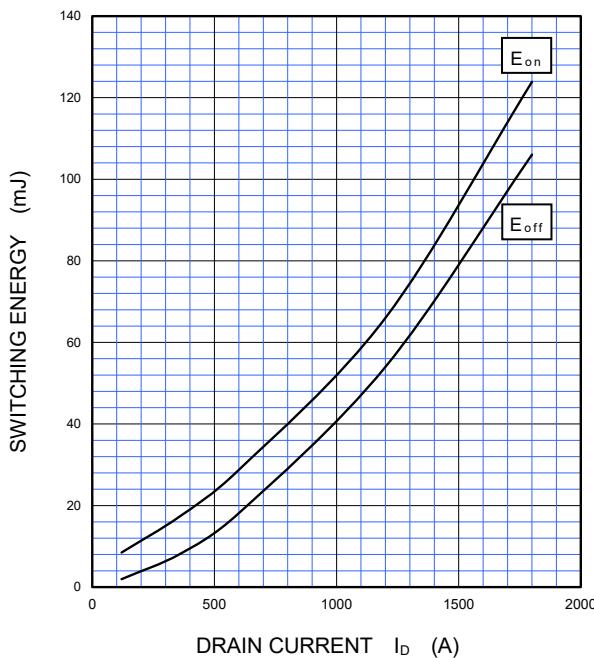
HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

$V_{DD}=600\text{ V}$, $V_{GS}=\pm 15\text{ V}$, $I_D=1200\text{ A}$, $L_{s,\text{ext}}=16\text{nH}$
 $T_{vj}=150\text{ }^\circ\text{C}$, INDUCTIVE LOAD



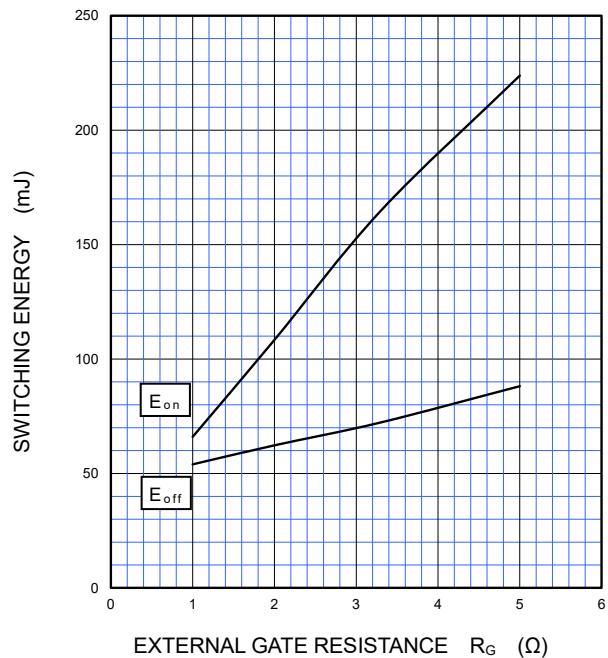
HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

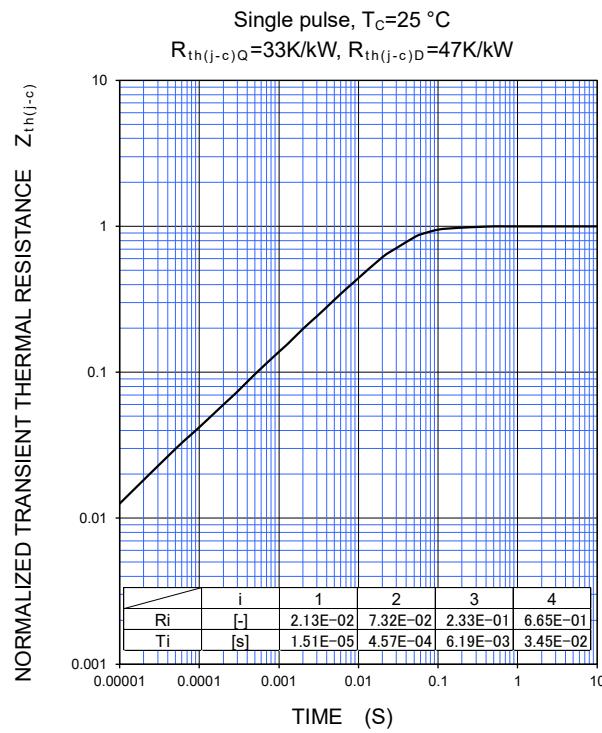
$V_{DD}=600\text{ V}$, $V_{GS}=\pm 15\text{ V}$, $R_G=1.0\Omega$, $T_{vj}=150\text{ }^\circ\text{C}$, $L_{s,\text{ext}}=16\text{nH}$
INDUCTIVE LOAD, PER PULSE



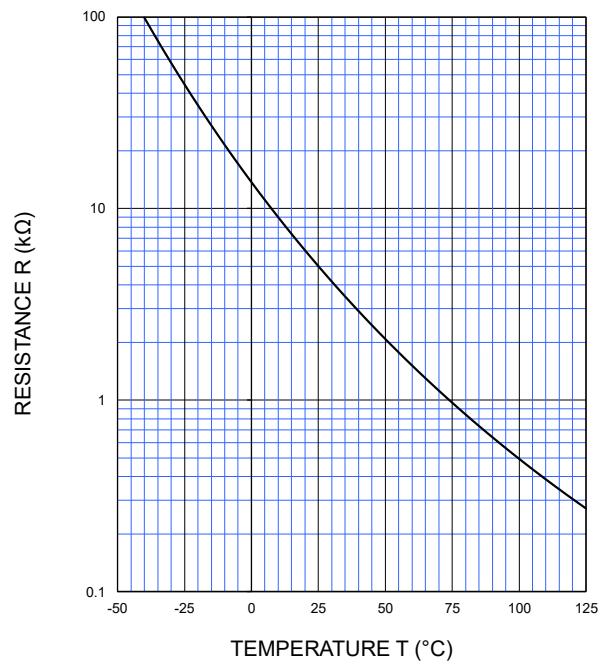
HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

$V_{DD}=600\text{ V}$, $V_{GS}=\pm 15\text{ V}$, $I_D=1200\text{ A}$, $T_{vj}=150\text{ }^\circ\text{C}$, $L_{s,\text{ext}}=16\text{nH}$
INDUCTIVE LOAD, PER PULSE



PERFORMANCE CURVESTRANSIENT THERMAL IMPEDANCE
CHARACTERISTICS
(MAXIMUM)

NTC thermistor part

TEMPERATURE
CHARACTERISTICS
(TYPICAL)

Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Important Notice

The information contained in this datasheet shall in no event be regarded as a guarantee of conditions or characteristics. This product has to be used within its specified maximum ratings, and is subject to customer's compliance with any applicable legal requirement, norms and standards.

Except as otherwise explicitly approved by Mitsubishi Electric Corporation in a written document signed by authorized representatives of Mitsubishi Electric Corporation, our products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.

In usage of power semiconductor, there is always the possibility that trouble may occur with them by the reliability lifetime such as Power Cycle, Thermal Cycle or others, or when used under special circumstances (e.g. condensation, high humidity, dusty, salty, highlands, environment with lots of organic matter / corrosive gas / explosive gas, or situations which terminals of semiconductor products receive strong mechanical stress). Therefore, please pay sufficient attention to such circumstances. Further, depending on the technical requirements, our semiconductor products may contain environmental regulation substances, etc. If there is necessity of detailed confirmation, please contact our nearest sales branch or distributor.

The contents or data contained in this datasheet are exclusively intended for technically trained staff. Customer's technical departments should take responsibility to evaluate the suitability of Mitsubishi Electric Corporation product for the intended application and the completeness of the product data with respect to such application. In the customer's research and development, please evaluate it not only with a single semiconductor product but also in the entire system, and judge whether it's applicable. As required, pay close attention to the safety design by installing appropriate fuse or circuit breaker between a power supply and semiconductor products to prevent secondary damage. Please also pay attention to the application note and the related technical information.

Keep safety first in your circuit designs!

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

- These materials are intended as a reference to assist our customers in the selection of the Mitsubishi Electric Semiconductor product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Mitsubishi Electric Corporation or a third party.
- Mitsubishi Electric Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
- All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Mitsubishi Electric Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Mitsubishi Electric Corporation or an authorized Mitsubishi Electric Semiconductor product distributor for the latest product information before purchasing a product listed herein.
- The information described here may contain technical inaccuracies or typographical errors. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
- Please also pay attention to information published by Mitsubishi Electric Corporation by various means, including the Mitsubishi Electric Semiconductor home page (<http://www.MitsubishiElectric.com/semiconductors/>).
- When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
- Mitsubishi Electric Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Electric Semiconductor product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- The prior written approval of Mitsubishi Electric Corporation is necessary to reprint or reproduce in whole or in part these materials.
- If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
- Any diversion or re-export contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
- Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Electric Semiconductor product distributor for further details on these materials or the products contained therein.

Generally the listed company name and the brand name are the trademarks or registered trademarks of the respective companies.