

WINSTAR Display

OLED SPECIFICATION

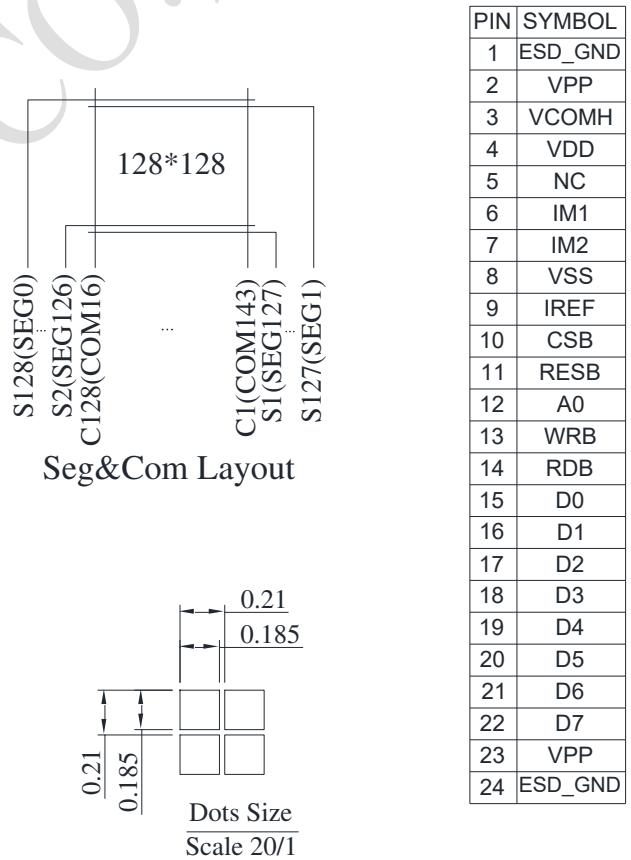
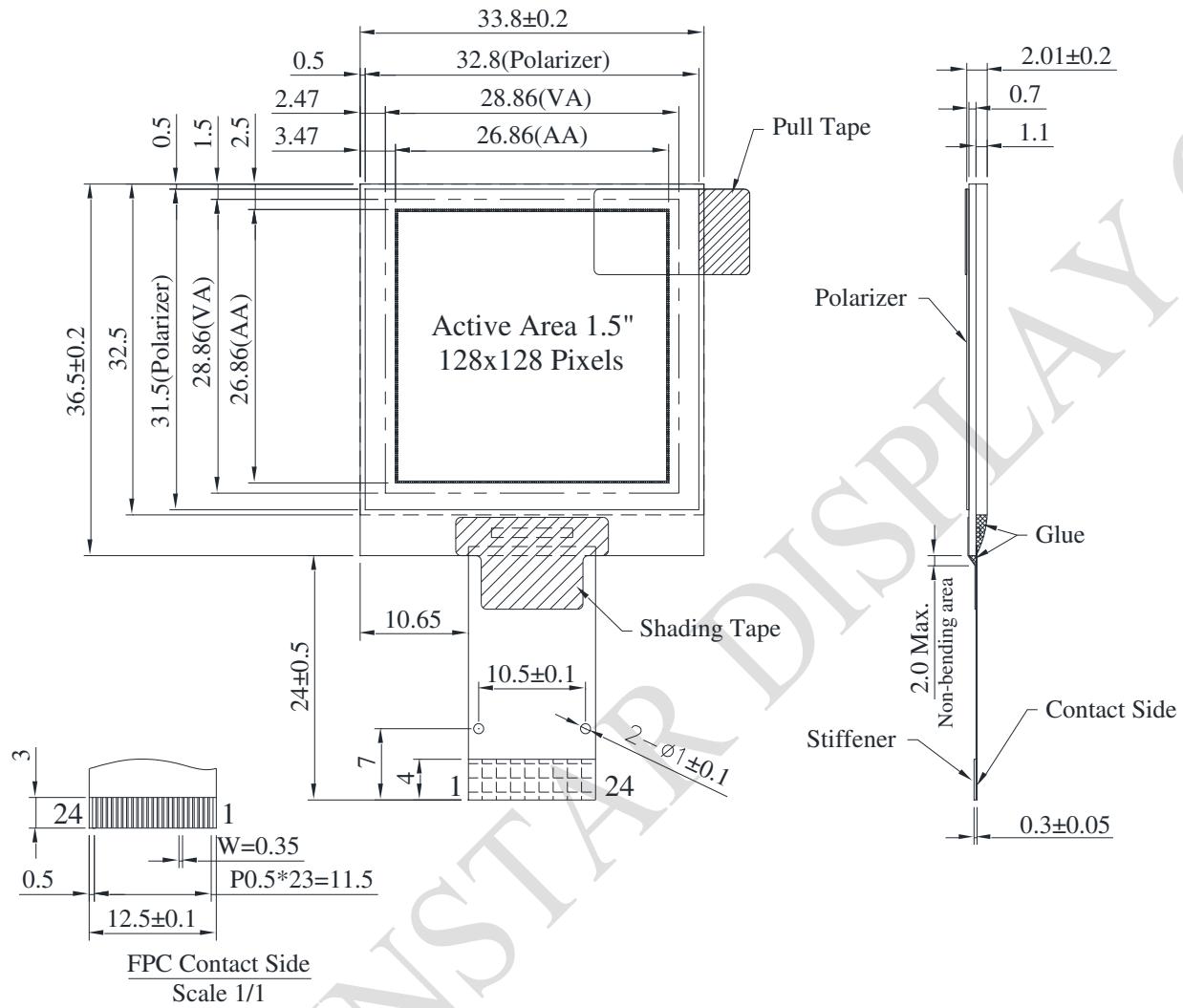
Model No:

WEO128128H

General Specification

Item	Dimension	Unit
Dot Matrix	128 x 128 Dots	—
Module dimension	33.80 x 36.50 x 2.01	mm
Active Area	26.86 x 26.86	mm
Pixel Size	0.185 x 0.185	mm
Pixel Pitch	0.210 x 0.210	mm
Display Mode	Passive Matrix	
Display Color	Monochrome	
Drive Duty	1/128 Duty	
Gray Scale	4 bits	
IC	CH1120	
Interface	8080,SPI,I2C	
Size	1.5 inch	

Contour Drawing & Block Diagram



The non-specified tolerance of dimension is ± 0.3 mm .

Interface Pin Function

No.	Symbol	Function															
1	ESD_GND	This pin should be connected to GND.															
2	VPP	This is the most positive voltage supply pin of the chip. It should be supplied externally.															
3	VCOMH	This is a pin for the voltage output high level for common signals. A capacitor should be connected between this pin and GND.															
4	VDD	Power supply for logic															
5	NC	No connection															
6	IM1	These are the MPU interface mode select pins. <table border="1"><tr><td></td><td>8080</td><td>3-wire SPI</td><td>4-wire SPI</td><td>I2C</td></tr><tr><td>IM1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr><tr><td>IM2</td><td>1</td><td>1</td><td>0</td><td>0</td></tr></table>		8080	3-wire SPI	4-wire SPI	I2C	IM1	1	0	0	1	IM2	1	1	0	0
	8080	3-wire SPI	4-wire SPI	I2C													
IM1	1	0	0	1													
IM2	1	1	0	0													
7	IM2	These pins must be connected to "H" or "L".															
8	VSS	Ground for logic and analog. This pin should be connected to GND externally.															
9	IREF	This is a segment current reference pin. A resistor should be connected between this pin and GND.															
10	CSB	This pin is the chip select input. When CSB = "L", then the chip select becomes active, and data/command I/O is enabled. When in I2C interface, this pin is not used, so it must be connected to "L".															
11	RESB	This is a reset signal input pin. When RESB is set to "L", the settings are initialized. The reset operation is performed by the RESB signal level. This pin internal pull high.															
12	A0	This is the Data/Command control pin that determines whether the data bits are data or a command. A0 = "H": the inputs at D0 to D7 are treated as display data. A0 = "L": the inputs at D0 to D7 are transferred to the command registers. In I2C interface, this pin serves as SA0 to distinguish the different address of OLED driver. When in 3-wire interface, this pin is not used, so it must be connected to "L".															
13	WRB	This is a MPU interface input pin. When connected to an 8080 MPU, this is active LOW. This pin connects to the 8080 MPU WR signal. The signals on the data bus are latched at the rising edge of the WR signal. When in 3-wire.4-wire & I2C interface, this pin is not used, so it must be connected to "L".															

14	RDB	<p>This is a MPU interface input pin. When connected to an 8080 series MPU, it is active LOW. This pin is connected to the RD signal of the 8080 series MPU, and the data bus is in an output status when this signal is "L". When in 3-wire.4-wire & I2C interface, this pin is not used, so it must be connected to "L".</p>
15	D0	
16	D1	
17	D2	<p>This is an 8-bit bi-directional data bus that connects to an 8-bit or 16-bit standard MPU data bus.</p>
18	D3	<p>When the serial interface(SPI) and I2C is selected, then D0 serves as the serial clock input pin (SCL) and D1 serves as the serial data input pin (SI). At this time, D2 to D7 are set to high impedance. D7~D2 is recommended to connect the VDD or GND. It is also allowed to leave D7~D2 unconnected.</p>
19	D4	
20	D5	
21	D6	
22	D7	
23	VPP	<p>This is the most positive voltage supply pin of the chip. It should be supplied externally.</p>
24	ESD_GND	<p>This pin should be connected to GND.</p>

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VDD	-0.3	3.5	V
Supply Voltage for Display	VPP	-0.3	15.0	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	-	1.65	3.0	3.3	V
Supply Voltage for Display	VPP	-	8.0	14.5	15.0	V
High Level Input	VIH	-	$0.8 \times VDD$	-	VDD	V
Low Level Input	VIL	-	GND	-	$0.2 \times VDD$	V
High Level Output	VOH	-	$0.8 \times VDD$	-	VDD	V
Low Level Output	VOL	-	GND	-	$0.1 \times VDD$	V
Display 50% Pixel on	IPP	VPP = 14.5V	-	20	30	mA