



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



SPECIFICATION

MODEL NO. : WLOF00050000FGAAASA00

Summary

5 Inch Smart Display Feature

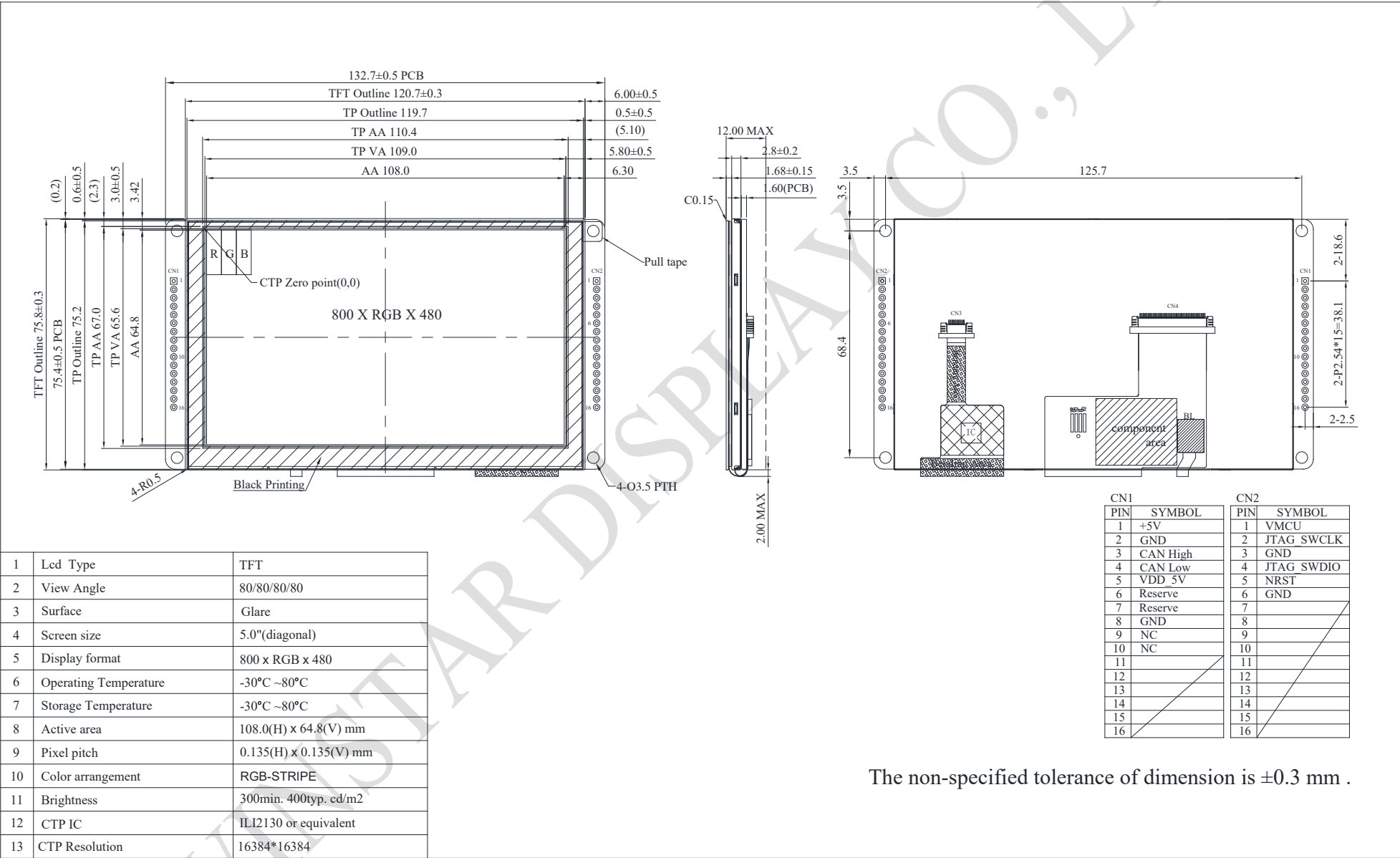
1. DC 5V working voltage, low power consumption for USB to drive.
2. Self-testing after booting function.
3. CAN bus communication interface.
4. Support CANopen negotiation. Default baud rate is 250KB.
5. Built in flash memory, store the font and Object Dictionary Data.
6. Support capacitive touch panel (CTP).
7. Smart Display scenario is slave device display and action from Master Device instruction.
8. Embedded buzzer controlled by Master Device.
9. Demo set HOST can be used on multiple platforms, such as Computer (with USB to CAN Dongle), MCU, Raspberry Pi (with PiCAN2).

Product information

General information

Item	Standard Value	Unit
Operating voltage	5	Vdc
Communication Interface	CAN bus differential ± 3.3	Vpp
MCU	STM32F429	N/A
Flash Memory	16	MB
SDRAM Frequency	90	MHz
LCD display size	5.0	inch
Dot Matrix	800× 3(RGB) × 480	dot
Module dimension	132.7(W) ×75.8(H) ×12.00	mm
Active area	108(W) ×64.8 (H)	mm
Dot pitch	0.135(W) ×0.135(H)	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	80/80/80/80	
Aspect Ratio	5:3	
Touch Panel	With CTP	
Surface	Glare	

Contour Drawing



The non-specified tolerance of dimension is ±0.3 mm .

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+80	°C
Storage Temperature	TST	-30	—	+80	°C

Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	VCC	4.75	5	5.25	V
Supply Current	ICC		410		mA

BOM

Item	Description
LCM	WF50FTWAGDNG0#
PCBA	SV10005R000FA00N0109

Interface

CN1 definition:

Pin	Symbol	Function	Remark
1	+5V	Power supply 5V input	Input
2	GND	Power supply GND input	Input
3	CAN_High	CAN bus D+	I/O
4	CAN_Low	CAN bus D-	I/O
5	VDD_5V	5V output for USART interface	Output
6	USART1_RX	USART RX interface(Reserve)	Reserve
7	USART1_TX	USART TX interface(Reserve)	Reserve
8	GND	GND for USART interface	Output
9-16	NC	--	-

CN2 definition:

Pin	Symbol	Function	Remark
1	VMCU	3.3V power for JTAG interface	Output
2	JTAG_SWCLK	CLK pin for JTAG interface	Input
3	GND	GND for JTAG interface	Output
4	JTAG_SWDIO	Data pin for JTAG interface	I/O
5	NRST	Reset pin for JTAG interface	Input
6	GND	GND	Output
7-16	NC	--	-