

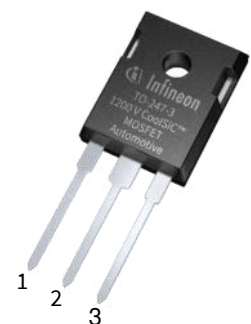
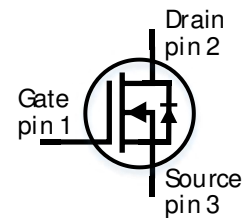
AIMW120R035M1H

CoolSiC™ Automotive 1200V SiC Trench MOSFET 1200V G1

Silicon Carbide MOSFET

Features

- Revolutionary semiconductor material - Silicon Carbide
- Very low switching losses
- Threshold-free on state characteristic
- IGBT-compatible driving voltage (18V for turn-on)
- 0V turn-off gate voltage
- Benchmark gate threshold voltage, $V_{GS(th)} = 4.5V$
- Fully controllable dv/dt
- Commutation robust body diode, ready for synchronous rectification
- Temperature independent turn-off switching losses



Benefits

- Efficiency improvement
- Enabling higher frequency
- Increased power density
- Cooling effort reduction
- Reduction of system complexity and cost

Potential Applications

- On-board Charger/PFC
- Booster/DC-DC Converter



Product validation

Qualified for Automotive Applications. Product Validation according to AEC-Q100/101

Table 1 Key Performance and Package Parameters

Type	V_{DS}	I_D ($T_C = 25^\circ C$, $R_{th(j-c, max)}$)	$R_{DS(on), typ}$ ($T_{vj} = 25^\circ C$, $I_D = 25A$, $V_{GS} = 18V$)	T_{vjmax}	Marking	Package
AIMW120R035M1H	1200V	52A	35mΩ	175°C	A120M1035	PG-TO247-3-41

Table of contents

1200V SiC Trench MOSFET

Table of contents

Features	1
Benefits	1
Potential applications	1
Product validation	1
Table of contents	2
1 Maximum ratings	3
2 Thermal resistances	4
3 Electrical Characteristics	5
3.1 Static characteristics	5
3.2 Dynamic characteristics	6
3.3 Switching characteristics	7
4 Electrical characteristic diagrams	8
5 Package drawing	14
6 Test conditions	15
Revision history	16

Maximum ratings

1 Maximum ratings

Table 2 Maximum ratings¹

Parameter	Symbol	Value	Unit
Drain-source voltage, $T_{vj} \geq 25^{\circ}\text{C}$	V_{DS}	1200	V
DC drain current for $R_{th(j-c,max)}$, limited by T_{vjmax} , $V_{GS} = 18\text{V}$, $T_C = 25^{\circ}\text{C}$ $T_C = 100^{\circ}\text{C}$	I_D	52 41	A
Pulsed drain current, t_p limited by T_{vjmax} , $V_{GS} = 18\text{V}$	$I_{D,pulse}^1$	130	A
DC body diode forward current for $R_{th(j-c,max)}$, limited by T_{vjmax} , $V_{GS} = 0\text{V}$ $T_C = 25^{\circ}\text{C}$ $T_C = 100^{\circ}\text{C}$	I_{SD}	52 34	A
Pulsed body diode current, t_p limited by T_{vjmax}	$I_{SD,pulse}^1$	68	A
Gate-source voltage ² Max transient voltage, < 1% duty cycle Recommended turn-on gate voltage Recommended turn-off gate voltage	V_{GS} $V_{GS,on}$ $V_{GS,off}$	-7... 23 18 0	V
Power dissipation, limited by T_{vjmax} $T_C = 25^{\circ}\text{C}$ $T_C = 100^{\circ}\text{C}$	P_{tot}	228 114	W
Virtual junction temperature	T_{vj}	-55... 175	$^{\circ}\text{C}$
Storage temperature	T_{stg}	-55... 150	$^{\circ}\text{C}$
Soldering temperature, wave soldering only allowed at leads, 1.6mm (0.063 in.) from case for 10 s	T_{sold}	260	$^{\circ}\text{C}$
Mounting torque, M3 screw Maximum of mounting processes: 3	M	0.6	Nm

¹ Not subject to production test. Parameter verified by design/characterization.

² Important note: The selection of positive and negative gate-source voltages impacts the long-term behavior of the device. The design guidelines described in [Application Note AN2018-09](#) must be considered to ensure sound operation of the device over the planned lifetime.

2 Thermal resistances

Table 3 Thermal resistances¹

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
MOSFET/body diode thermal resistance, junction – case	$R_{th(j-c)}$		-	0.51	0.66	K/W
Thermal resistance, junction – ambient	$R_{th(j-a)}$	leaded	-	-	62	K/W

¹ Not subject to production test. Parameter verified by design/characterization.

3 Electrical Characteristics

3.1 Static characteristics

Table 4 Static characteristics (at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Drain-source on-state resistance ²	$R_{DS(on)}$	$V_{GS} = 18\text{V}, I_D = 25\text{A}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 100^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	- - -	35 44 66	46 - -	$\text{m}\Omega$
Body diode forward voltage	V_{SD}	$V_{GS} = 0\text{V}, I_{SD} = 25\text{A}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 100^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	- - -	3.8 3.7 3.6	5.2 - -	V
Gate-source threshold voltage	$V_{GS(th)}$	(tested after 1 ms pulse at $V_{GS} = 20\text{V}$ $I_D = 10\text{mA}, V_{DS} = V_{GS}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	3.5 -	4.5 3.6	5.7 -	V
Zero gate voltage drain current	I_{DSS}	$V_{GS} = 0\text{V}, V_{DS} = 1200\text{V}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	- -	2 50	200 -	μA
Gate-source leakage current	I_{GSS}	$V_{GS} = 23\text{V}, V_{DS} = 0\text{V}$	-	-	120	nA
		$V_{GS} = -7\text{V}, V_{DS} = 0\text{V}$	-	-	-120	nA
Transconductance	g_{fs}	$V_{DS} = 20\text{V}, I_D = 25\text{A}$	-	11.1	-	S
Internal gate resistance	R_{Gint}	$f = 1\text{MHz}, V_{AC} = 25\text{mV}$	-	4.5	-	Ω

² Important note: The selection of positive and negative gate-source voltages impacts the long-term behavior of the device. The design guidelines described in [Application Note AN2018-09](#) must be considered to ensure sound operation of the device over the planned lifetime.

3.2 Dynamic characteristics

Table 5 Dynamic characteristics (at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Input capacitance	C_{iss}	$V_{DD} = 800\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$, $V_{AC} = 25\text{mV}$	-	2130	-	pF
Output capacitance	C_{oss}		-	107	-	
Reverse capacitance	C_{rss}		-	11	-	
C_{oss} stored energy	E_{oss}		-	44	-	μJ
Total gate charge	Q_G	$V_{DD} = 800\text{V}$, $I_D = 25\text{A}$, $V_{GS} = 0/18\text{V}$, turn-on pulse	-	59	-	nC
Gate to source charge	$Q_{GS,pl}$		-	19	-	
Gate to drain charge	Q_{GD}		-	13	-	

3.3 Switching characteristics

Table 6 Switching characteristics, Inductive load ⁴

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
MOSFET Characteristics, $T_{vj} = 25^{\circ}\text{C}$						
Turn-on delay time	$t_{d(\text{on})}$	$V_{DD} = 800\text{V}$, $I_D = 25\text{A}$, $V_{GS} = 0/18\text{V}$, $R_{G\text{ext}} = 2\Omega$, $L_G = 40\text{nH}$, diode: body diode at $V_{GS} = 0\text{V}$ see Fig. E	-	12	-	ns
Rise time	t_r		-	25	-	
Turn-off delay time	$t_{d(\text{off})}$		-	22	-	
Fall time	t_f		-	13	-	
Turn-on energy	E_{on}		-	417	-	μJ
Turn-off energy	E_{off}		-	147	-	
Total switching energy	E_{tot}	-	564	-		
Body Diode Characteristics, $T_{vj} = 25^{\circ}\text{C}$						
Diode reverse recovery charge	Q_{rr}	$V_{DD} = 800\text{V}$, $I_{SD} = 25\text{A}$, V_{GS} at diode = 0V , $di_f/dt = 1000\text{A}/\mu\text{s}$, Q_{rr} includes also Q_C , see Fig. C	-	140	-	nC
Diode peak reverse recovery current	I_{rrm}		-	10	-	A
MOSFET Characteristics, $T_{vj} = 175^{\circ}\text{C}$						
Turn-on delay time	$t_{d(\text{on})}$	$V_{DD} = 800\text{V}$, $I_D = 25\text{A}$, $V_{GS} = 0/18\text{V}$, $R_{G\text{ext}} = 2\Omega$, $L_G = 40\text{nH}$, diode: body diode at $V_{GS} = 0\text{V}$ see Fig. E	-	12	-	ns
Rise time	t_r		-	31	-	
Turn-off delay time	$t_{d(\text{off})}$		-	24	-	
Fall time	t_f		-	14	-	
Turn-on energy	E_{on}		-	700	-	μJ
Turn-off energy	E_{off}		-	161	-	
Total switching energy	E_{tot}	-	861	-		
Body Diode Characteristics, $T_{vj} = 175^{\circ}\text{C}$						
Diode reverse recovery charge	Q_{rr}	$V_{DD} = 800\text{V}$, $I_{SD} = 25\text{A}$, V_{GS} at diode = 0V , $di_f/dt = 1000\text{A}/\mu\text{s}$, Q_{rr} includes also Q_C , see Fig. C	-	550	-	nC
Diode peak reverse recovery current	I_{rrm}		-	15	-	A

⁴ The chip technology was characterized up to $200\text{ kV}/\mu\text{s}$. The measured dV/dt was limited by measurement test setup and package.

4 Electrical characteristic diagrams

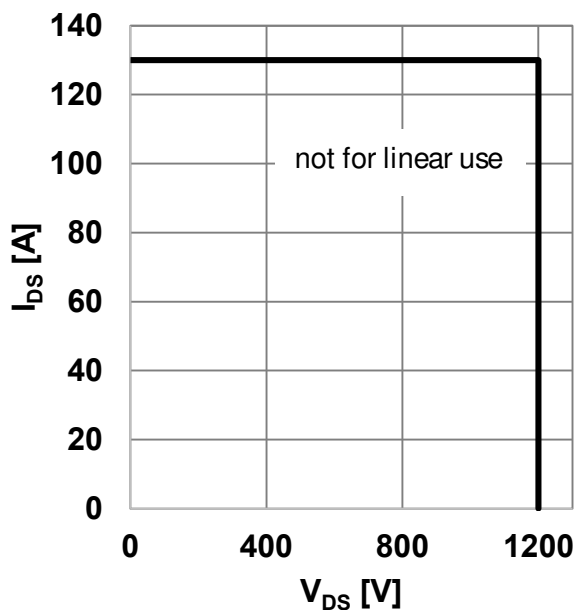


Figure 1 Safe operating area (SOA)
($V_{GS} = 0/18V$, $T_c = 25^\circ C$, $T_j \leq 175^\circ C$)

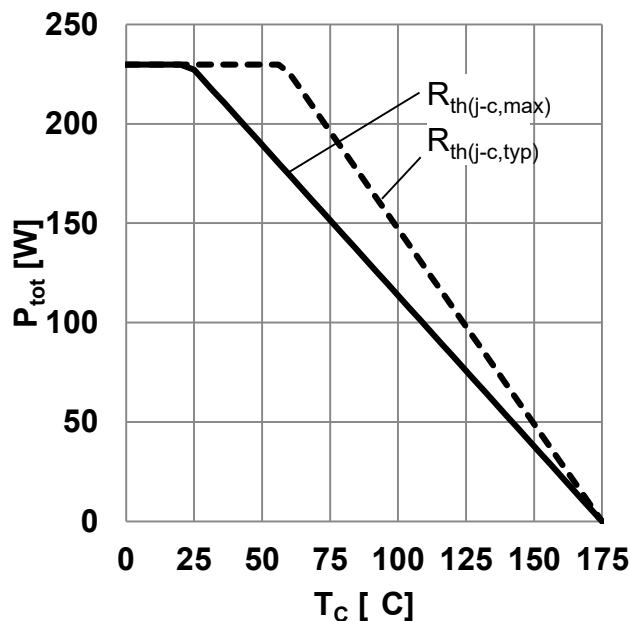


Figure 2 Power dissipation as a function of case temperature limited by bond wire
($P_{tot} = f(T_c)$)

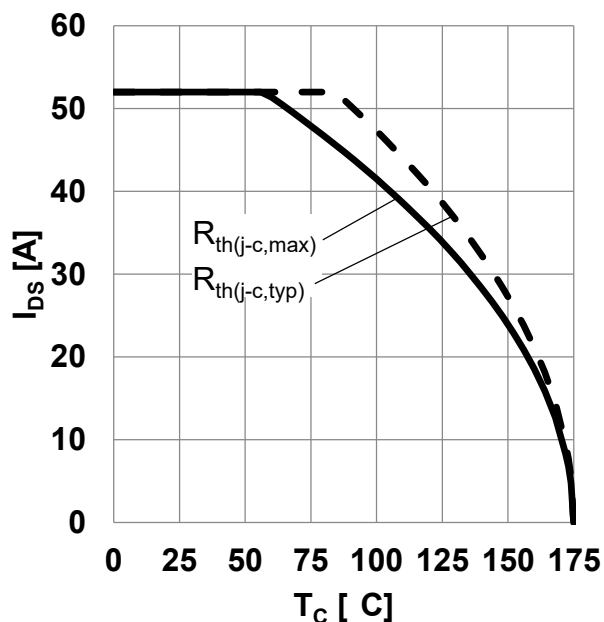


Figure 3 Maximum DC drain to source current as a function of case temperature limited by bond wire ($I_{DS} = f(T_c)$)

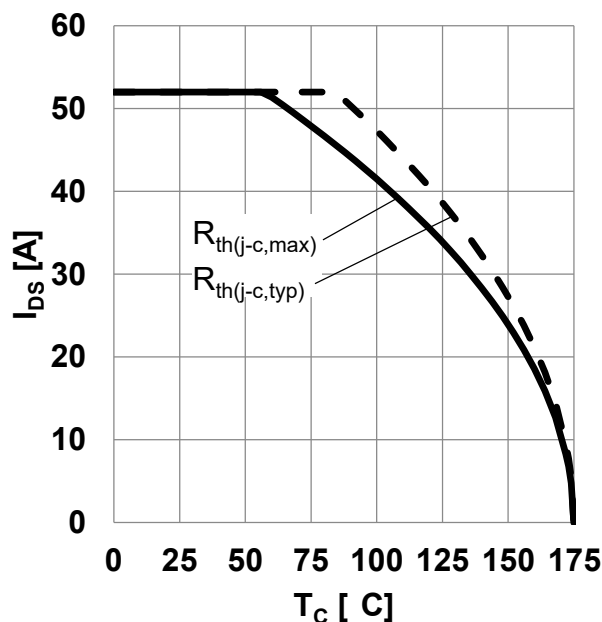


Figure 4 Maximum source to drain current as a function of case temperature limited by bond wire ($I_{SD} = f(T_c)$, $V_{GS} = 0V$)

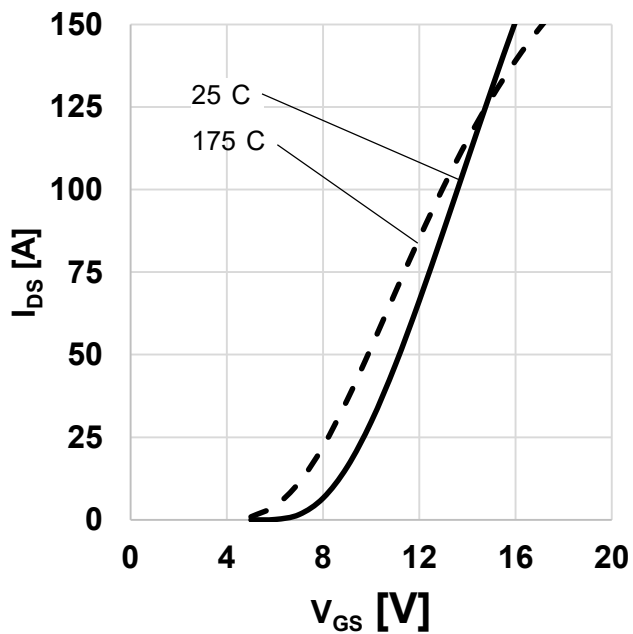


Figure 5 Typical transfer characteristic
($I_{DS} = f(V_{GS})$, $V_{DS} = 20V$, $t_P = 20\mu s$)

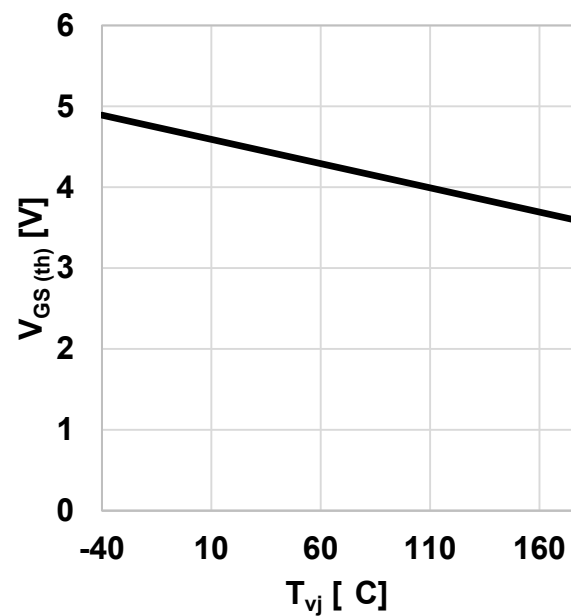


Figure 6 Typical gate-source threshold voltage
as a function of junction temperature
($V_{GS(th)} = f(T_{vj})$, $I_{DS} = 10mA$, $V_{GS} = V_{DS}$)

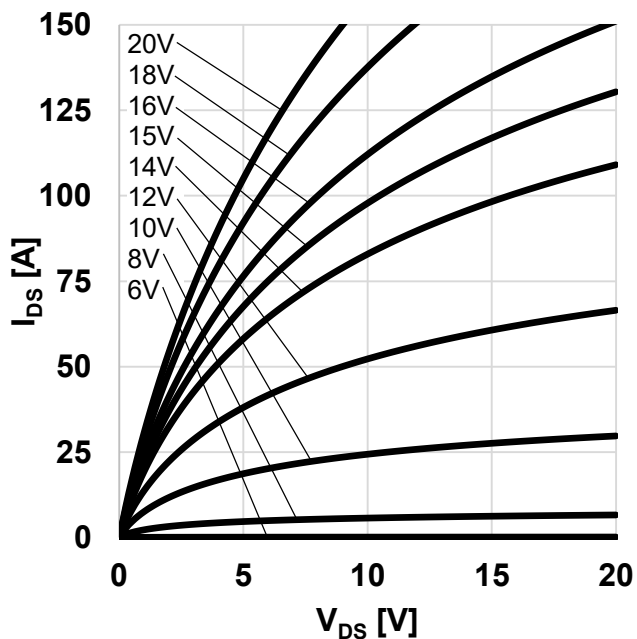


Figure 7 Typical output characteristic, V_{GS} as
parameter
($I_{DS} = f(V_{DS})$, $T_{vj} = 25^\circ C$, $t_P = 20\mu s$)

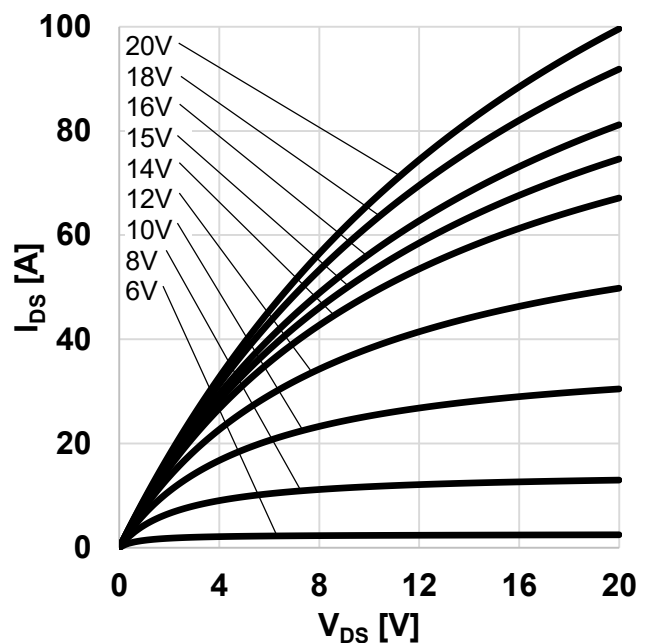


Figure 8 Typical output characteristic, V_{GS} as
parameter
($I_{DS} = f(V_{DS})$, $T_{vj} = 175^\circ C$, $t_P = 20\mu s$)

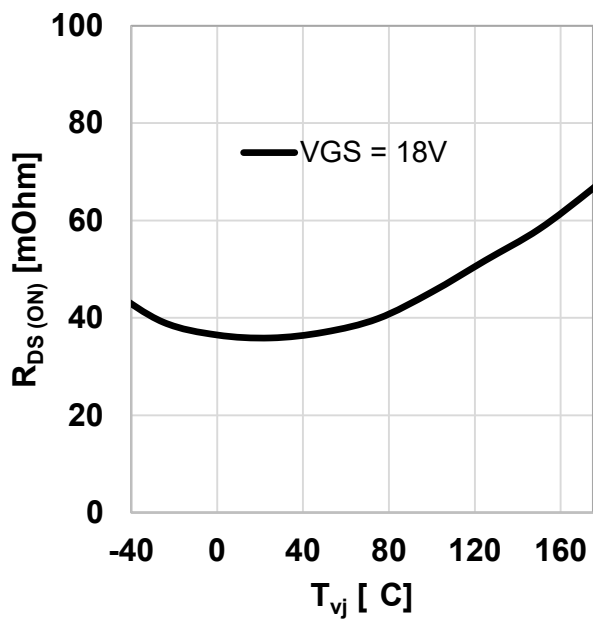


Figure 9 Typical on-resistance as a function of junction temperature
($R_{DS(on)} = f(T_{vj})$, $I_{DS} = 25A$)

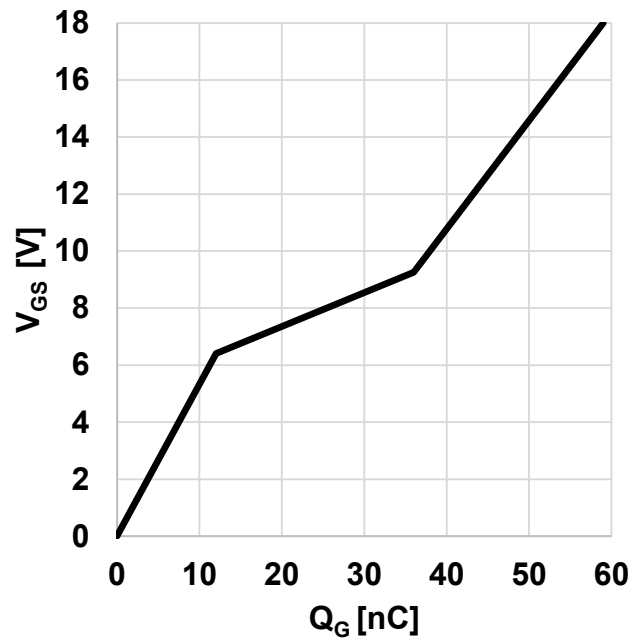


Figure 10 Typical gate charge
($V_{GS} = f(Q_G)$, $I_{DS} = 25A$, $V_{DS} = 800V$, turn-on pulse)

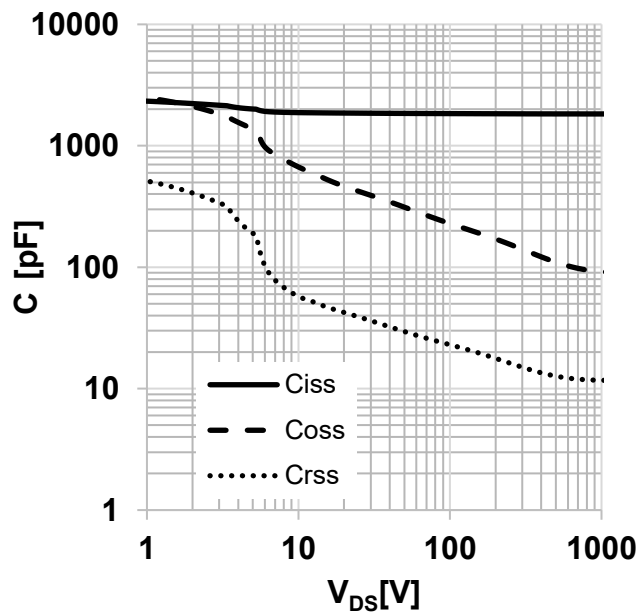


Figure 11 Typical capacitance as a function of drain-source voltage
($C = f(V_{DS})$, $V_{GS} = 0V$, $f = 1MHz$)

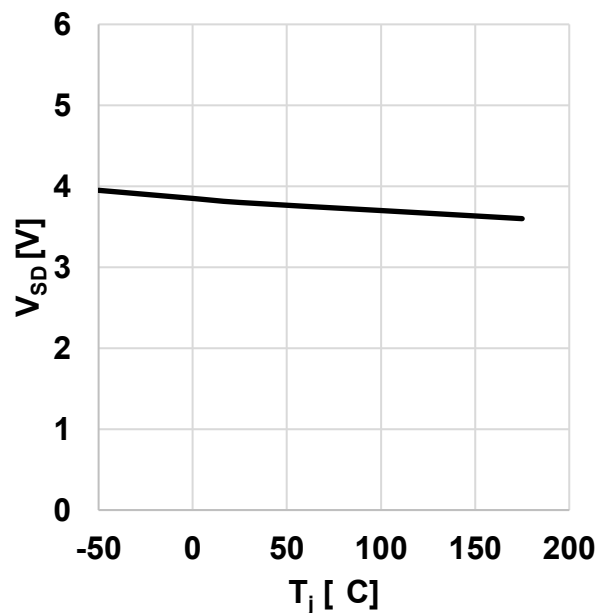


Figure 12 Typical body diode forward voltage as function of junction temperature
($V_{SD} = f(T_{vj})$, $V_{GS} = 0V$, $I_{SD} = 25A$)

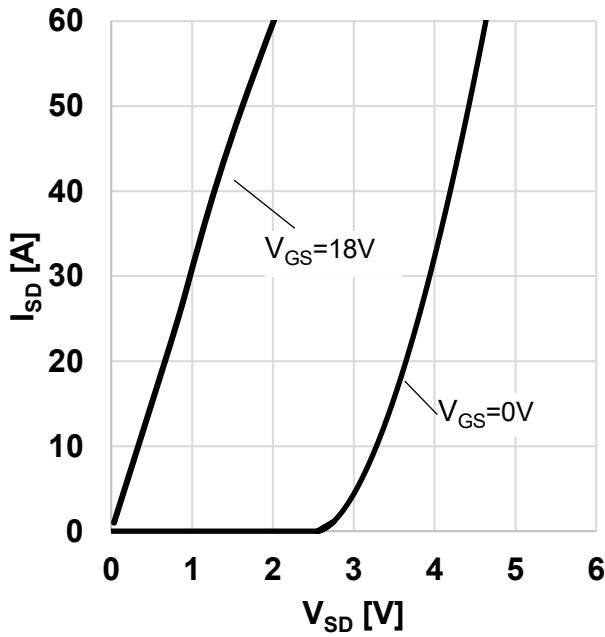


Figure 13 Typical body diode forward current as function of forward voltage, V_{GS} as parameter
($I_{SD} = f(V_{SD})$, $T_{vj} = 25^{\circ}\text{C}$, $t_P = 20\mu\text{s}$)

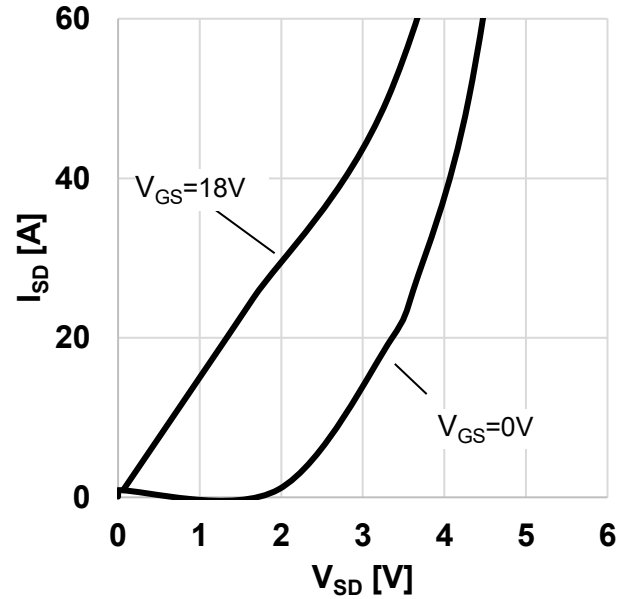


Figure 14 Typical body diode forward current as function of forward voltage, V_{GS} as parameter
($I_{SD} = f(V_{SD})$, $T_{vj} = 175^{\circ}\text{C}$, $t_P = 20\mu\text{s}$)

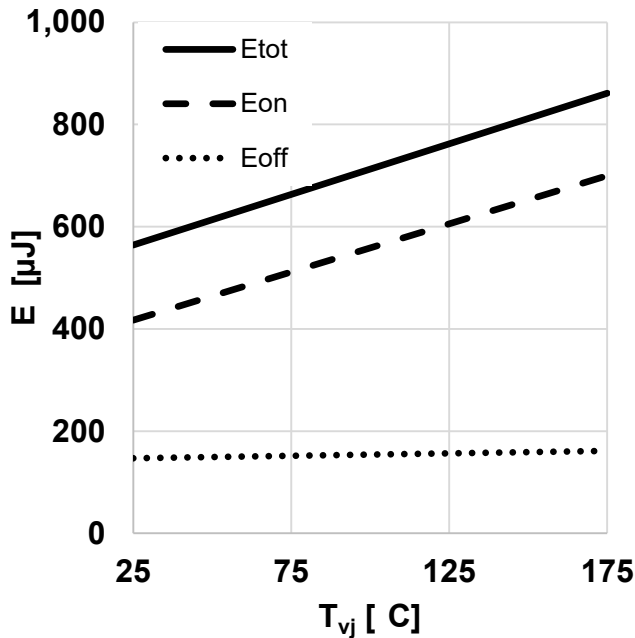


Figure 15 Typical switching energy losses as a function of junction temperature
($E = f(T_{vj})$, $V_{DD} = 800\text{V}$, $V_{GS} = 0\text{V}/18\text{V}$, $R_{Gext} = 2\Omega$, $I_D = 25\text{A}$, ind. load, test circuit in Fig. E, diode: body diode at $V_{GS} = 0\text{V}$)

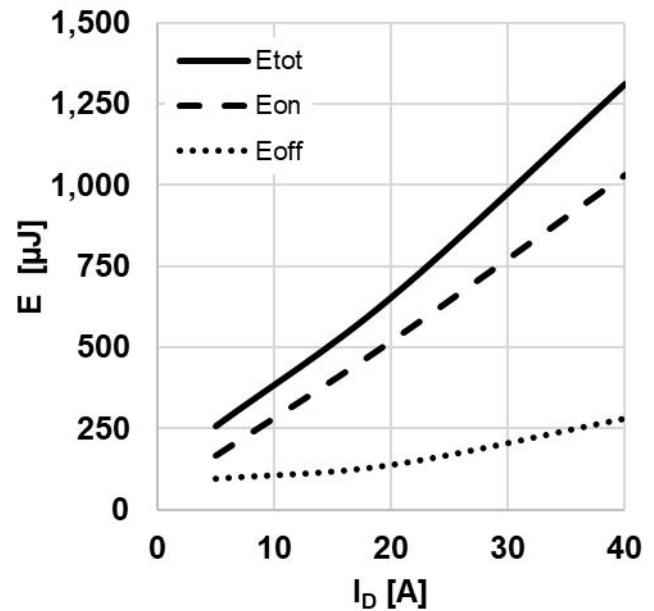


Figure 16 Typical switching energy losses as a function of drain-source current
($E = f(I_{DS})$, $V_{DD} = 800\text{V}$, $V_{GS} = 0\text{V}/18\text{V}$, $R_{Gext} = 2\Omega$, $T_{vj} = 175^{\circ}\text{C}$, ind. load, test circuit in Fig. E, diode: body diode at $V_{GS} = 0\text{V}$)

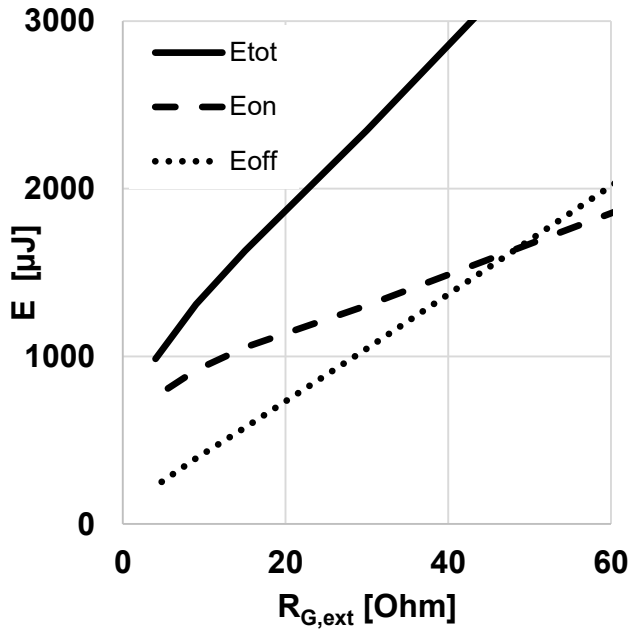


Figure 17 Typical switching energy losses as a function of gate resistance
($E = f(R_{G,ext})$, $V_{DD} = 800V$, $V_{GS} = 0V/18V$, $I_D = 25A$, $T_{vj} = 175^\circ C$, ind. load, test circuit in Fig. E, diode: body diode at $V_{GS} = 0V$)

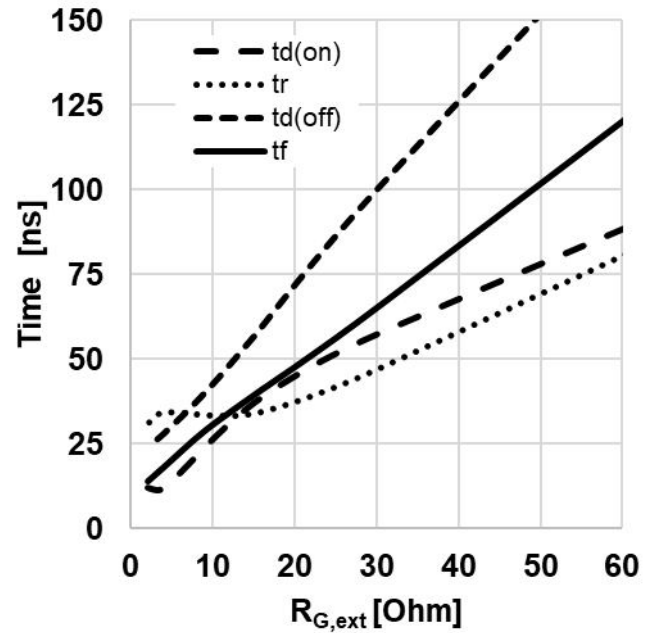


Figure 18 Typical switching times as a function of gate resistor
($t = f(R_{G,ext})$, $V_{DD} = 800V$, $V_{GS} = 0V/18V$, $I_D = 25A$, $T_{vj} = 175^\circ C$, ind. load, test circuit in Fig. E, diode: body diode at $V_{GS} = 0V$)

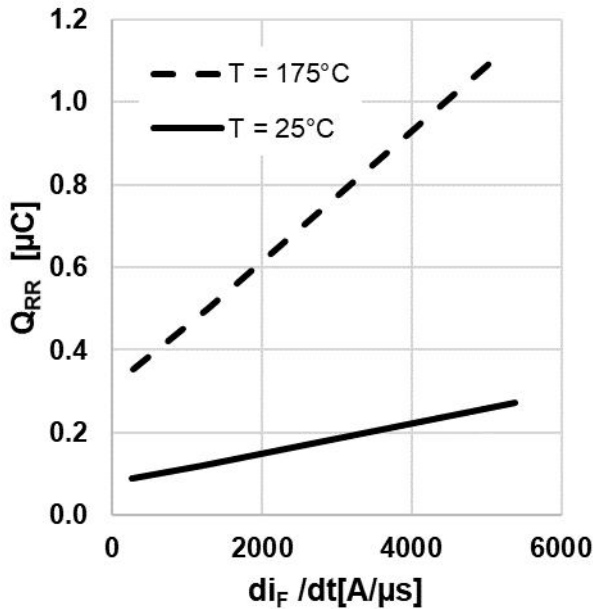


Figure 19 Typical reverse recovery charge as a function of diode current slope
($Q_r = f(di_F/dt)$, $V_{DD} = 800V$, $V_{GS} = 0V/18V$, $I_D = 25A$, ind. load, test circuit in Fig. E, body diode at $V_{GS} = 0V$)

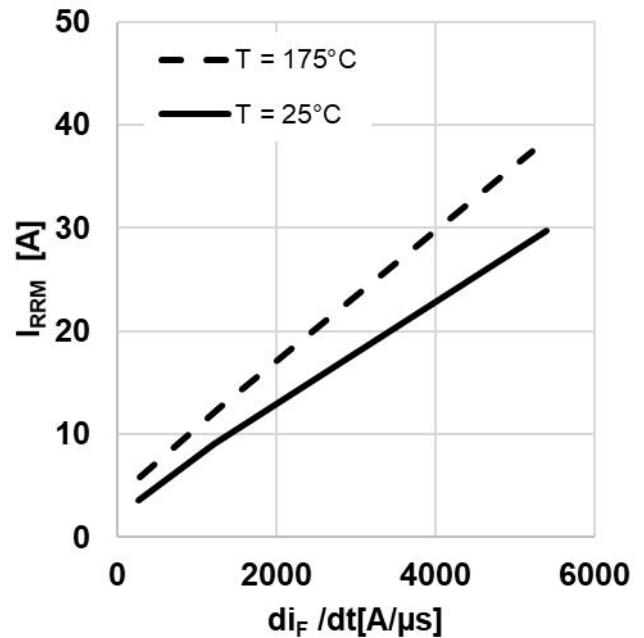


Figure 20 Typical reverse recovery current as a function of diode current slope
($I_{rrm} = f(di_F/dt)$, $V_{DD} = 800V$, $V_{GS} = 0V/18V$, $I_D = 25A$, ind. load, test circuit in Fig. E, body diode at $V_{GS} = 0V$)

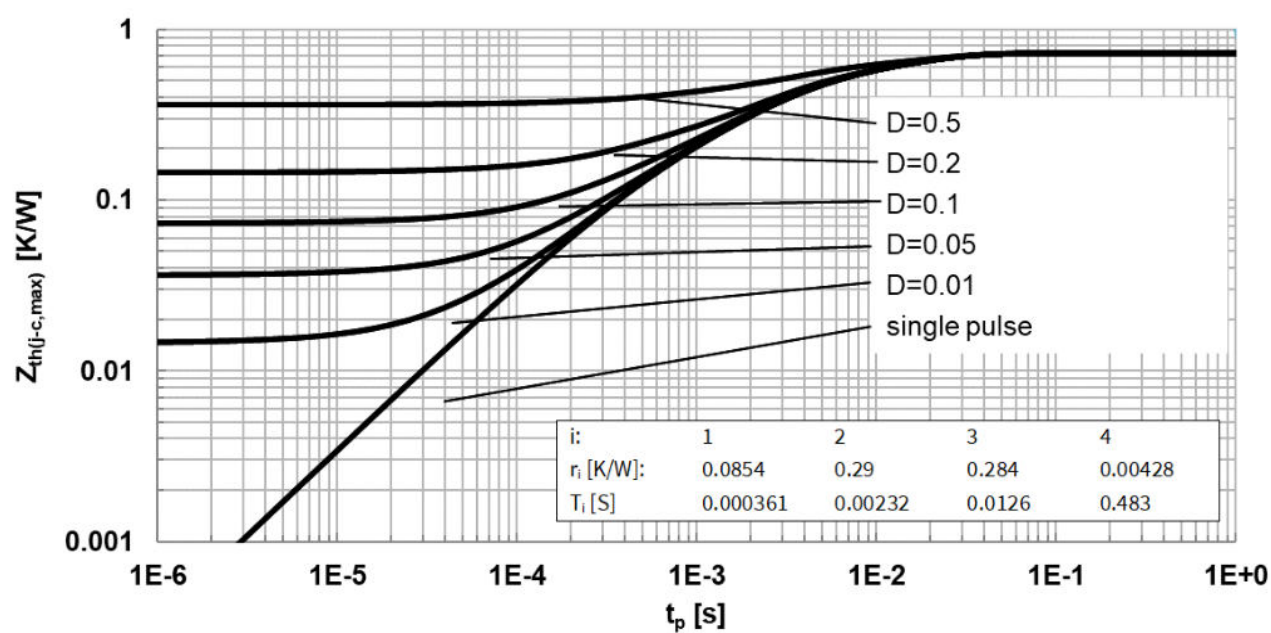


Figure 21 Max. transient thermal resistance (MOSFET/diode)
 $(Z_{th(j-c,max)} = f(t_p), \text{ parameter } D = t_p/T, \text{ thermal equivalent circuit in Fig. D})$

5 Package drawing

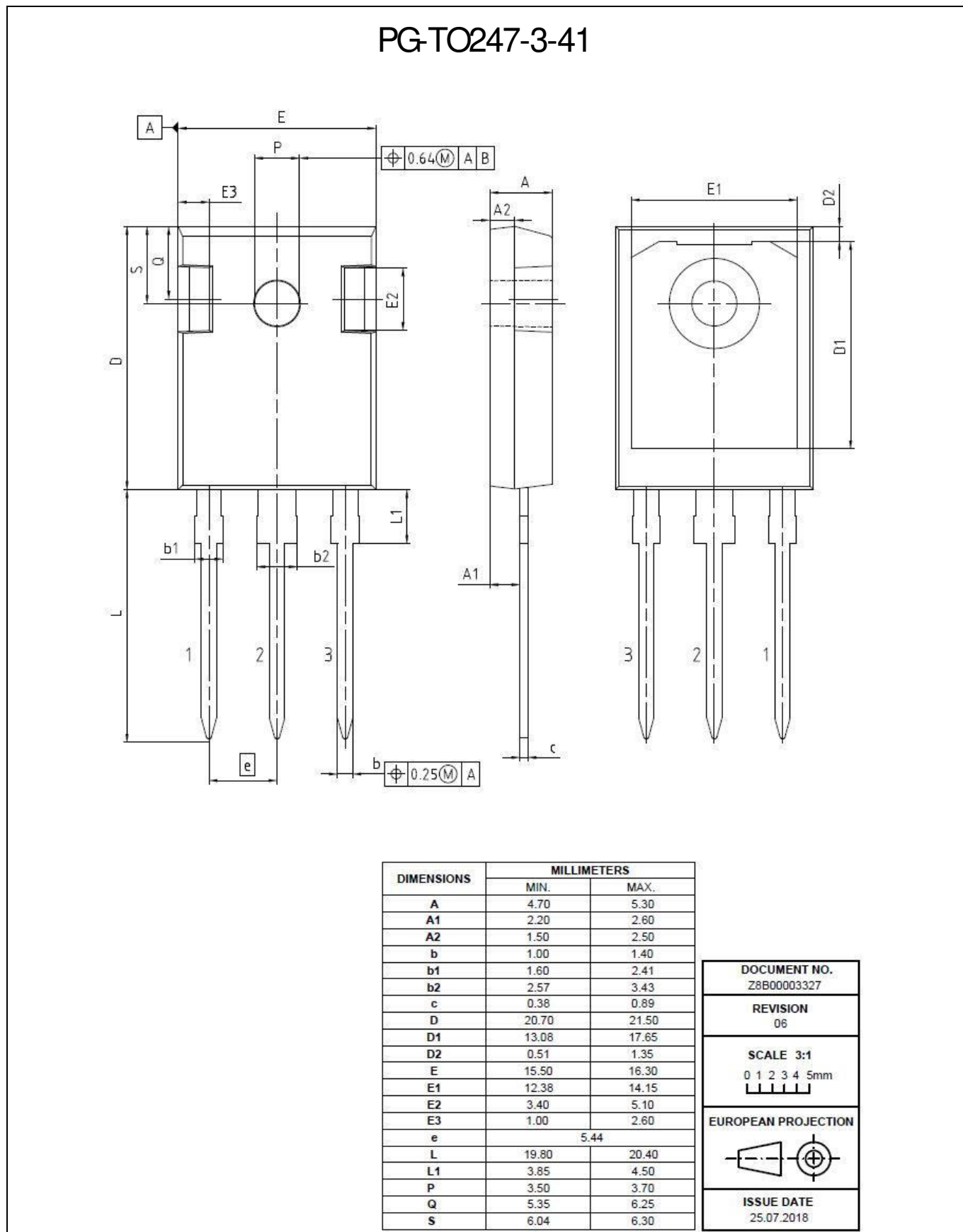


Figure 22 Package drawing

6 Test conditions

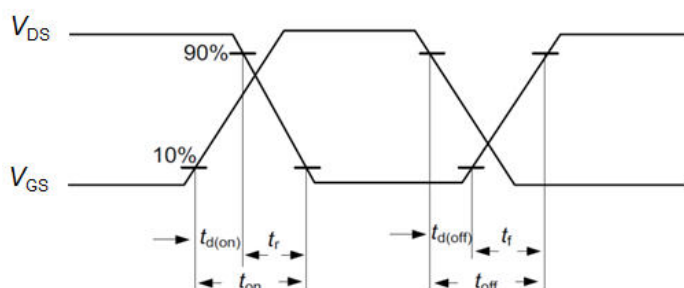


Figure A. Definition of switching times

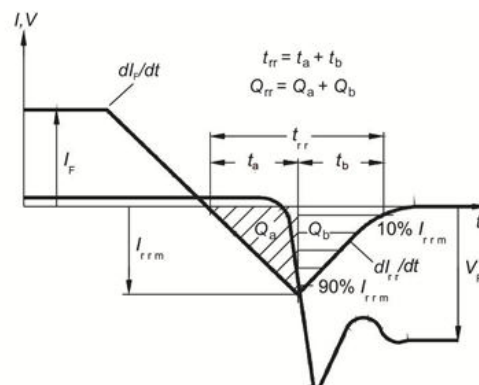


Figure C. Definition of diode switching characteristics

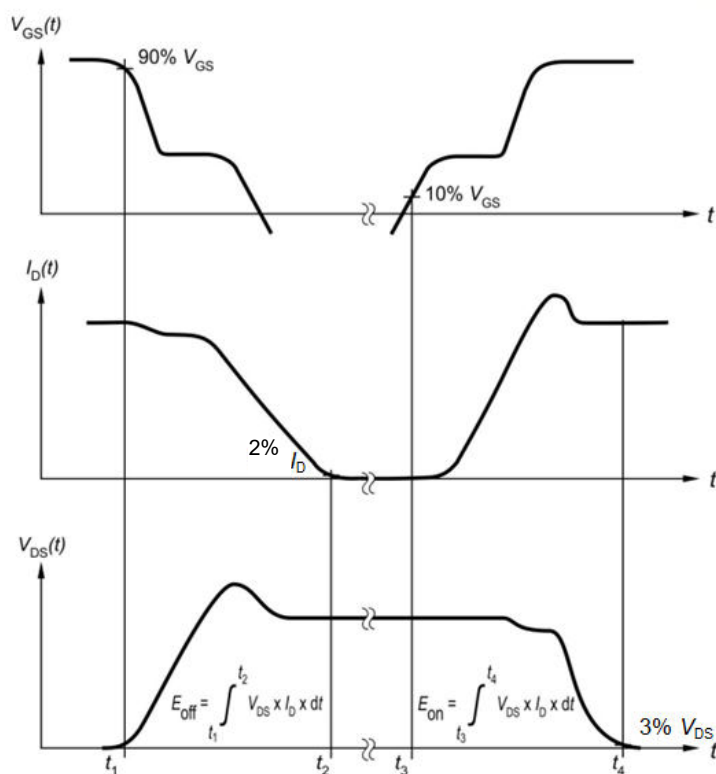


Figure B. Definition of switching losses

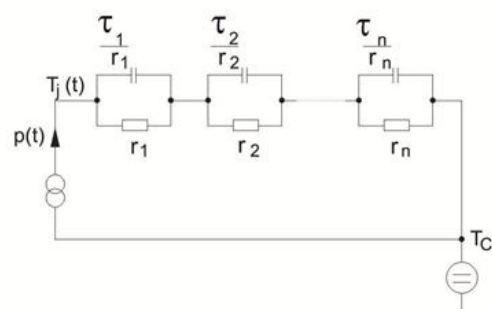


Figure D. Thermal equivalent circuit

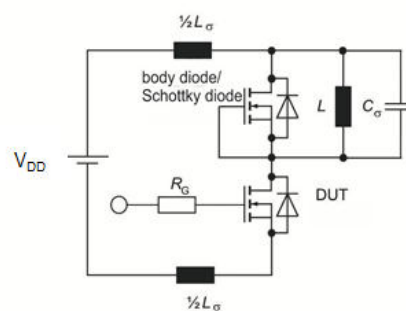


Figure E. Dynamic test circuit
Parasitic inductance L_{σ} ,
parasitic capacitor C_{σ}

Figure 23 Test conditions

Revision history

Revision history

Document version	Date of release	Description of changes
V01_00	2021-03-09	-
V01_10	2023-01-16	I _{SD,pulse} value adjusted

Trademarks

All referenced product or service names and trademarks are the property of their respective owners.

Published by
Infineon Technologies AG
81726 München, Germany
© Infineon Technologies AG 2023.
All Rights Reserved.

Important notice

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie"). With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications.

The data contained in this document is exclusively intended for technically trained staff. It is the responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application.

For further information on the product, technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies office (www.infineon.com).

Please note that this product is qualified according to the AECQ100 or AECQ101 documents of the Automotive Electronics Council.

Warnings

Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office.

Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.