

HybridPACK™ Drive Module

FS820R08A6P2LB

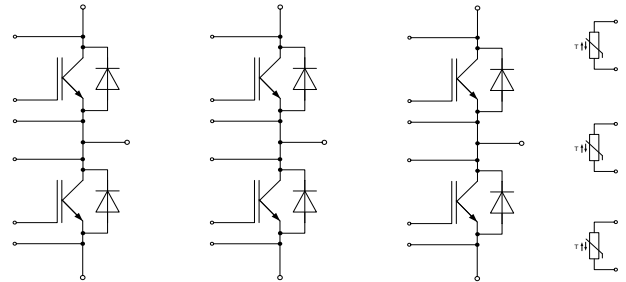
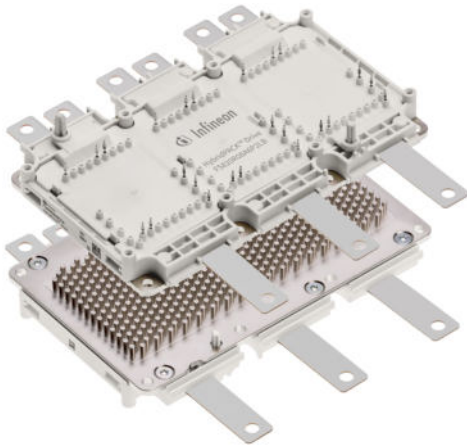
Final Data Sheet

V3.4, 2019-10-09

Automotive High Power

1 Features / Description

HybridPACK™ Drive module with EDT2 IGBT and Diode



$V_{CES} = 750 \text{ V}$

$I_C = 820 \text{ A}$

Typical Applications

- Automotive Applications
- Hybrid Electrical Vehicles (H)EV
- Motor Drives
- Commercial Agriculture Vehicles

Electrical Features

- Blocking voltage 750V
- Low V_{CEsat}
- Low Switching Losses
- Low Q_g and Cr_{ss}
- Low Inductive Design
- $T_{vj\ op} = 150^\circ\text{C}$
- Short-time extended Operation Temperature
 $T_{vj\ op} = 175^\circ\text{C}$

Mechanical Features

- 4.2kV DC 1sec Insulation
- High Creepage and Clearance Distances
- Compact design
- High Power Density
- Direct Cooled PinFin Base Plate
- Guiding elements for PCB and cooler assembly
- Integrated NTC temperature sensor
- PressFIT Contact Technology
- RoHS compliant
- UL 94 V0 module frame

Description

The HybridPACK™ Drive is a very compact six-pack module (750V/820A) optimized for hybrid and electric vehicles. The power module implements the new EDT2 IGBT generation, which is an automotive Micro-Pattern Trench-Field-Stop cell design optimized for electric drive train applications. The chipset has benchmark current density combined with short circuit ruggedness and increased blocking voltage for reliable inverter operation under harsh environmental conditions. The EDT2 IGBTs also show excellent light load power losses, which helps to improve system efficiency over a real driving cycle. The EDT2 IGBT was optimized for applications with switching frequencies in the range of 10 kHz.

The new HybridPACK™ Drive power module family comes with mechanical guiding elements supporting easy assembly processes for customers. Furthermore, the press-fit pins for the signal terminals avoid additional time consuming selective solder processes, which provides cost savings on system level and increases system reliability. The direct cooled baseplate with PinFin structure in the FS820R08A6P2LB product best utilizes the implemented chipset and shows superior thermal characteristics. Due to the high clearance & creepage distances, the module family is also well suited for increased system working voltages and supports modular inverter approaches.

Product Name	Ordering Code
FS820R08A6P2LB	SP001611366

2 IGBT, Inverter

2.1 Maximum Rated Values

Parameter	Conditions	Symbol	Value	Unit
Collector-emitter voltage	$T_{vj} = 25^{\circ}\text{C}$	V_{CES}	750	V
Implemented collector current		I_{CN}	820	A
Continuous DC collector current	$T_F = 80^{\circ}\text{C}$, $T_{vj\text{ max}} = 175^{\circ}\text{C}$	$I_{C\text{ nom}}$	450 ¹⁾	A
Repetitive peak collector current	$t_P = 1\text{ ms}$	I_{CRM}	1640	A
Total power dissipation	$T_F = 75^{\circ}\text{C}$, $T_{vj\text{ max}} = 175^{\circ}\text{C}$	P_{tot}	714 ¹⁾	W
Gate-emitter peak voltage		V_{GES}	+/-20	V

2.2 Characteristic Values

			min.	typ.	max.	
Collector-emitter saturation voltage	$I_C = 450\text{ A}$, $V_{GE} = 15\text{ V}$ $I_C = 450\text{ A}$, $V_{GE} = 15\text{ V}$ $I_C = 450\text{ A}$, $V_{GE} = 15\text{ V}$ $I_C = 820\text{ A}$, $V_{GE} = 15\text{ V}$ $I_C = 820\text{ A}$, $V_{GE} = 15\text{ V}$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	$V_{CE\text{ sat}}$	1.10 1.15 1.15 1.30 1.50	1.35	V
Gate threshold voltage	$I_C = 9.60\text{ mA}$, $V_{CE} = V_{GE}$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	V_{GEth}	4.90 5.80 4.10	6.50	V
Gate charge	$V_{GE} = -8\text{ V} \dots 15\text{ V}$, $V_{CE} = 400\text{ V}$		Q_G	4.40		μC
Internal gate resistor		$T_{vj} = 25^{\circ}\text{C}$	R_{Gint}	0.7		Ω
Input capacitance	$f = 1\text{ MHz}$, $V_{CE} = 50\text{ V}$, $V_{GE} = 0\text{ V}$	$T_{vj} = 25^{\circ}\text{C}$	C_{ies}	80.0		nF
Output capacitance	$f = 1\text{ MHz}$, $V_{CE} = 50\text{ V}$, $V_{GE} = 0\text{ V}$	$T_{vj} = 25^{\circ}\text{C}$	C_{oes}	1.00		nF
Reverse transfer capacitance	$f = 1\text{ MHz}$, $V_{CE} = 50\text{ V}$, $V_{GE} = 0\text{ V}$	$T_{vj} = 25^{\circ}\text{C}$	C_{res}	0.30		nF
Collector-emitter cut-off current	$V_{CE} = 750\text{ V}$, $V_{GE} = 0\text{ V}$ $V_{CE} = 750\text{ V}$, $V_{GE} = 0\text{ V}$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	I_{CES}	5	1.0	mA
Gate-emitter leakage current	$V_{CE} = 0\text{ V}$, $V_{GE} = 20\text{ V}$	$T_{vj} = 25^{\circ}\text{C}$	I_{GES}		400	nA
Turn-on delay time, inductive load	$I_C = 450\text{ A}$, $V_{CE} = 400\text{ V}$ $V_{GE} = -8\text{ V} / +15\text{ V}$ $R_{Gon} = 2.4\text{ }\Omega$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	t_{don}	0.28 0.29 0.30		μs
Rise time, inductive load	$I_C = 450\text{ A}$, $V_{CE} = 400\text{ V}$ $V_{GE} = -8\text{ V} / +15\text{ V}$ $R_{Gon} = 2.4\text{ }\Omega$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	t_r	0.07 0.08 0.08		μs
Turn-off delay time, inductive load	$I_C = 450\text{ A}$, $V_{CE} = 400\text{ V}$ $V_{GE} = -8\text{ V} / +15\text{ V}$ $R_{Goff} = 5.1\text{ }\Omega$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	t_{doff}	0.94 1.05 1.05		μs
Fall time, inductive load	$I_C = 450\text{ A}$, $V_{CE} = 400\text{ V}$ $V_{GE} = -8\text{ V} / +15\text{ V}$ $R_{Goff} = 5.1\text{ }\Omega$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	t_f	0.04 0.05 0.06		μs
Turn-on energy loss per pulse	$I_C = 450\text{ A}$, $V_{CE} = 400\text{ V}$, $L_S = 20\text{ nH}$ $V_{GE} = -8\text{ V} / +15\text{ V}$ $R_{Gon} = 2.4\text{ }\Omega$ di/dt ($T_{vj} 25^{\circ}\text{C}$) = 5500 A/ μs di/dt ($T_{vj} 150^{\circ}\text{C}$) = 5000 A/ μs	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	E_{on}	13.5 17.5 18.0		mJ
Turn-off energy loss per pulse	$I_C = 450\text{ A}$, $V_{CE} = 400\text{ V}$, $L_S = 20\text{ nH}$ $V_{GE} = -8\text{ V} / +15\text{ V}$ $R_{Goff} = 5.1\text{ }\Omega$ dv/dt ($T_{vj} 25^{\circ}\text{C}$) = 3100 V/ μs dv/dt ($T_{vj} 150^{\circ}\text{C}$) = 2500 V/ μs	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	E_{off}	23.5 29.0 30.0		mJ
SC data	$V_{GE} \leq 15\text{ V}$, $V_{CC} = 400\text{ V}$ $V_{CEmax} = V_{CES} - L_{SCE} \cdot di/dt$	$t_P \leq 6\text{ }\mu\text{s}$, $T_{vj} = 25^{\circ}\text{C}$ $t_P \leq 3\text{ }\mu\text{s}$, $T_{vj} = 175^{\circ}\text{C}$	I_{SC}	4800 3900		A
Thermal resistance, junction to cooling fluid	per IGBT; $\Delta V/\Delta t = 10\text{ dm}^3/\text{min}$, $T_F = 75^{\circ}\text{C}$		R_{thJF}	0.120 ²⁾	0.140 ²⁾	K/W
Temperature under switching conditions	t_{op} continuous for 10s within a period of 30s, occurrence maximum 3000 times over lifetime		$T_{vj\text{ op}}$	-40 150	150 ³⁾ 175	$^{\circ}\text{C}$

¹⁾ Verified by characterization / design not by test.

²⁾ Cooler design and flow direction according to application note AN-HPD-ASSEMBLY. Cooling fluid 50% water / 50% ethylenglycol.

³⁾ For $T_{vjop} > 150^{\circ}\text{C}$: Baseplate temperature has to be limited to 125°C .

3 Diode, Inverter

3.1 Maximum Rated Values

Parameter	Conditions	Symbol	Value	Unit
Repetitive peak reverse voltage	$T_{vj} = 25^{\circ}\text{C}$	V_{RRM}	750	V
Implemented forward current		I_{FN}	820	A
Continuous DC forward current		I_F	450 ¹⁾	A
Repetitive peak forward current	$t_P = 1 \text{ ms}$	I_{FRM}	1640	A
I^2t - value	$V_R = 0 \text{ V}, t_P = 10 \text{ ms}, T_{vj} = 150^{\circ}\text{C}$ $V_R = 0 \text{ V}, t_P = 10 \text{ ms}, T_{vj} = 175^{\circ}\text{C}$	I^2t	19000 16000	A ² s A ² s

3.2 Characteristic Values

			min.	typ.	max.	
Forward voltage	$I_F = 450 \text{ A}, V_{GE} = 0 \text{ V}$ $I_F = 450 \text{ A}, V_{GE} = 0 \text{ V}$ $I_F = 450 \text{ A}, V_{GE} = 0 \text{ V}$ $I_F = 820 \text{ A}, V_{GE} = 0 \text{ V}$ $I_F = 820 \text{ A}, V_{GE} = 0 \text{ V}$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	V_F	1.45 1.30 1.25 1.70 1.60	1.65	V
Peak reverse recovery current	$I_F = 450 \text{ A}, -di_F/dt = 5000 \text{ A}/\mu\text{s} (T_{vj} = 150^{\circ}\text{C})$ $V_R = 400 \text{ V}$ $V_{GE} = -8 \text{ V}$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	I_{RM}	250 350 370		A
Recovered charge	$I_F = 450 \text{ A}, -di_F/dt = 5000 \text{ A}/\mu\text{s} (T_{vj} = 150^{\circ}\text{C})$ $V_R = 400 \text{ V}$ $V_{GE} = -8 \text{ V}$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	Q_r	20.0 40.0 45.0		μC
Reverse recovery energy	$I_F = 450 \text{ A}, -di_F/dt = 5000 \text{ A}/\mu\text{s} (T_{vj} = 150^{\circ}\text{C})$ $V_R = 400 \text{ V}$ $V_{GE} = -8 \text{ V}$	$T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 150^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	E_{rec}	7.00 13.0 15.0		mJ
Thermal resistance, junction to cooling fluid	per diode; $\Delta V/\Delta t = 10 \text{ dm}^3/\text{min}, T_F = 75^{\circ}\text{C}$	R_{thJF}		0.175 ²⁾	0.200 ²⁾	K/W
Temperature under switching conditions	t_{op} continuous for 10s within a period of 30s, occurrence maximum 3000 times over lifetime	$T_{vj op}$	-40 150		150 ³⁾ 175	$^{\circ}\text{C}$

4 NTC-Thermistor

			min.	typ.	max.	
Parameter	Conditions	Symbol		Value		Unit
Rated resistance	$T_C = 25^{\circ}\text{C}$	R_{25}		5.00		k Ω
Deviation of R100	$T_C = 100^{\circ}\text{C}, R_{100} = 493 \Omega$	$\Delta R/R$	-5		5	%
Power dissipation	$T_C = 25^{\circ}\text{C}$	P_{25}			20.0	mW
B-value	$R_2 = R_{25} \exp [B_{25/50}(1/T_2 - 1/(298,15 \text{ K}))]$	$B_{25/50}$		3375		K
B-value	$R_2 = R_{25} \exp [B_{25/80}(1/T_2 - 1/(298,15 \text{ K}))]$	$B_{25/80}$		3411		K
B-value	$R_2 = R_{25} \exp [B_{25/100}(1/T_2 - 1/(298,15 \text{ K}))]$	$B_{25/100}$		3433		K

Specification according to the valid application note.

¹⁾ Verified by characterization / design not by test.

²⁾ Cooler design and flow direction according to application note AN-HPD-ASSEMBLY. Cooling fluid 50% water / 50% ethylenglycol.

³⁾ For $T_{vjop} > 150^{\circ}\text{C}$: Baseplate temperature has to be limited to 125°C .

5 Module

Parameter	Conditions	Symbol	Value	Unit
Isolation test voltage	RMS, $f = 0 \text{ Hz}$, $t = 1 \text{ sec}$	V_{ISOL}	4.2	kV
Maximum RMS module terminal current	$T_F = 75^\circ\text{C}$, $T_{\text{Ct}} = 105^\circ\text{C}$	I_{RMS}	500 ¹⁾	A
Material of module baseplate			Cu+Ni ²⁾	
Internal isolation	basic insulation (class 1, IEC 61140)		Al ₂ O ₃ ³⁾	
Creepage distance	terminal to heatsink terminal to terminal	d_{Creep}	9.0 9.0	mm
Clearance	terminal to heatsink terminal to terminal	d_{Clear}	4.5 4.5	mm
Comperative tracking index		CTI	> 200	
			min. typ. max.	
Pressure drop in cooling circuit	$\Delta V/\Delta t = 10.0 \text{ dm}^3/\text{min}$; $T_F = 75^\circ\text{C}$	Δp		64 ⁴⁾ mbar
Maximum pressure in cooling circuit	$T_{\text{baseplate}} < 40^\circ\text{C}$ $T_{\text{baseplate}} > 40^\circ\text{C}$ (relative pressure)	p		2.5 2.0 bar
Stray inductance module		L_{sCE}	8.0	nH
Module lead resistance, terminals - chip	$T_F = 25^\circ\text{C}$, per switch	$R_{\text{CC}'+\text{EE}'}$	0.75	mΩ
Storage temperature		T_{stg}	-40	125 °C
Mounting torque for modul mounting	Screw M4 baseplate to heatsink Screw EJOT Delta PCB to frame	M	1.80 0.45	2.00 0.50 2.20 0.55 ⁵⁾ Nm
Weight		G	730	g

¹⁾ Continous, steady state. Verified by characterization / design not by test.

²⁾ Ni plated Cu baseplate.

³⁾ Improved Al₂O₃ ceramic.

⁴⁾ Cooler design and flow direction according to application note AN-HPD-ASSEMBLY. Cooling fluid 50% water / 50% ethylenglycol.

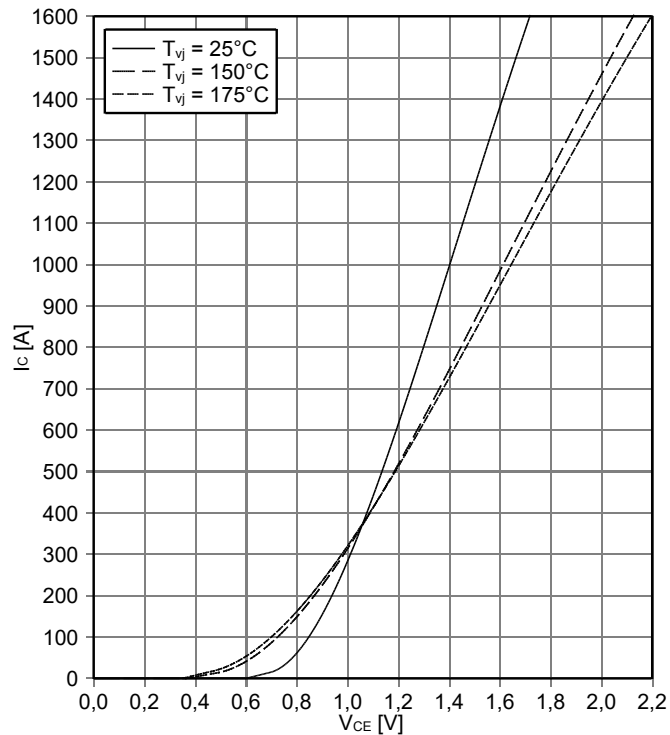
⁵⁾ EJOT Delta PT WN 5451 30x10. Effective mounting torque according to application note AN-HPD-ASSEMBLY

6 Characteristics Diagrams

output characteristic IGBT, Inverter (typical)

$$I_C = f(V_{CE})$$

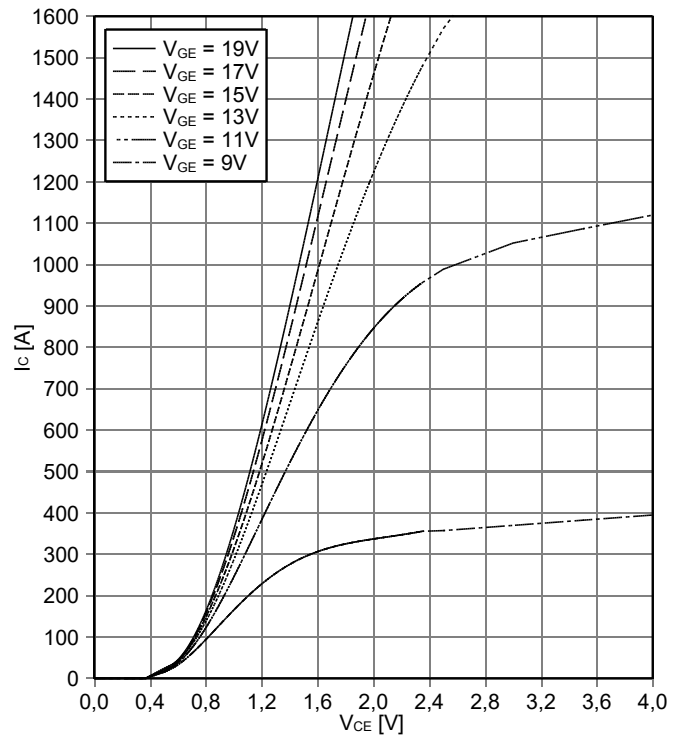
$$V_{GE} = 15 \text{ V}$$



output characteristic IGBT, Inverter (typical)

$$I_C = f(V_{CE})$$

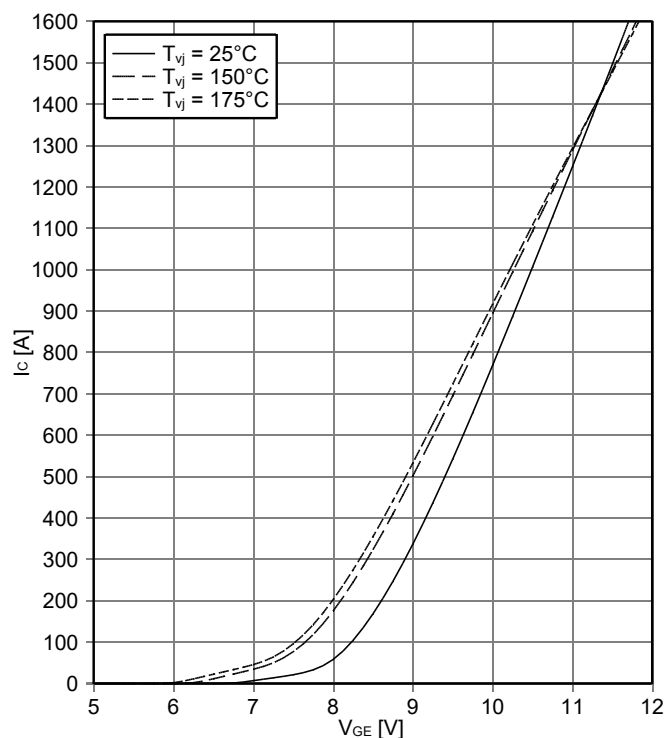
$$T_{vj} = 150^\circ\text{C}$$



transfer characteristic IGBT, Inverter (typical)

$$I_C = f(V_{GE})$$

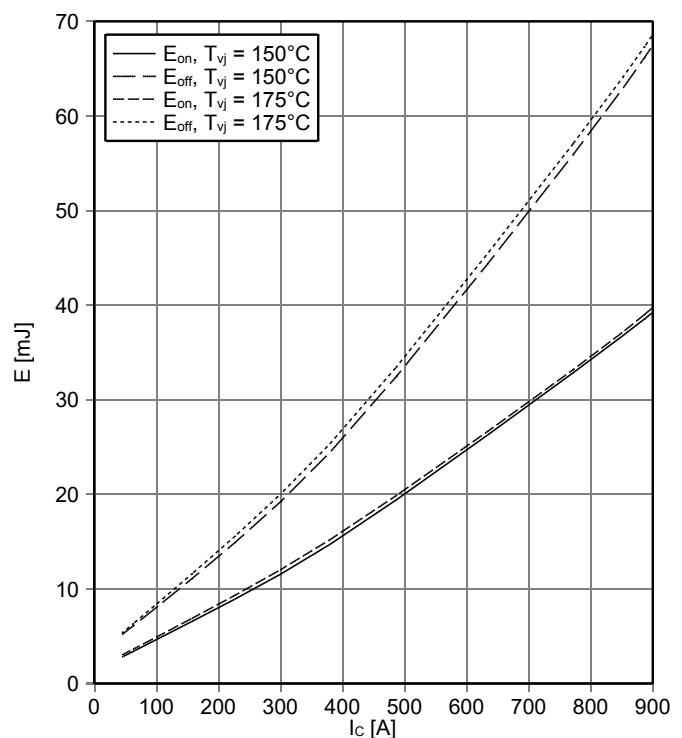
$$V_{CE} = 20 \text{ V}$$



switching losses IGBT, Inverter (typical)

$$E_{on} = f(I_C), E_{off} = f(I_C)$$

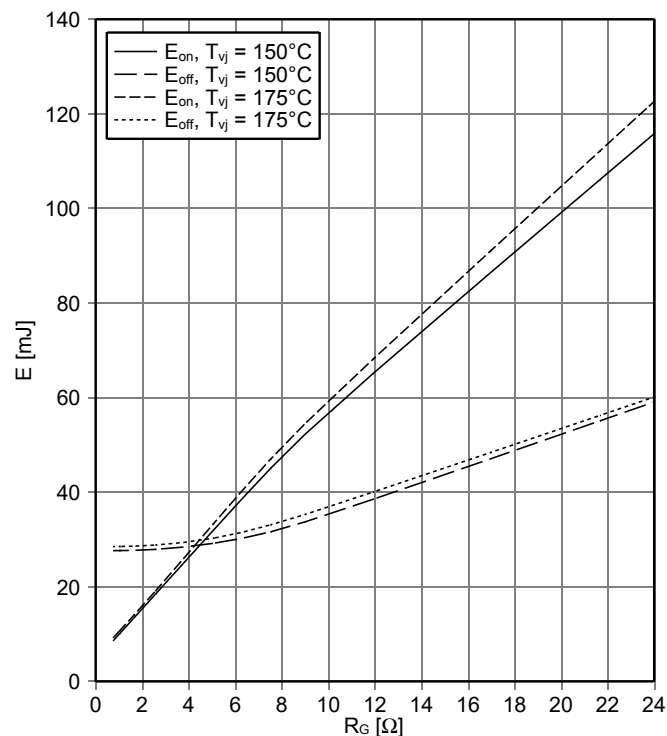
$$V_{GE} = +15 \text{ V} / -8 \text{ V}, R_{Gon} = 2.4 \Omega, R_{Goff} = 5.1 \Omega, V_{CE} = 400 \text{ V}$$



switching losses IGBT, Inverter (typical)

$$E_{on} = f(R_G), E_{off} = f(R_G)$$

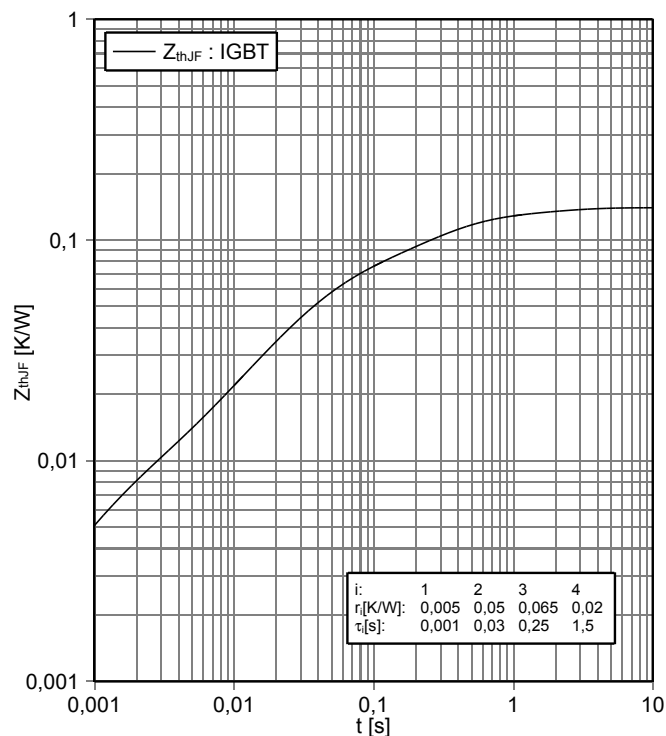
$$V_{GE} = +15V / -8V, I_C = 450 A, V_{CE} = 400 V$$



transient thermal impedance IGBT, Inverter

$$Z_{thJF} = f(t), \text{ cooler design according to AN-HPD-ASSEMBLY}$$

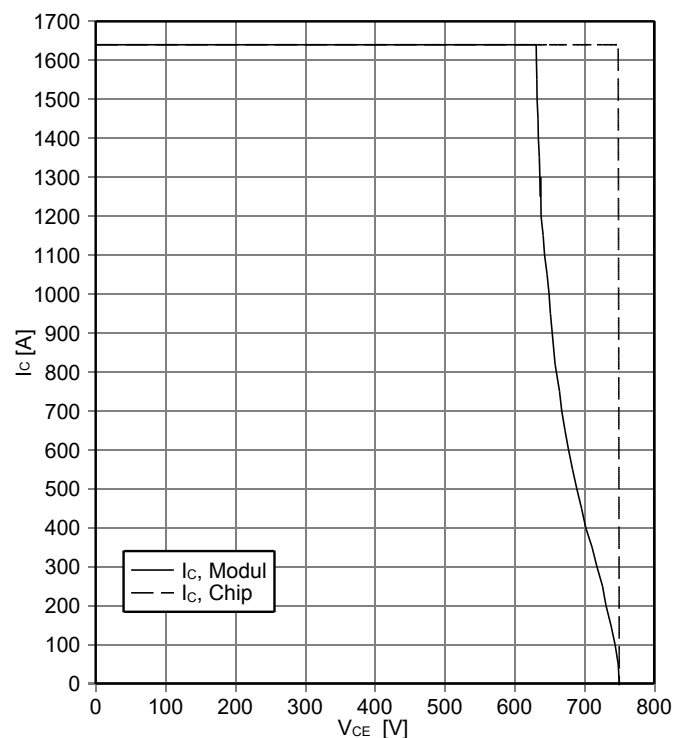
$$\Delta V/\Delta t = 10 \text{ dm}^3/\text{min}; T_f = 75^{\circ}C; 50\% \text{ water} / 50\% \text{ ethylenglycol}$$



reverse bias safe operating area IGBT, Inverter (RBSOA)

$$I_C = f(V_{CE})$$

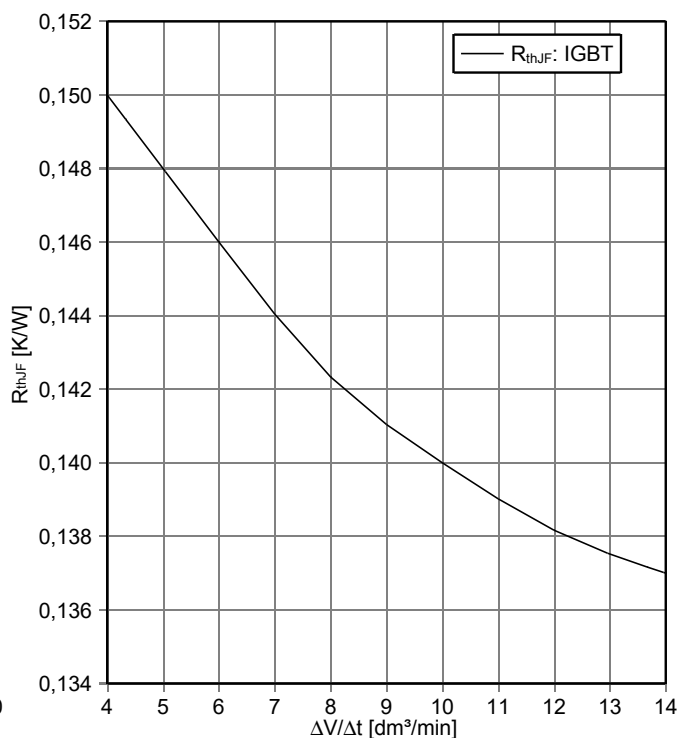
$$V_{GE} = +15V / -8V, R_{Goff} = 5,1 \Omega, T_{vj} = 175^{\circ}C$$



thermal impedance IGBT, Inverter

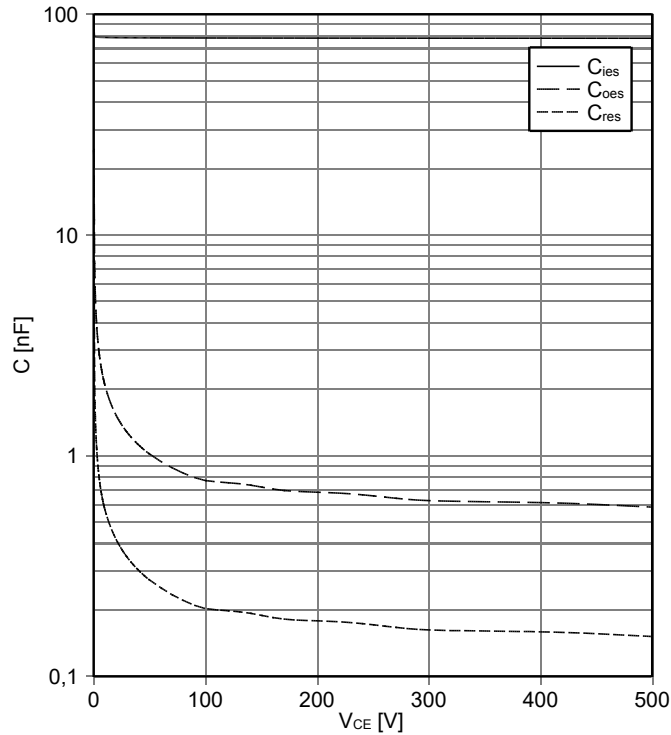
$$R_{thJF} = f(\Delta V/\Delta t), \text{ cooler design according to AN-HPD-Assembly}$$

$$T_f = 75^{\circ}C; 50\% \text{ water} / 50\% \text{ ethylenglycol}$$



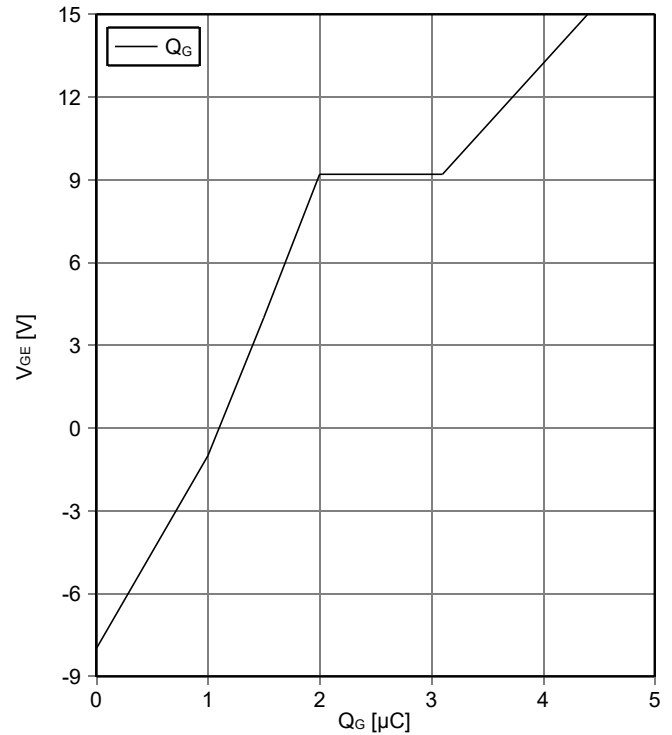
capacity characteristic IGBT, Inverter (typical)

$C = f(V_{CE})$
 $V_{GE} = 0 \text{ V}$, $T_{vj} = 25^\circ\text{C}$, $f = 1 \text{ MHz}$



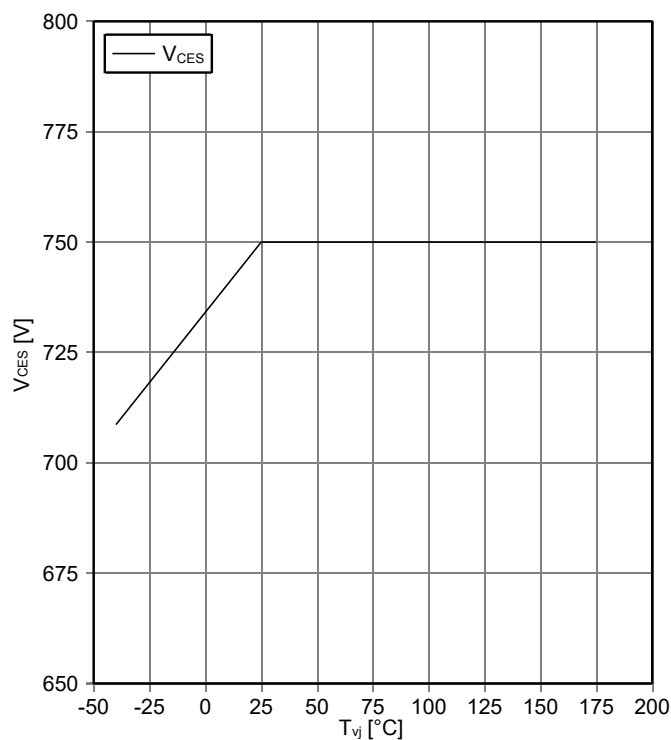
gate charge characteristic IGBT, Inverter (typical)

$V_{GE} = f(Q_G)$
 $V_{CE} = 400 \text{ V}$, $I_C = 450 \text{ A}$, $T_{vj} = 25^\circ\text{C}$



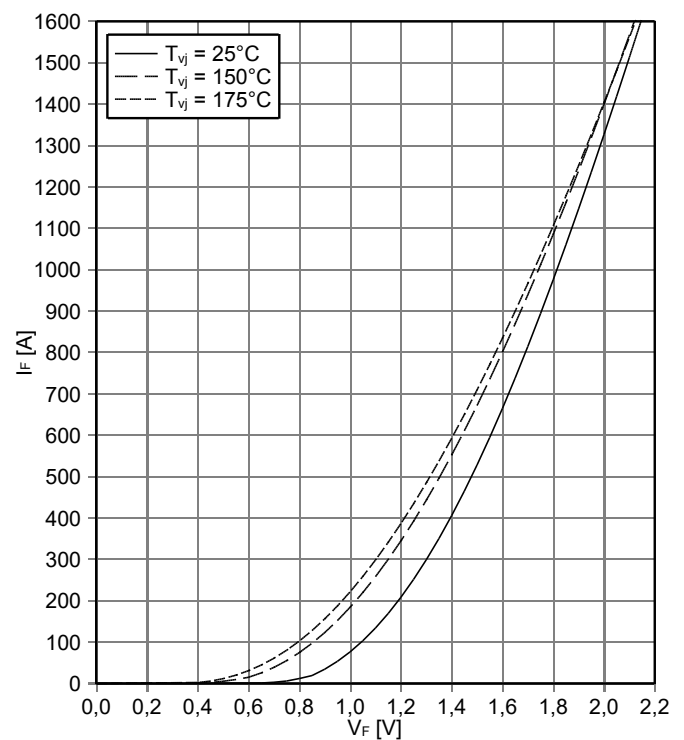
maximum allowed collector-emitter voltage

$V_{CES} = f(T_{vj})$, verified by characterization / design not by test
 $I_{CES} = 1 \text{ mA}$ for $T_{vj} \leq 25^\circ\text{C}$; $I_{CES} = 30 \text{ mA}$ for $T_{vj} > 25^\circ\text{C}$



forward characteristic of Diode, Inverter (typical)

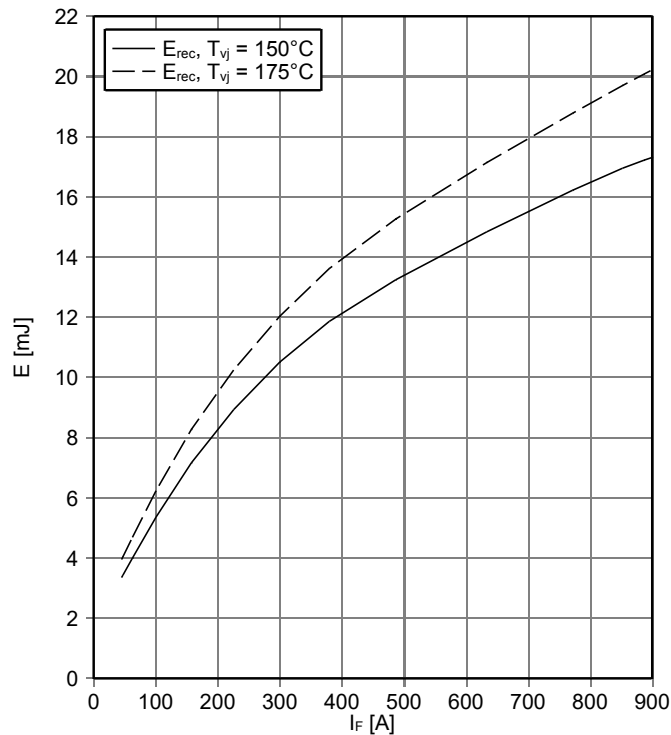
$I_F = f(V_F)$



switching losses Diode, Inverter (typical)

$$E_{\text{rec}} = f(I_F),$$

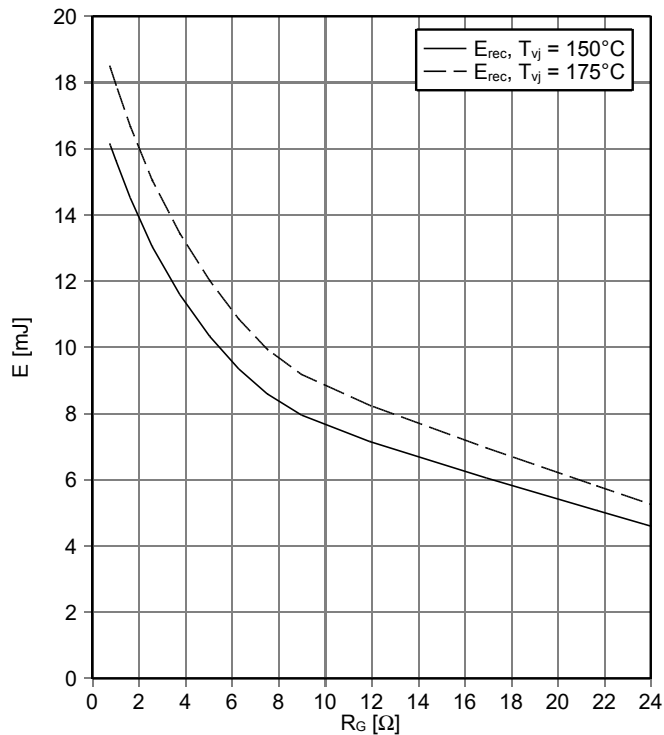
$$R_{\text{Gon}} = 2.4 \, \Omega, V_{\text{CE}} = 400 \, \text{V}$$



switching losses Diode, Inverter (typical)

$$E_{\text{rec}} = f(R_G),$$

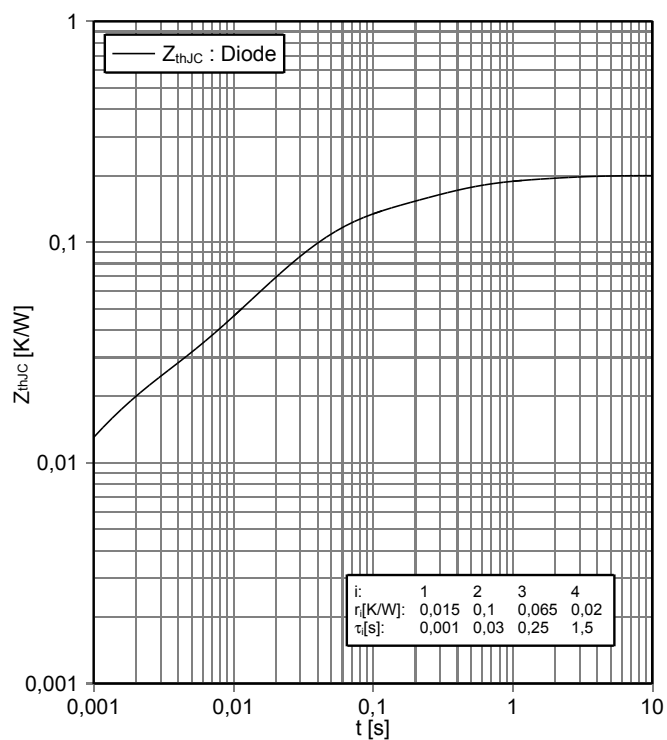
$$I_F = 450 \, \text{A}, V_{\text{CE}} = 400 \, \text{V}$$



transient thermal impedance Diode, Inverter

$$Z_{\text{thJF}} = f(t), \text{ cooler design according to AN-HPD-ASSEMBLY}$$

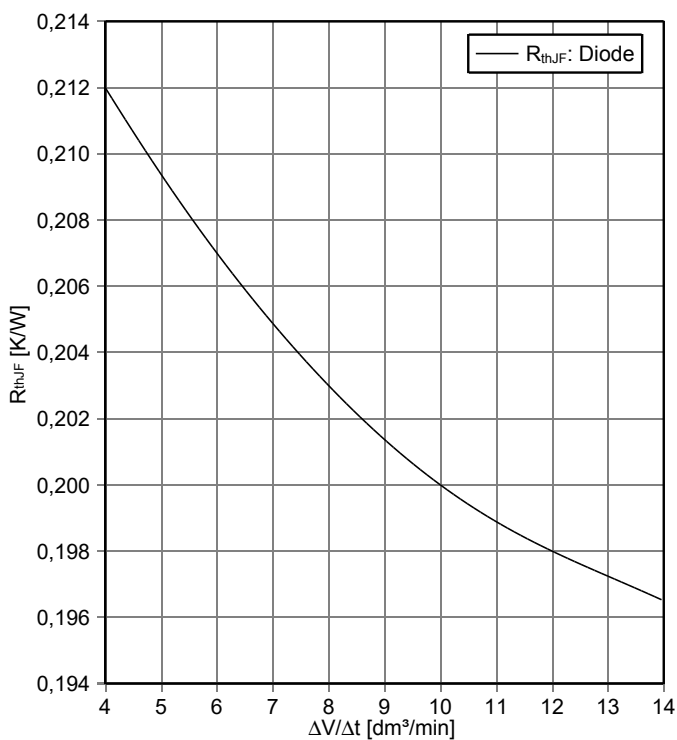
$$\Delta V/\Delta t = 10 \, \text{dm}^3/\text{min}; T_f = 75^\circ\text{C}; 50\% \text{ water} / 50\% \text{ ethylenglycol}$$



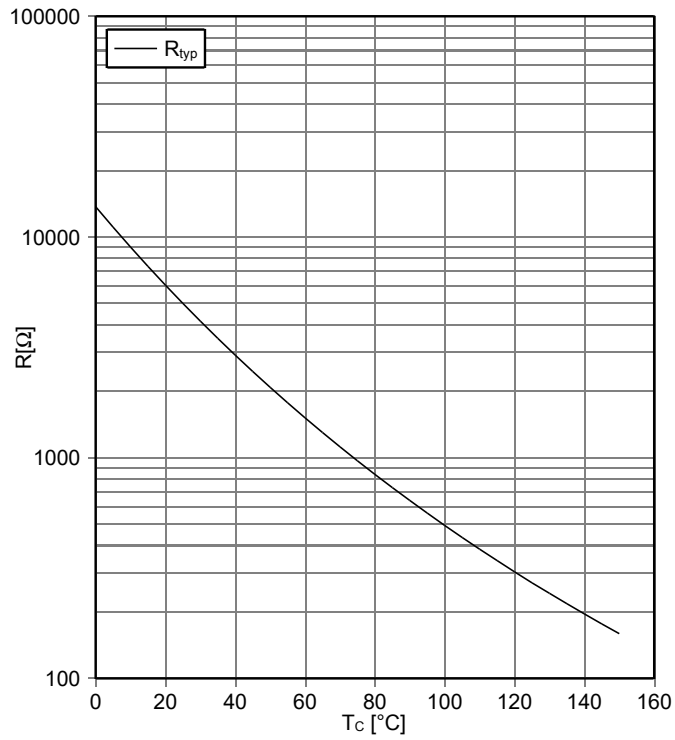
thermal impedance Diode, Inverter

$$R_{\text{thJF}} = f(\Delta V/\Delta t), \text{ cooler design according to AN-HPD-ASSEMBLY}$$

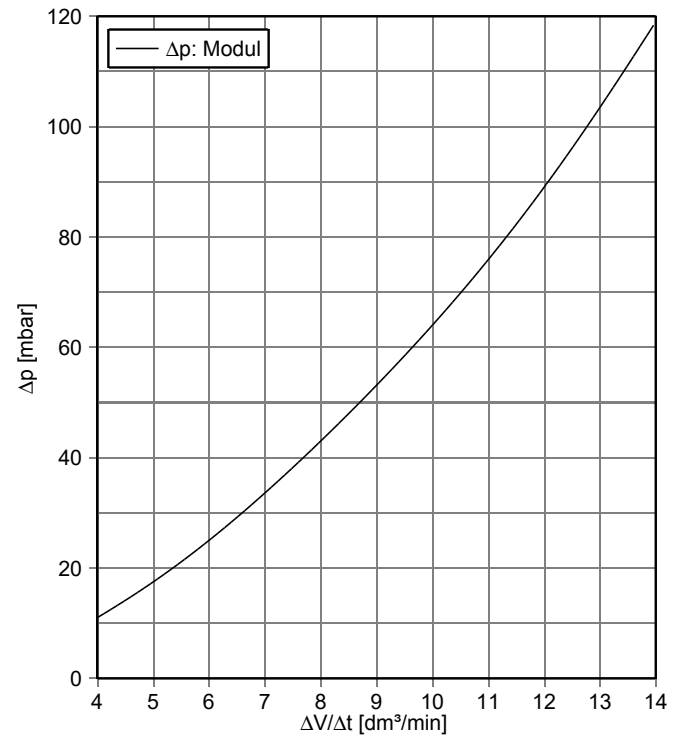
$$T_f = 75^\circ\text{C}; 50\% \text{ water} / 50\% \text{ ethylenglycol}$$



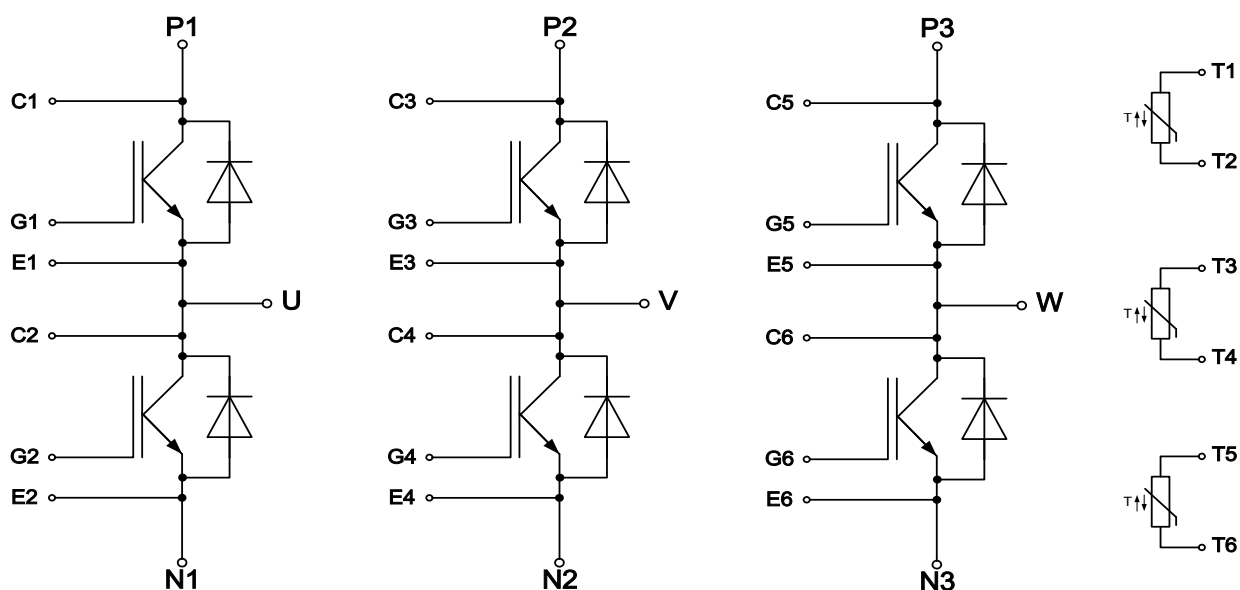
NTC-Thermistor-temperature characteristic (typical)
 $R = f(T)$



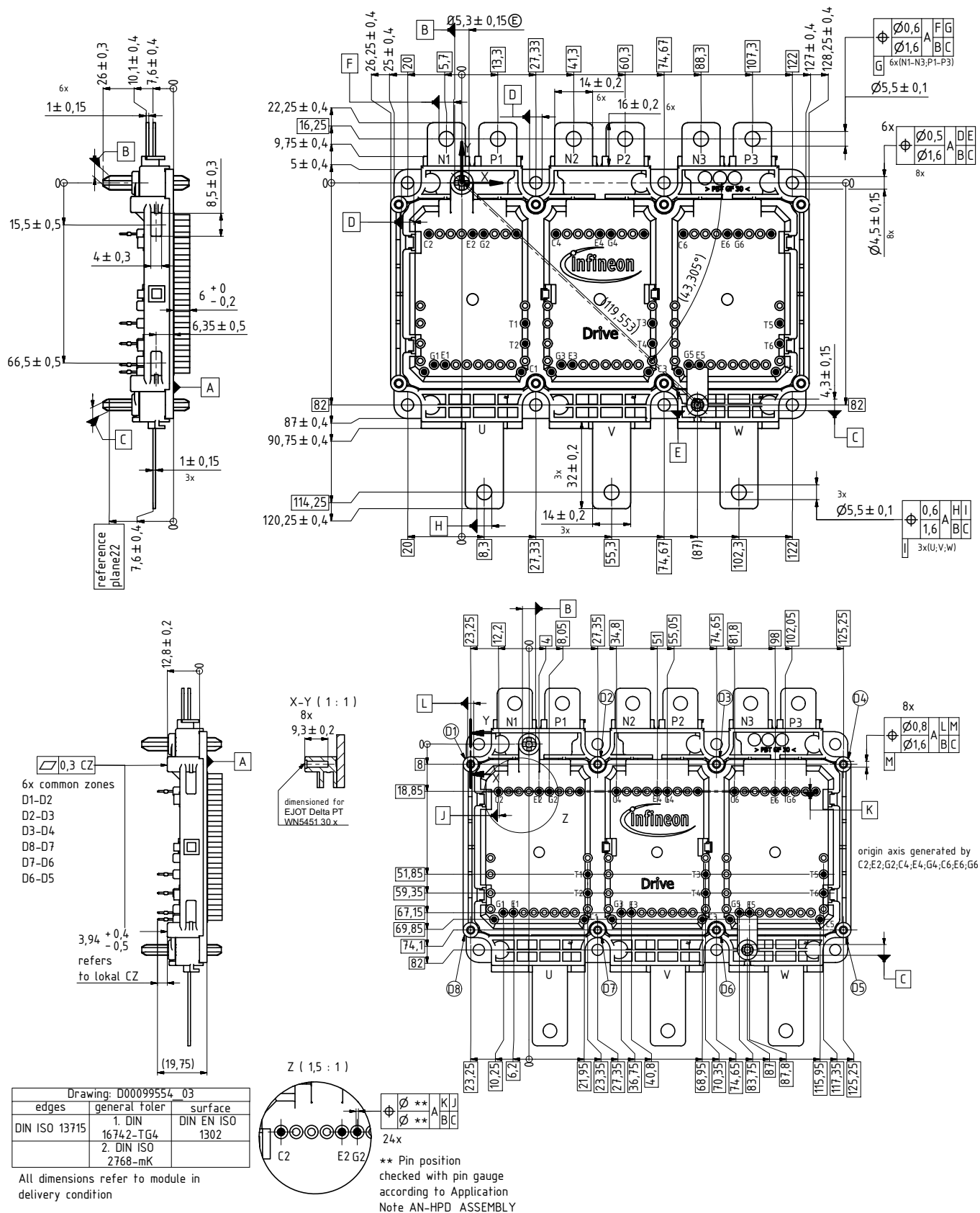
pressure drop in cooling circuit
 $\Delta p = f(\Delta V/\Delta t)$, cooler design according to AN-HPD-ASSEMBLY
 $T_f = 75^\circ\text{C}$; 50% water / 50% ethylenglycol



7 Circuit diagram



8 Package outlines



9 Label Codes

9.1 Module Code

Code Format	Data Matrix		
Encoding	ASCII Text		
Symbol Size	16x16		
Standard	IEC24720 and IEC16022		
Code Content	Content Module Serial Number Module Material Number Production Order Number Datecode (Production Year) Datecode (Production Week)	Digit 1 - 5 6 - 11 12 - 19 20 - 21 22 - 23	Example (below) 71549 142846 55054991 15 30
Example	 71549142846550549911530		

9.2 Packing Code

Code Format	Code128			
Encoding	Code Set A			
Symbol Size	34 digits			
Standard	IEC8859-1			
Code Content	Content Backend Construction Number Production Lot Number Serial Number Date Code Box Quantity	Identifier X 1T S 9D Q	Digit 2 - 9 12 - 19 21 - 25 28 - 31 33 - 34	Example (below) 95056609 2X0003E0 754389 1139 15
Example	 X950566091T2X0003E0S754389D1139Q15			

Revision History

Major changes since previous revision

Revision History

Reference	Date	Description
V3.0	2017-03-23	Final datasheet
V3.1	2019-06-18	Adjustment of package outlines
V3.2	2019-06-26	Adjustment of package outlines
V3.3	2019-07-10	Adjustment of package outlines; Correction of typing errors.
V3.4	2019-10-09	Adjustment of package outlines. Correction of typing errors.

Terms & Conditions of usage

Edition 2018-08-01

Published by
Infineon Technologies AG
81726 Munich, Germany
© 2018 Infineon Technologies AG
All Rights Reserved.

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (<http://www.infineon.com>)

Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.

These components are not designed for "special applications" that demand extremely high reliability or safety such as aerospace, defense or life support devices or systems (Class III medical devices). If you intend to use the components in any of these special applications, please contact your local representative at International Rectifier HiRel Products, Inc. or the Infineon support (<https://www.infineon.com/support>) to review product requirements and reliability testing.

Infineon Technologies components may be used in special applications only with the express written approval of Infineon Technologies. Class III medical devices are intended to be implanted in the human body or to support and/or maintain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.

Trademarks

Trademarks of Infineon Technologies AG

AURIX™, C166™, CanPAK™, CIPOS™, CIPURSE™, EconoPACK™, CoolMOS™, CoolSET™, CORECONTROL™, CROSSAVE™, DAVE™, DI-POL™, EasyPIM™, EconoBRIDGE™, EconoDUAL™, EconoPIM™, EconoPACK™, EiceDRIVER™, eupec™, FCOS™, HITFET™, HybridPACK™, I²RF™, ISOFACE™, IsoPACK™, MIPAQ™, ModSTACK™, my-d™, NovalithIC™, OptiMOS™, ORIGA™, POWERCODE™, PRIMARION™, PrimePACK™, PrimeSTACK™, PRO-SIL™, PROFET™, RASIC™, ReverSave™, SatRIC™, SIEGET™, SINDRION™, SIPMOS™, SmartLEWIS™, SOLID FLASH™, TEMPFET™, thinQ!™, TRENCHSTOP™, TriCore™.

Other Trademarks

Advance Design System™ (ADS) of Agilent Technologies, AMBA™, ARM™, MULTI-ICE™, KEIL™, PRIMECELL™, REALVIEW™, THUMB™, μVision™ of ARM Limited, UK. AUTOSAR™ is licensed by AUTOSAR development partnership. Bluetooth™ of Bluetooth SIG Inc. CAT-iq™ of DECT Forum. COLOSSUS™, FirstGPS™ of Trimble Navigation Ltd. EMV™ of EMVCo, LLC (Visa Holdings Inc.). EPCOS™ of Epcos AG. FLEXGO™ of Microsoft Corporation. FlexRay™ is licensed by FlexRay Consortium. HYPERTERMINAL™ of Hilgraeve Incorporated. IEC™ of Commission Electrotechnique Internationale. IrDA™ of Infrared Data Association Corporation. ISO™ of INTERNATIONAL ORGANIZATION FOR STANDARDIZATION. MATLAB™ of MathWorks, Inc. MAXIM™ of Maxim Integrated Products, Inc. MICROTEC™, NUCLEUS™ of Mentor Graphics Corporation. MIPI™ of MIPI Alliance, Inc. MIPS™ of MIPS Technologies, Inc., USA. muRata™ of MURATA MANUFACTURING CO., MICROWAVE OFFICE™ (MWO) of Applied Wave Research Inc., OmniVision™ of OmniVision Technologies, Inc. Openwave™ Openwave Systems Inc. RED HAT™ Red Hat, Inc. RFMD™ RF Micro Devices, Inc. SIRIUS™ of Sirius Satellite Radio Inc. SOLARIS™ of Sun Microsystems, Inc. SPANSION™ of Spansion LLC Ltd. Symbian™ of Symbian Software Limited. TAIYO YUDEN™ of Taiyo Yuden Co. TEAKLITE™ of CEVA, Inc. TEKTRONIX™ of Tektronix Inc. TOKO™ of TOKO KABUSHIKI KAISHA TA. UNIX™ of X/Open Company Limited. VERILOG™, PALLADIUM™ of Cadence Design Systems, Inc. VLYNQ™ of Texas Instruments Incorporated. VXWORKS™, WIND RIVER™ of WIND RIVER SYSTEMS, INC. ZETEX™ of Diodes Zetex Limited.

Last update

2011-11-11

www.infineon.com